

# Lowering Diameter Enables Cost-Effective and High-Performance Networks

**MACIEJ BESTA, ERIK HENRIKSSON, TORSTEN HOEFLER**







50% [1]

[1] D. Abts et al. (2010), *Energy Proportional Datacenter Networks*, ISCA'10



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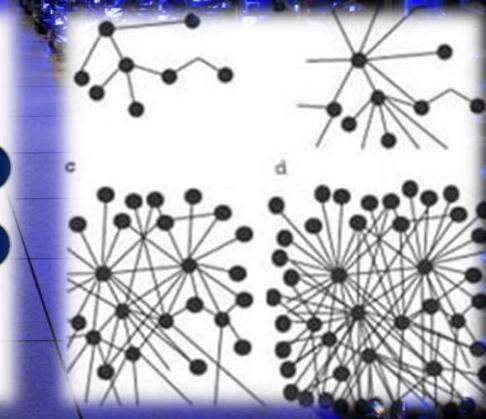
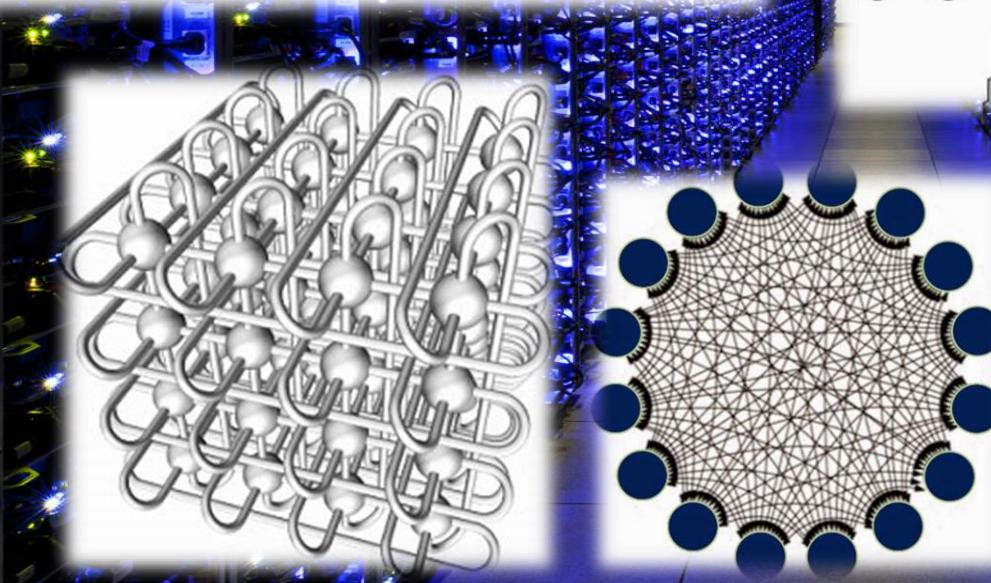
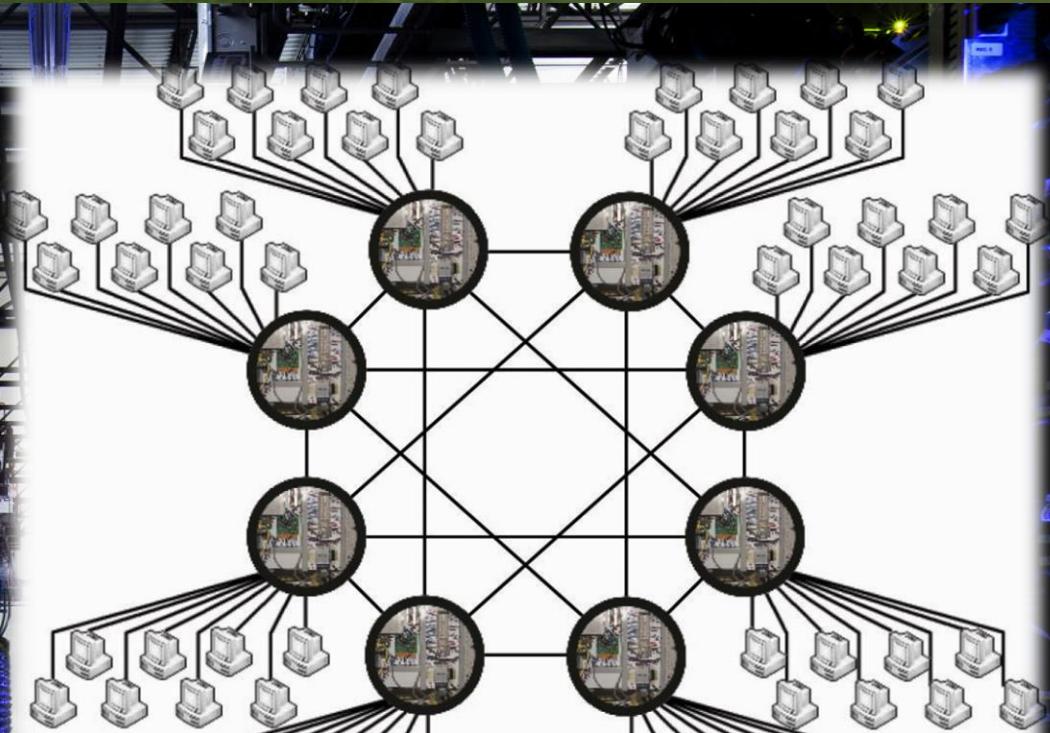
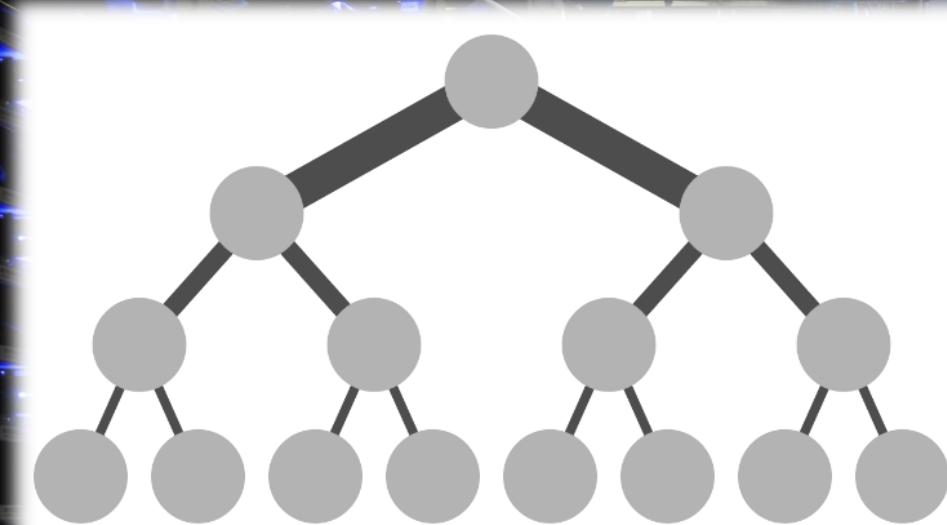


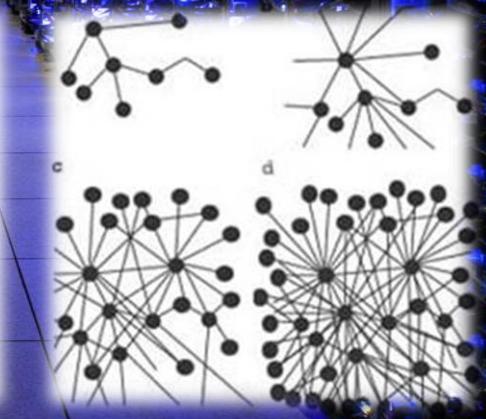
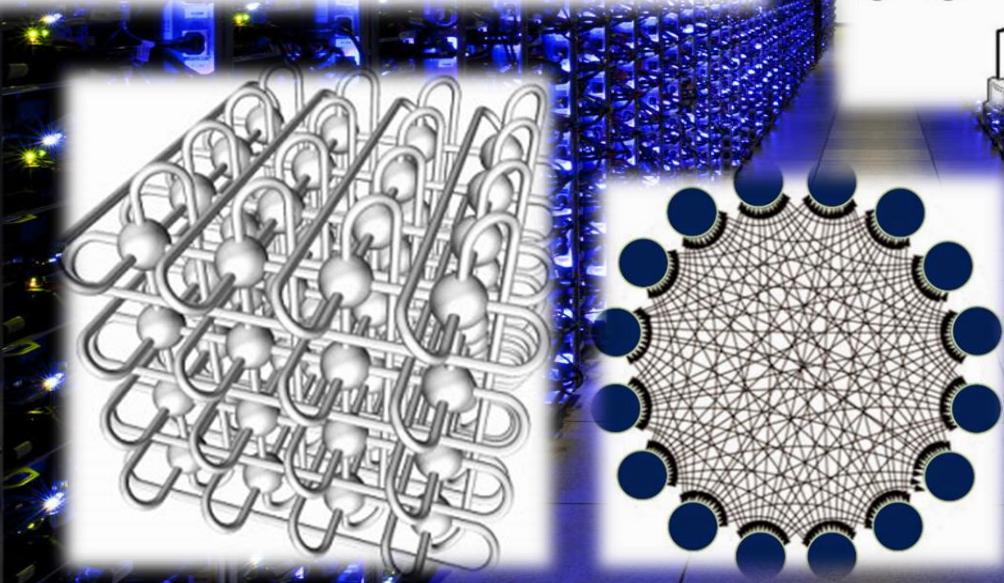
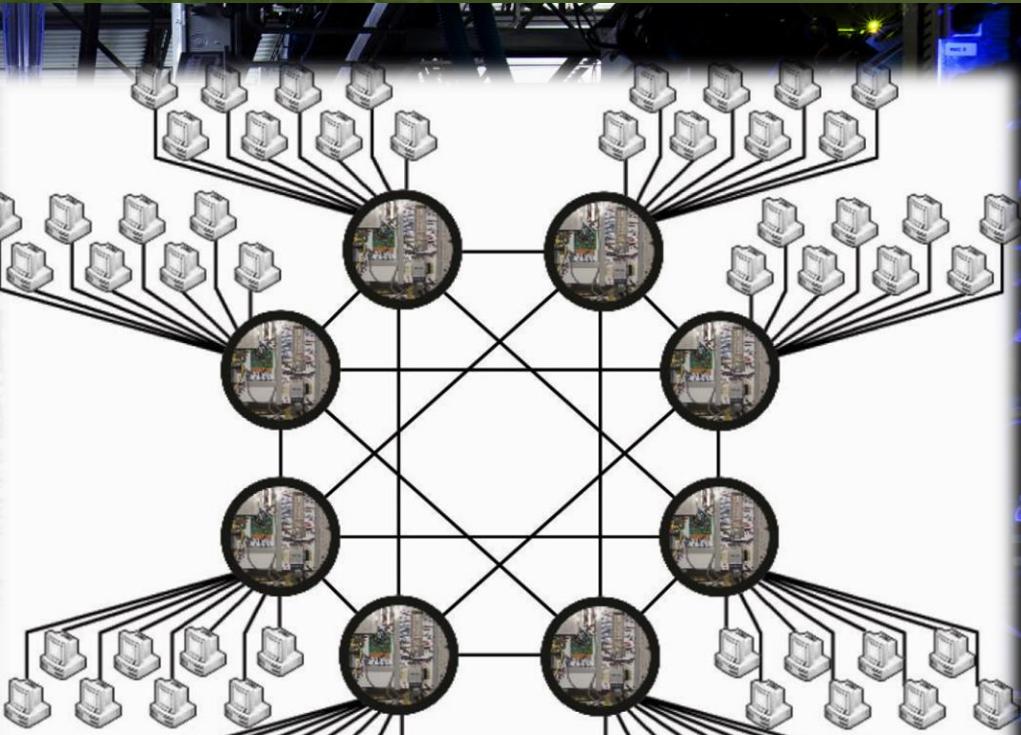
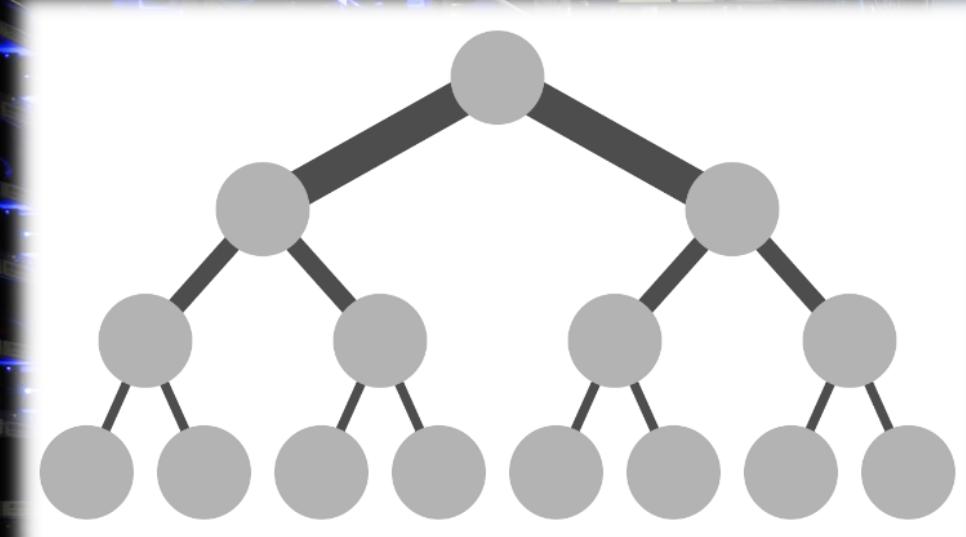
33% [2]

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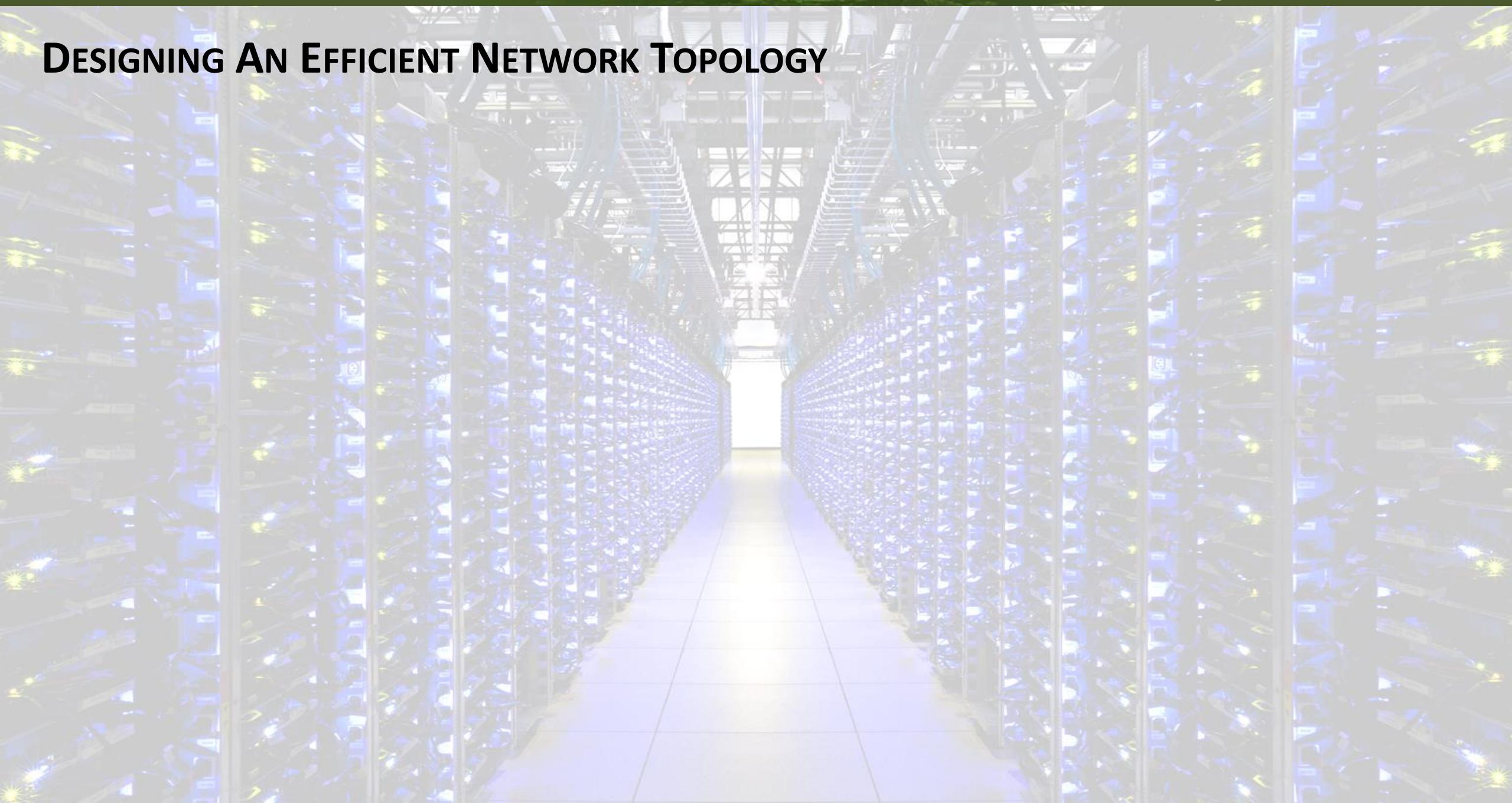
[2] J. Kim et al. (2007), *Flattened Butterfly: A Cost-Efficient Topology for High-Radix Networks*, ISCA'07







# DESIGNING AN EFFICIENT NETWORK TOPOLOGY



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Key idea

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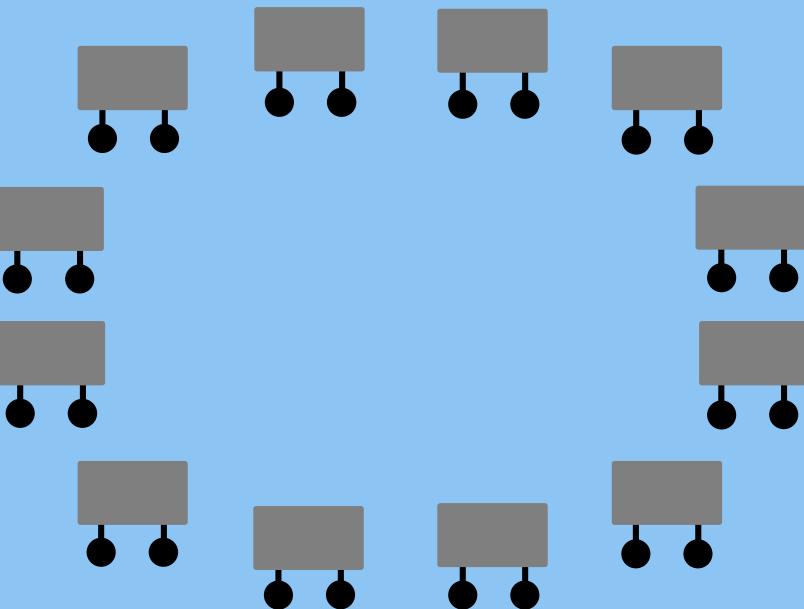
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fewer cables  
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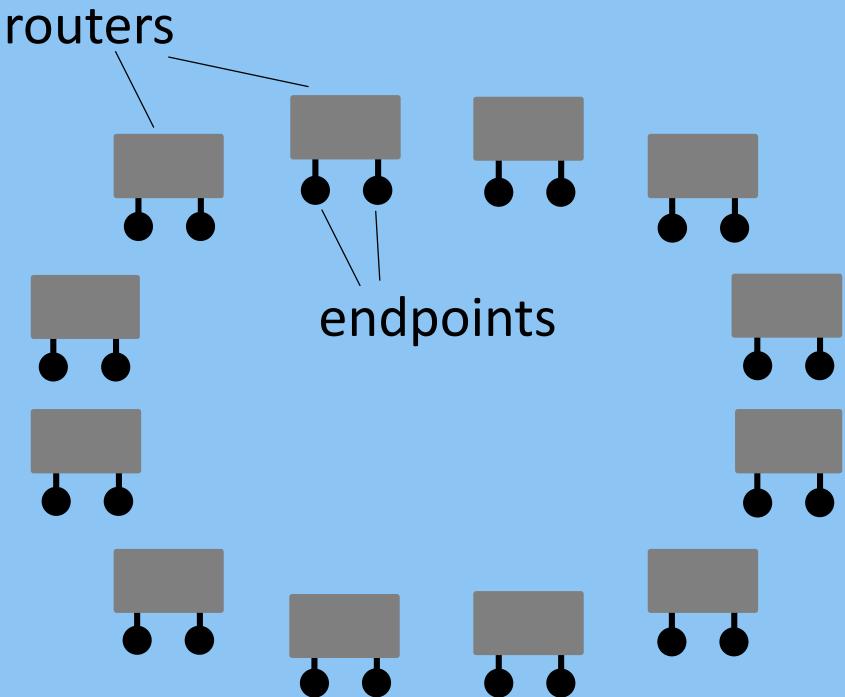


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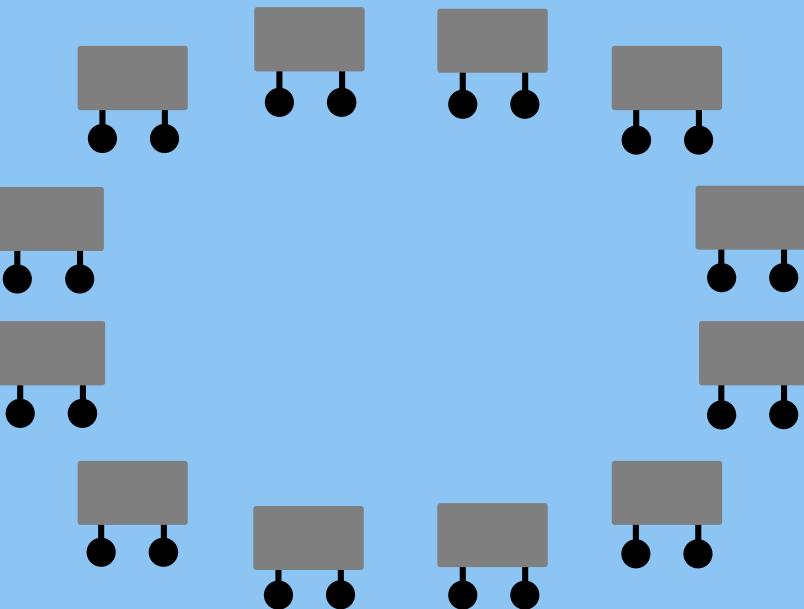


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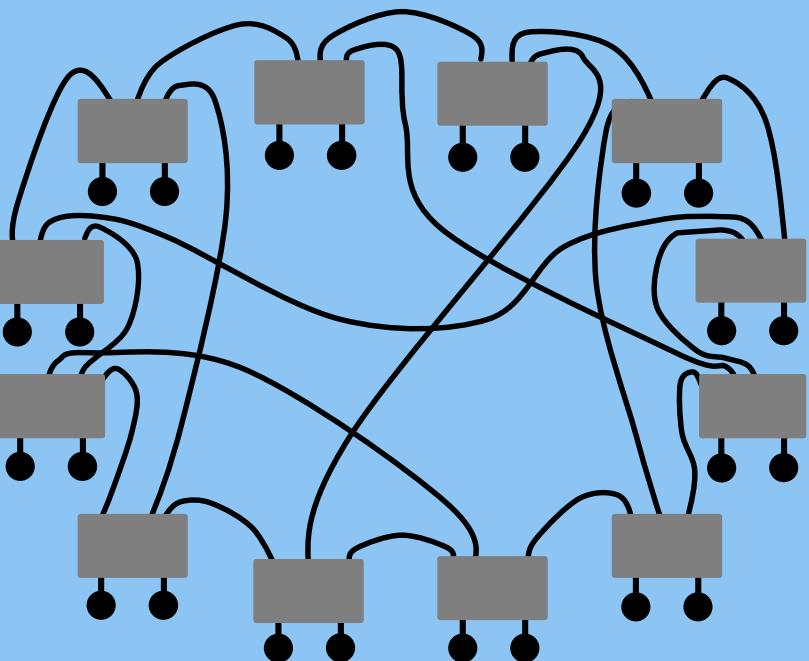


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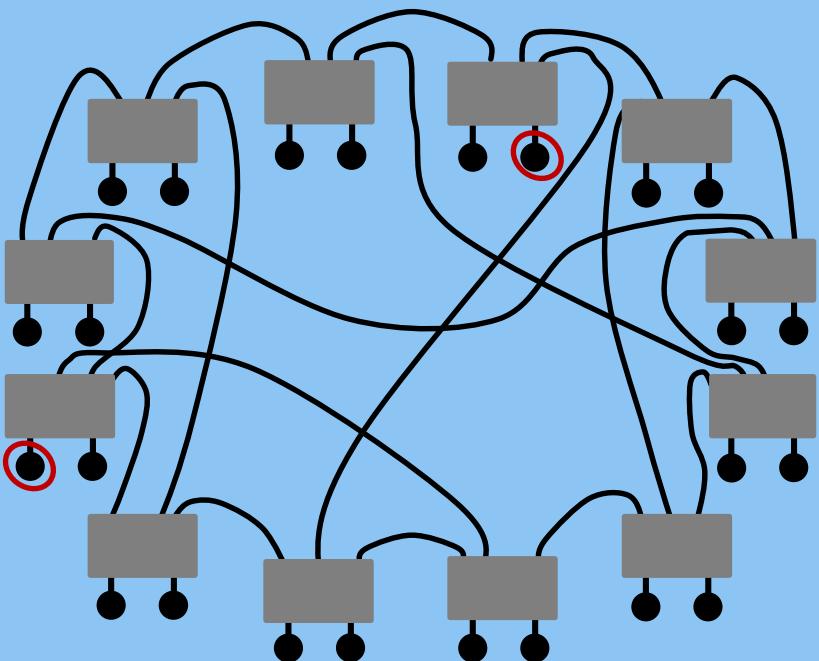


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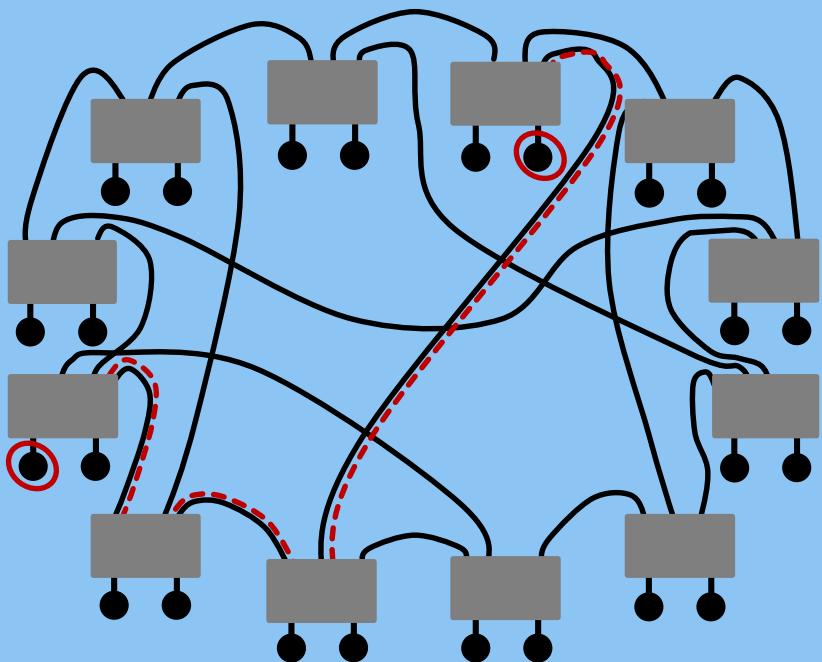


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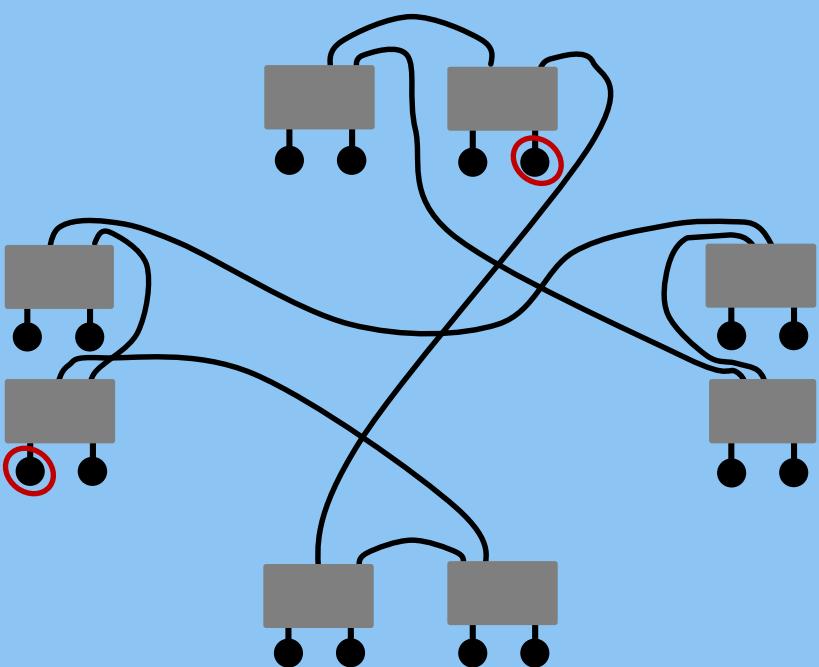


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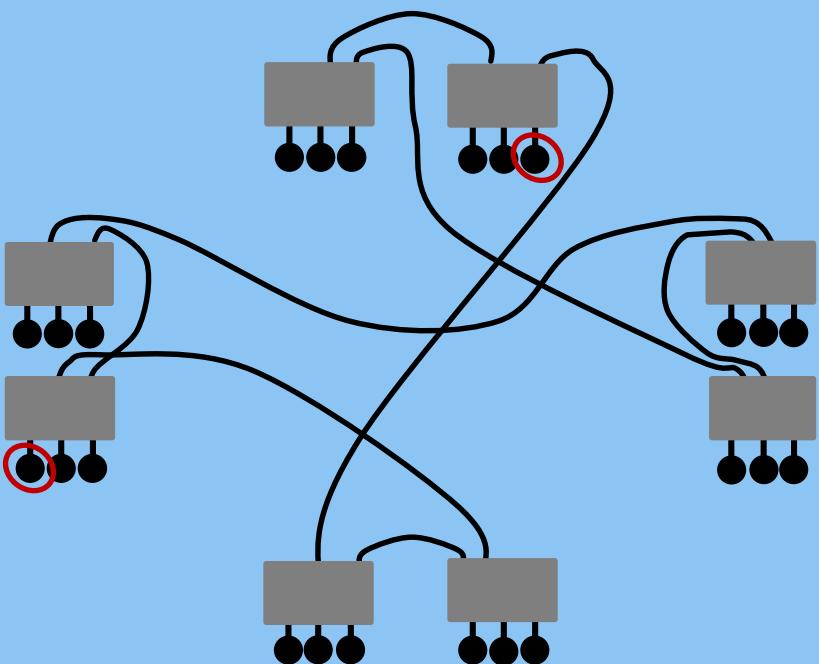


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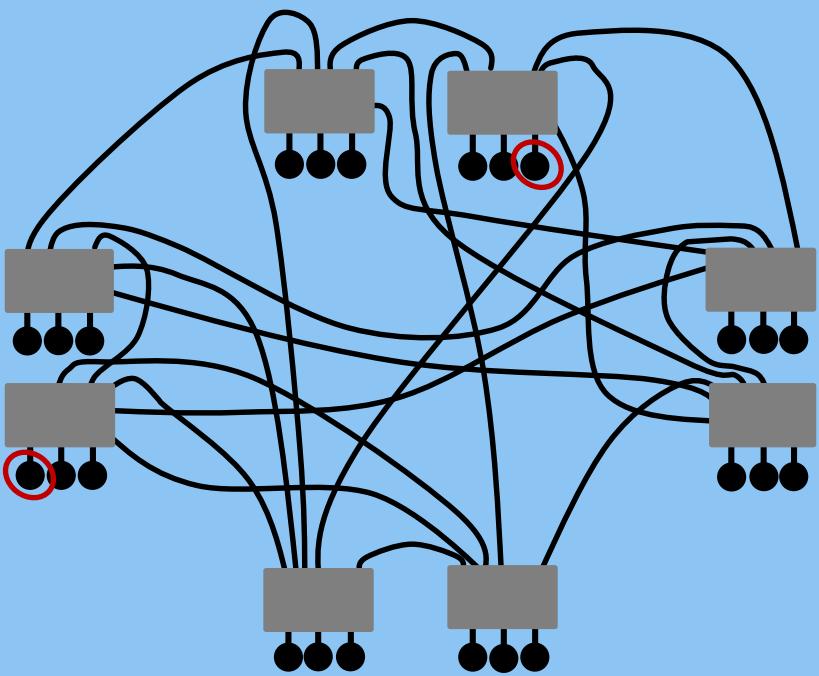


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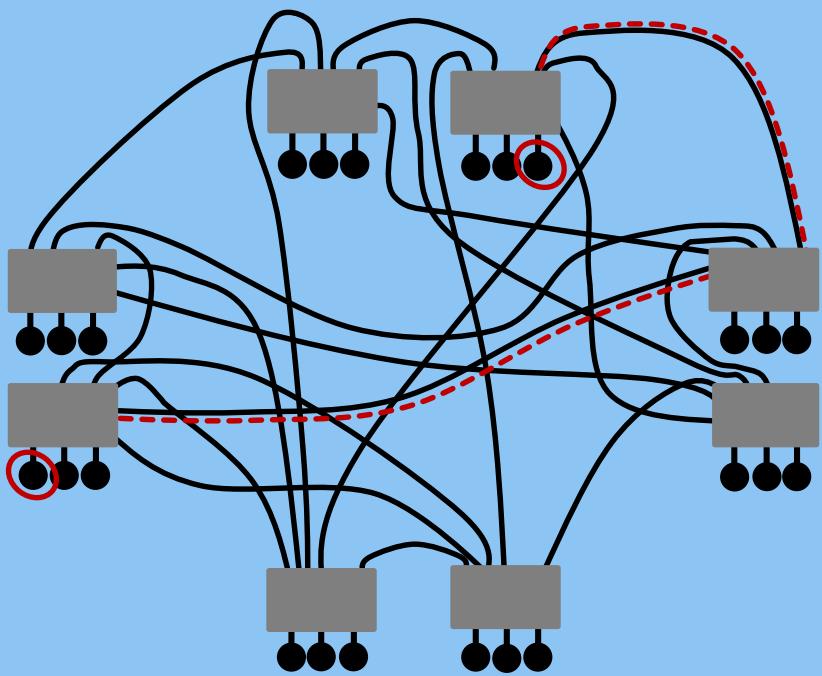


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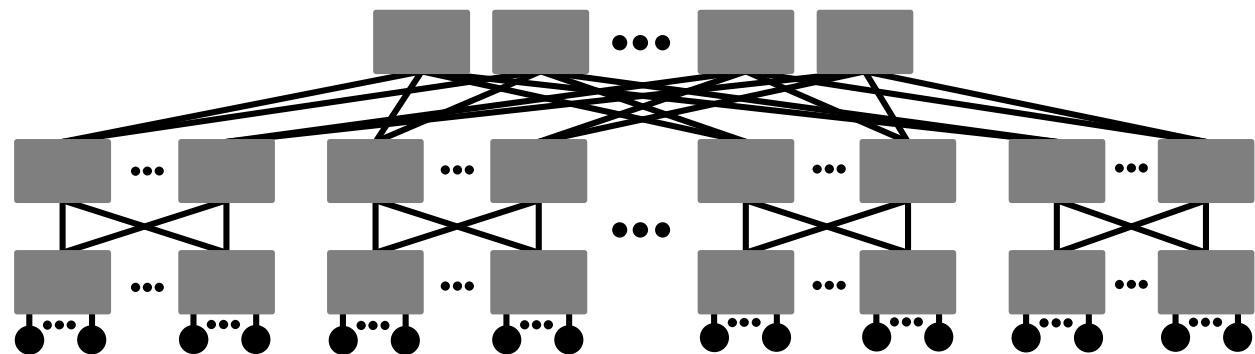
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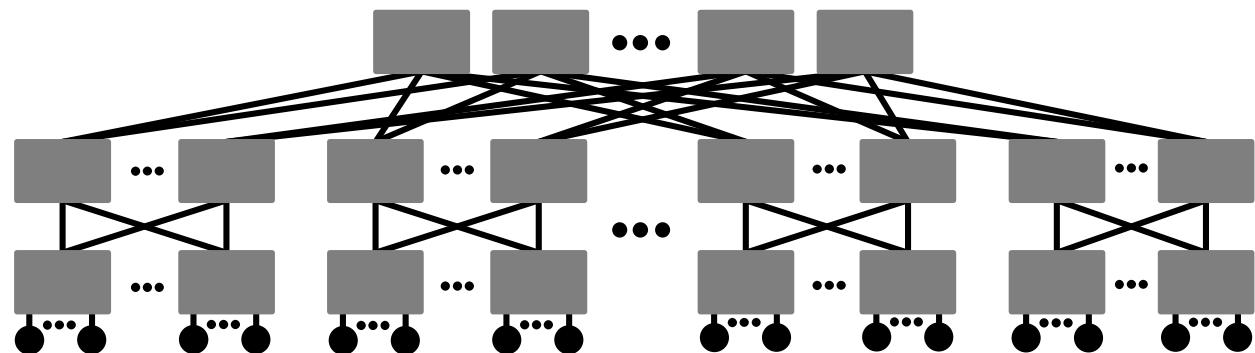
# DESIGNING AN EFFICIENT NETWORK TOPOLOGY

3-level fat tree [1]:



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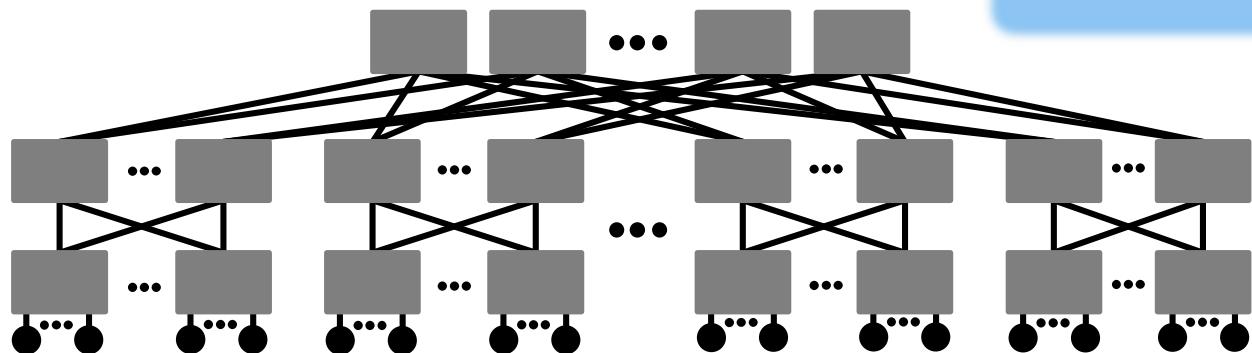
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TSUBAME2.0

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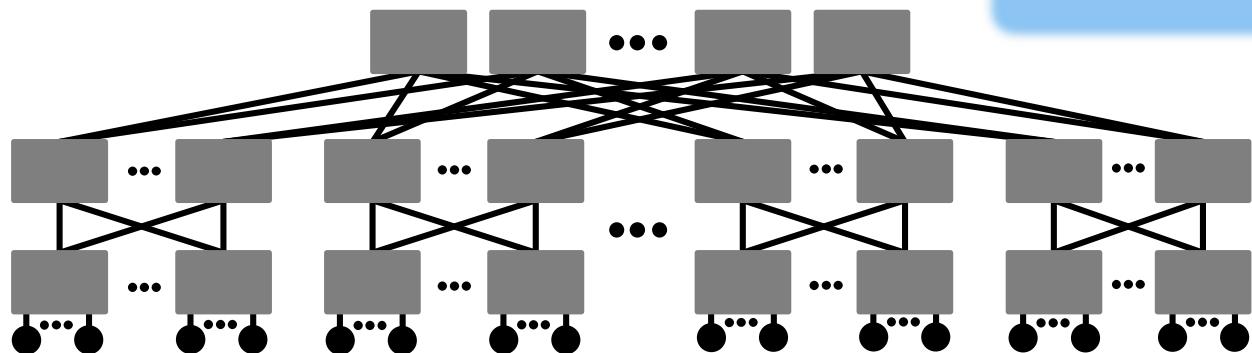
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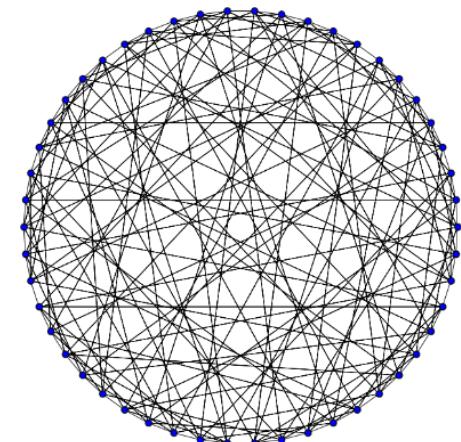
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Slim Fly [2] based on  
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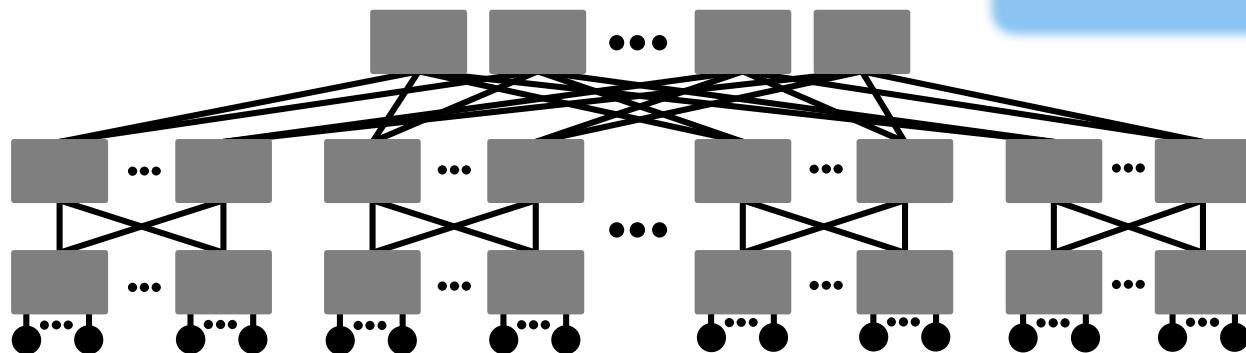
[1] C. E. Leiserson. Fat-trees: Universal Networks for Hardware-Efficient Supercomputing. IEEE Transactions on Computers. 1985.

[2] M. Besta and T. Hoefler. Slim Fly: A Cost Effective Low-Diameter Network Topology. SC14.

[3] Hoffman, Alan J.; Singleton, Robert R. (1960), *Moore graphs with diameter 2 and 3*, IBM Journal of Research and Development

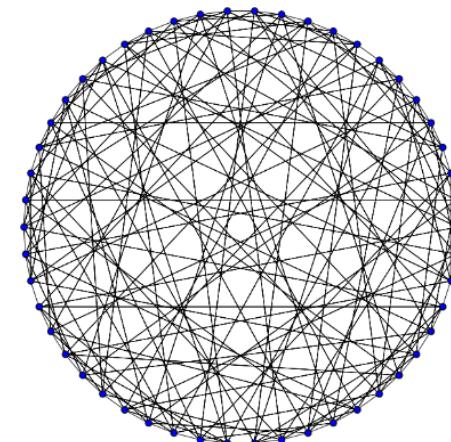
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3-level fat tree [1]: diameter = 4



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Slim Fly [2] based on the Hoffman-Singleton Graph [3]:



diameter = 2  
> ~50% fewer routers  
> ~30% fewer cables

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$$MB(D, k) = 1$$

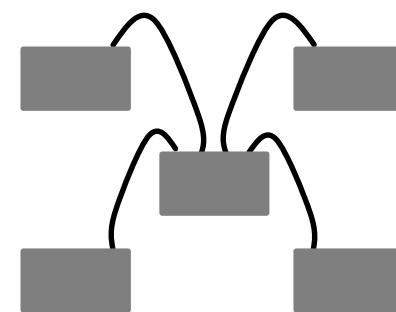


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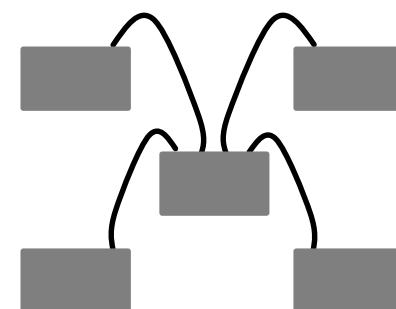


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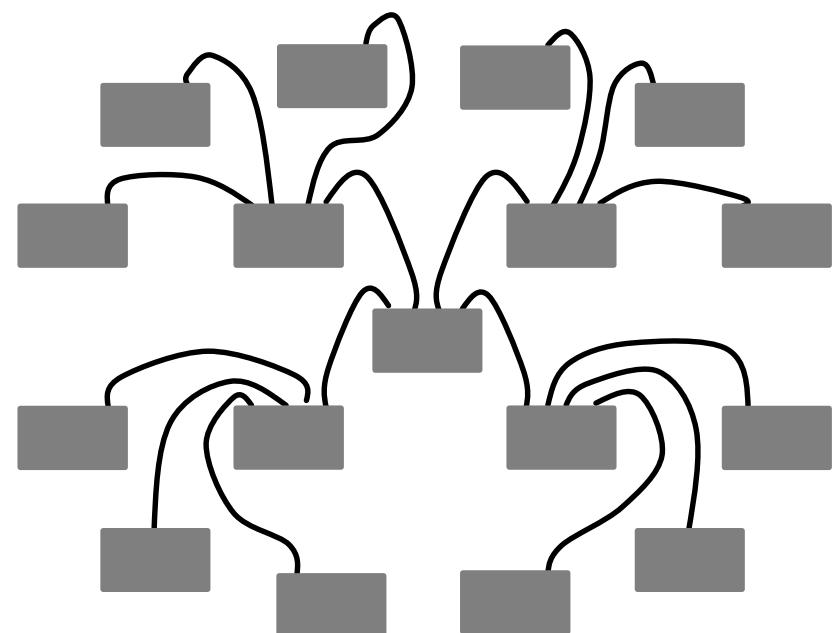


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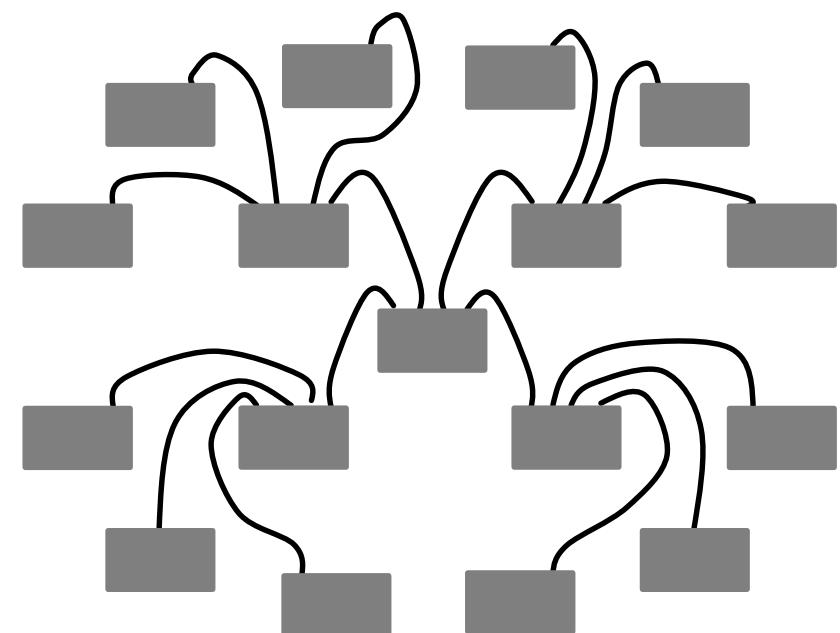


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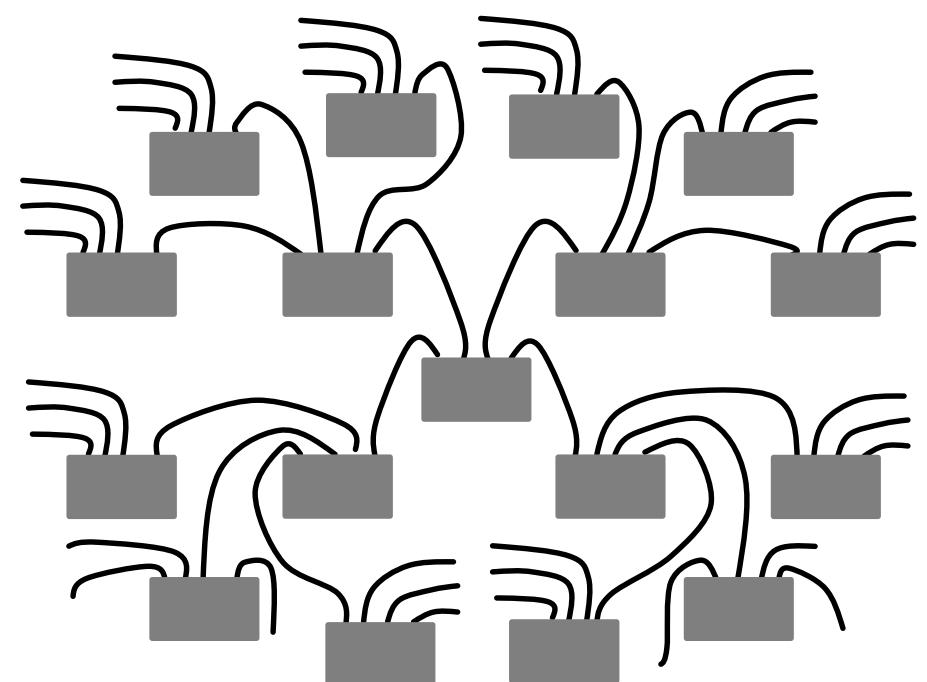


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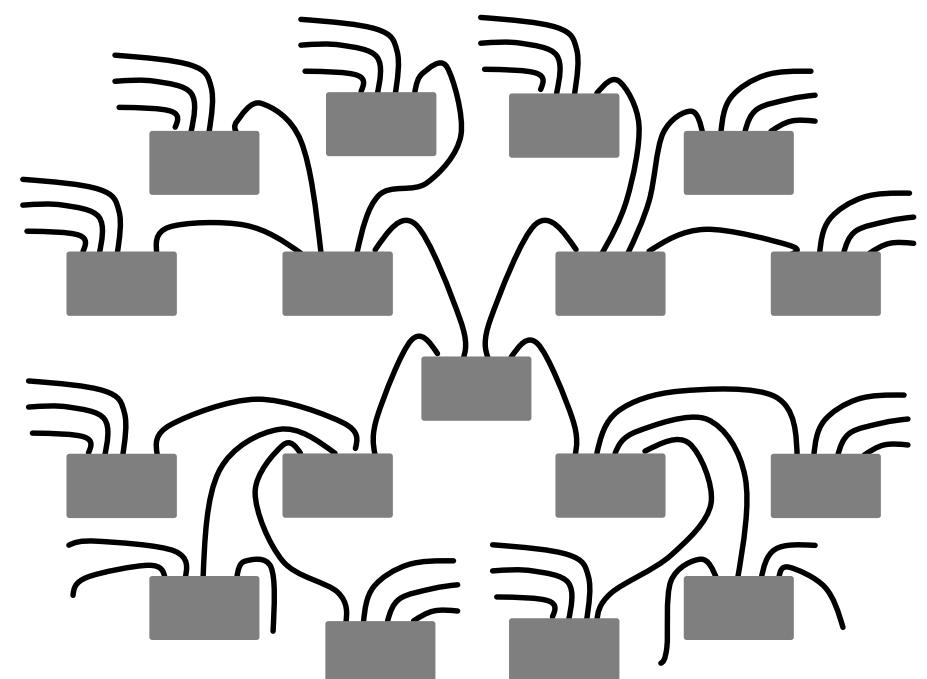


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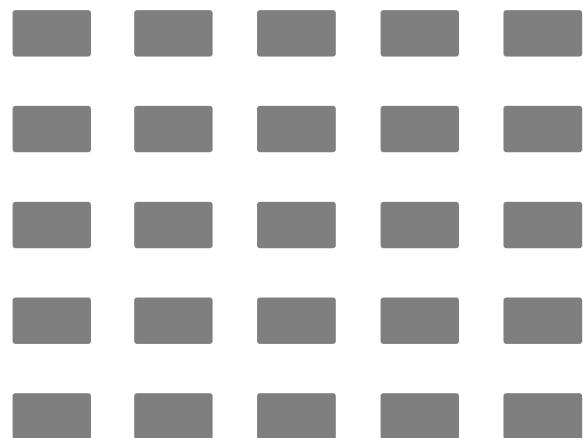
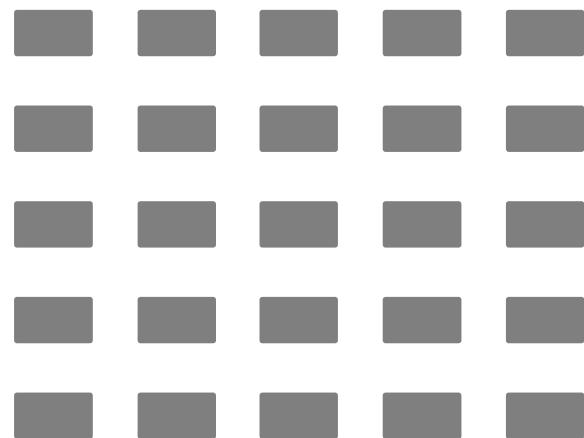
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- Example design for *diameter* = 2

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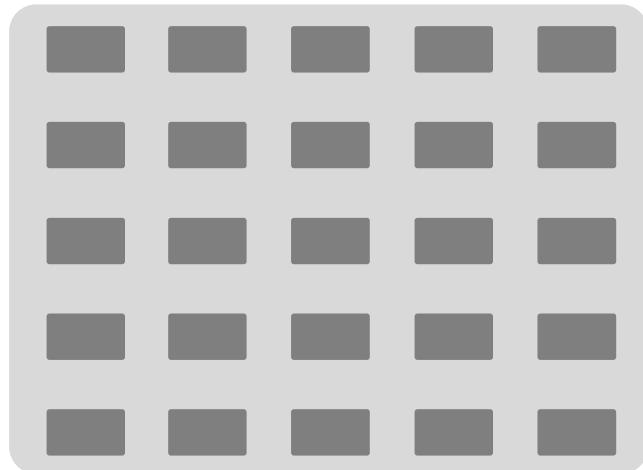
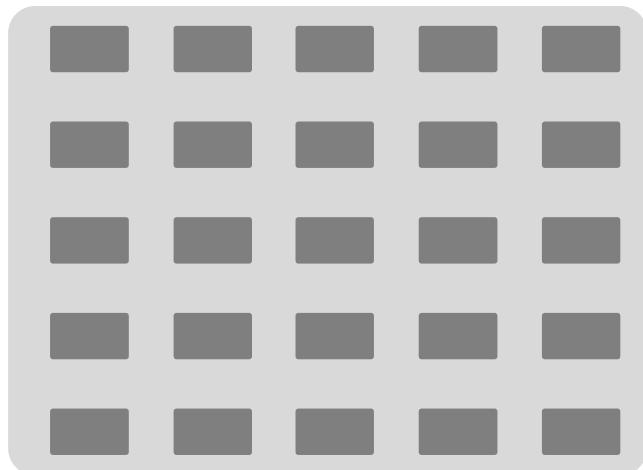


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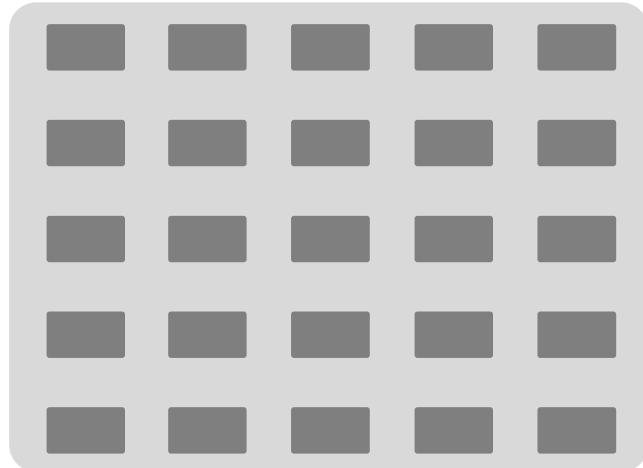
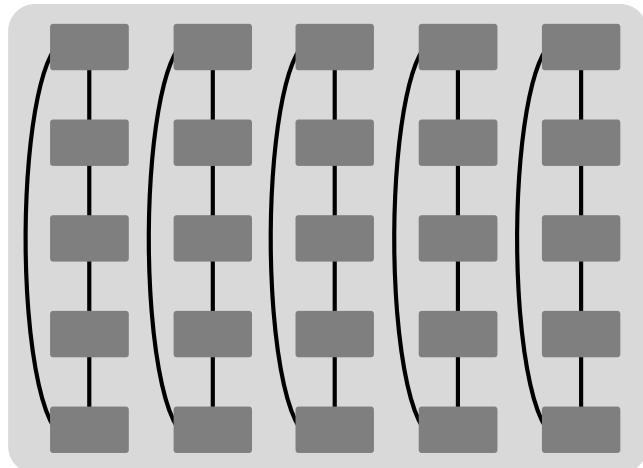
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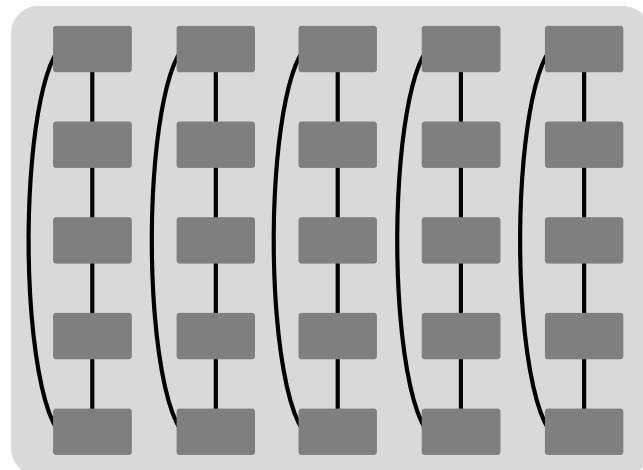


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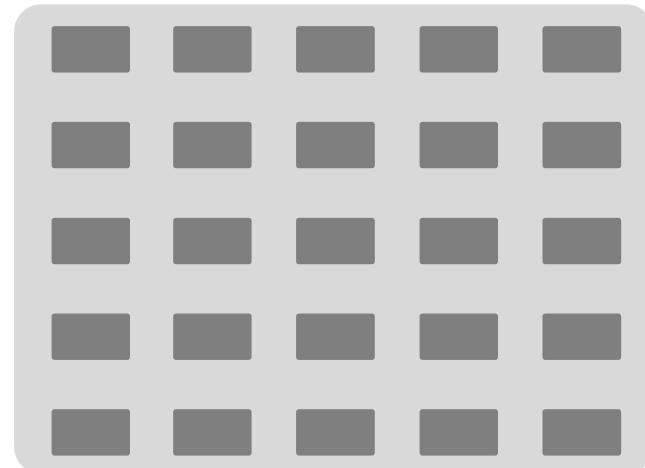
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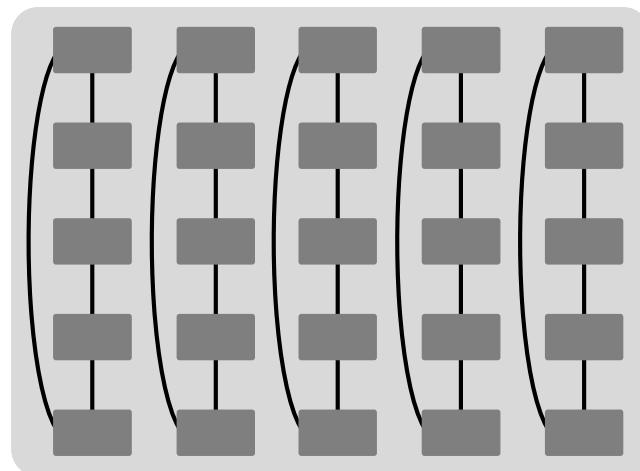
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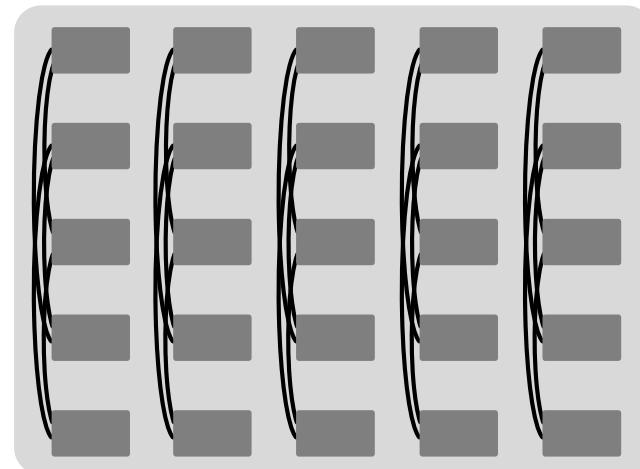
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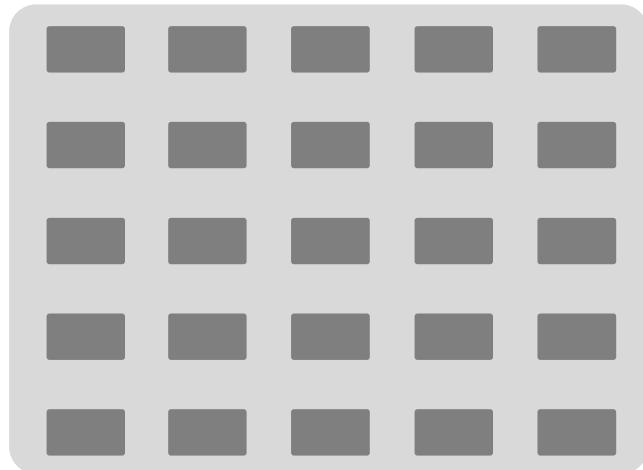
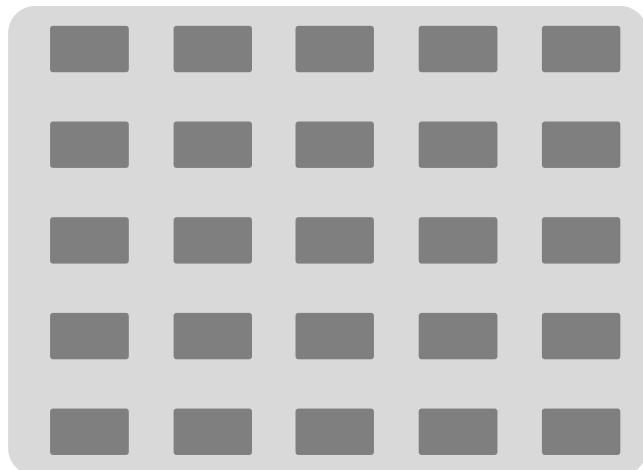


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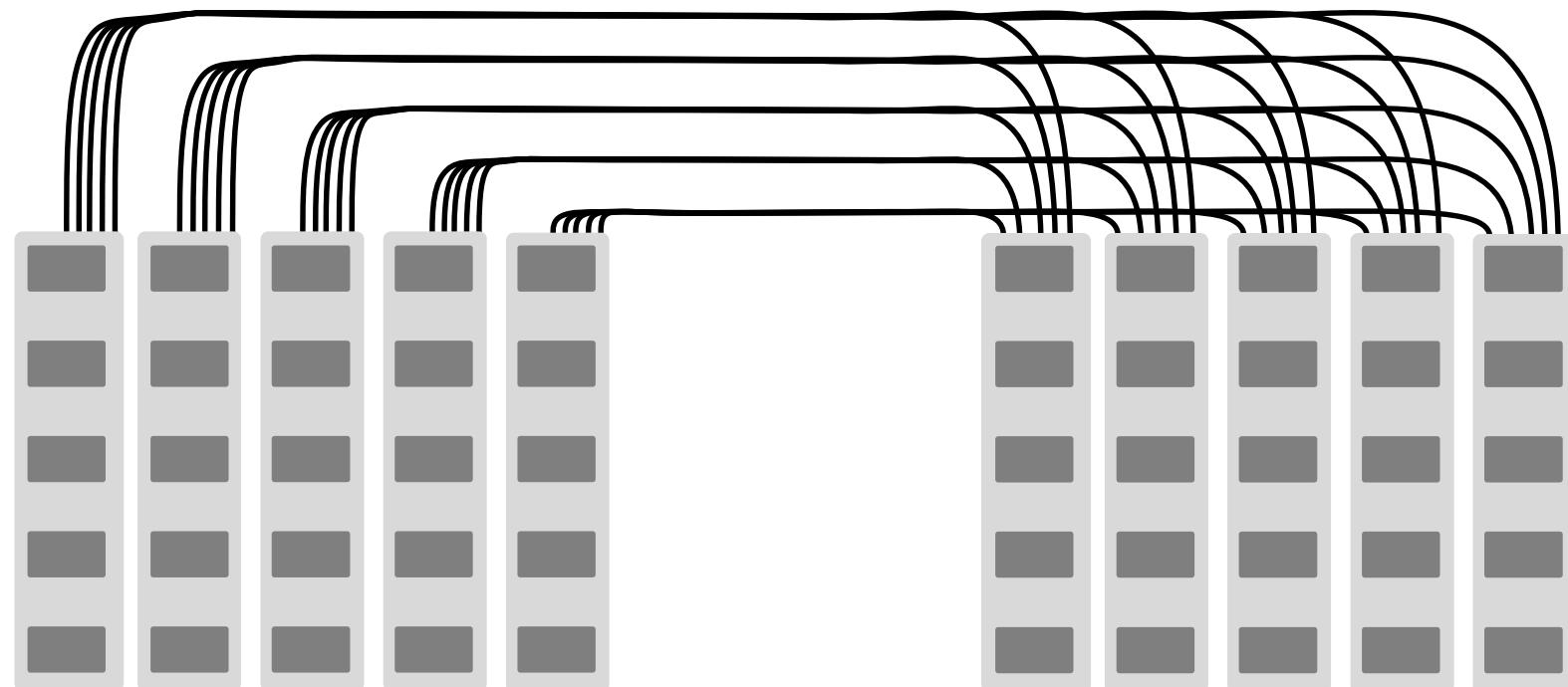


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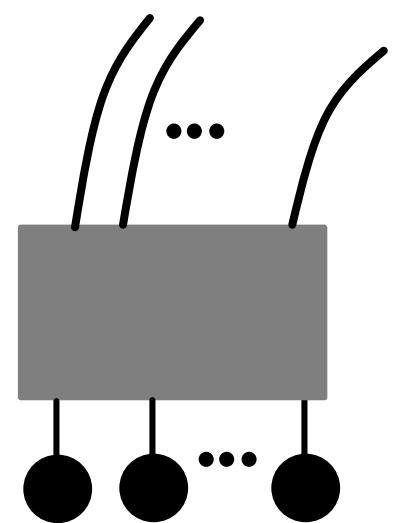
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Groups form a fully-connected bipartite graph

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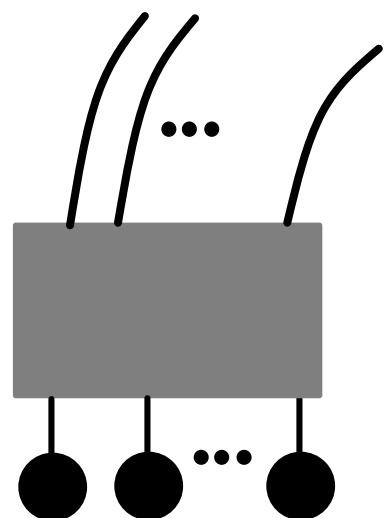
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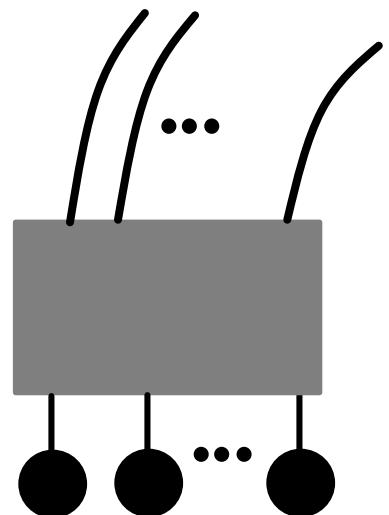


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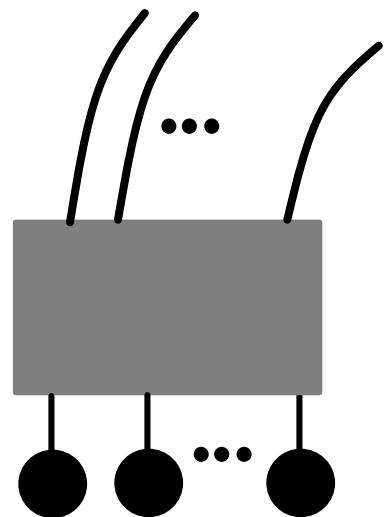
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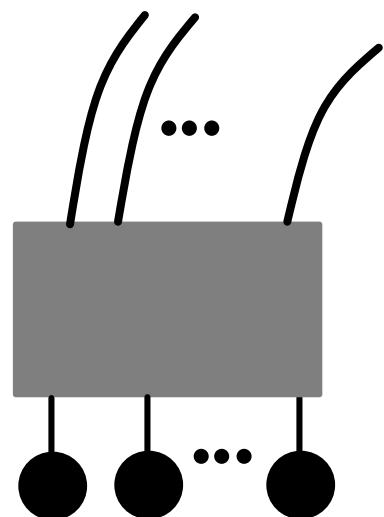
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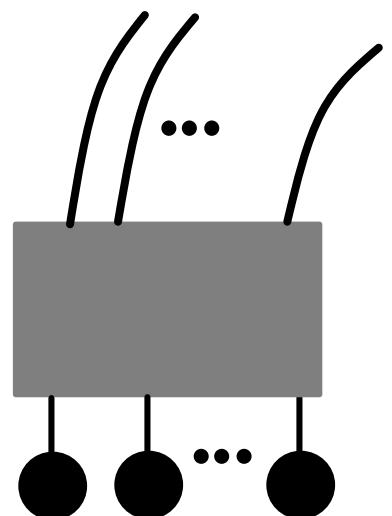
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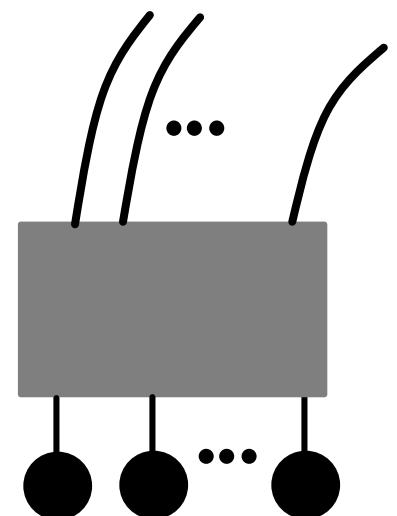
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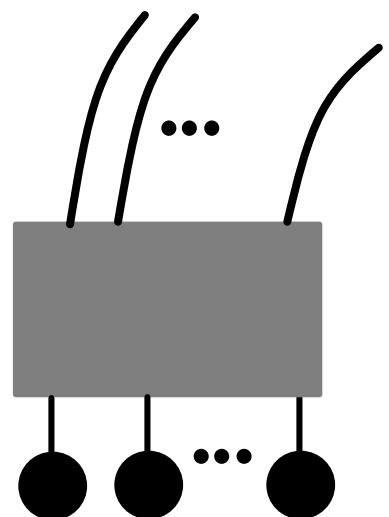
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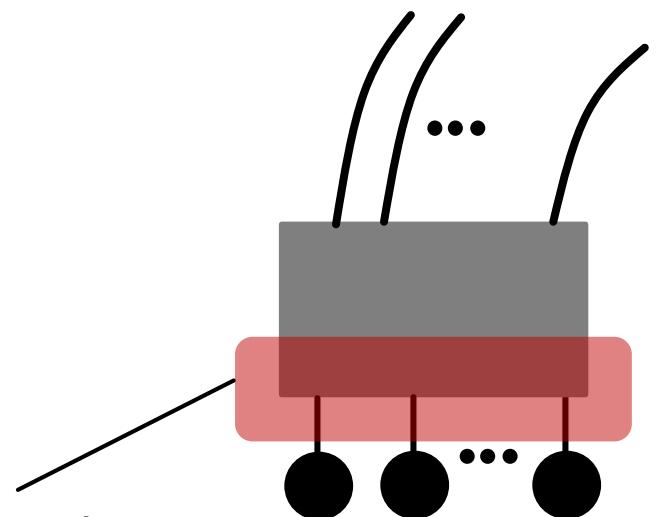
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*concentration = 33% of router radix*



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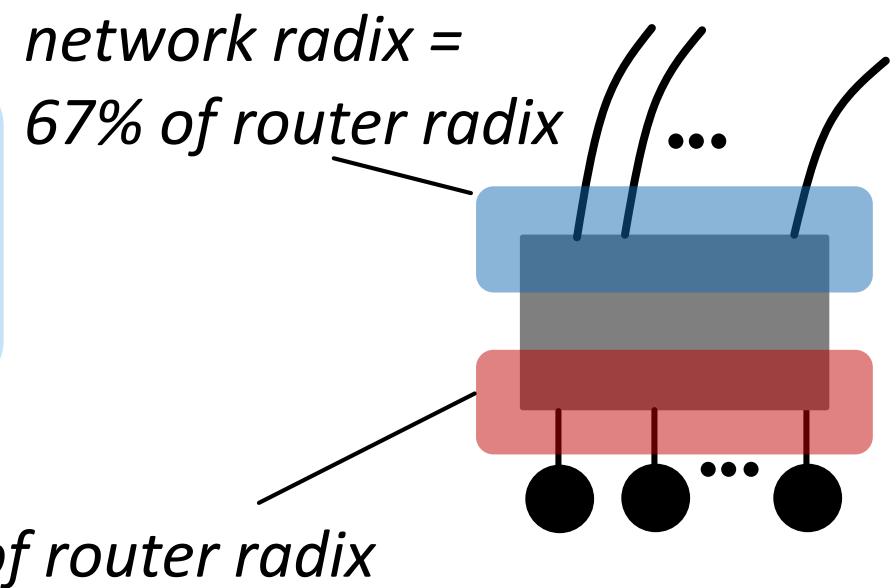
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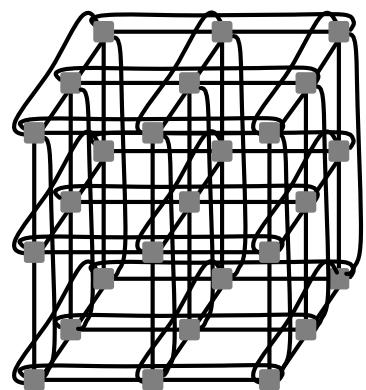
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## LOW-RADIX TOPOLOGIES

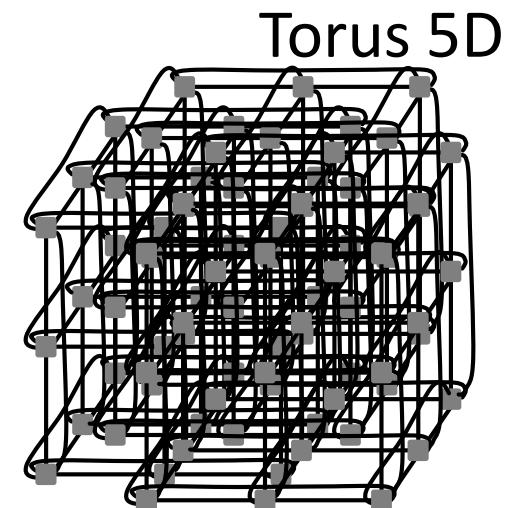
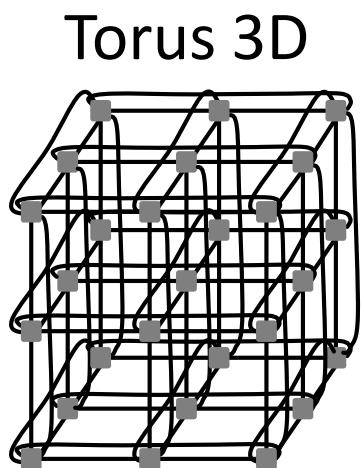
Torus 3D



Cray XE6

# COMPARISON TARGETS

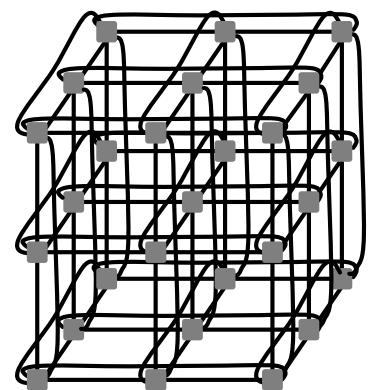
## LOW-RADIX TOPOLOGIES



# COMPARISON TARGETS

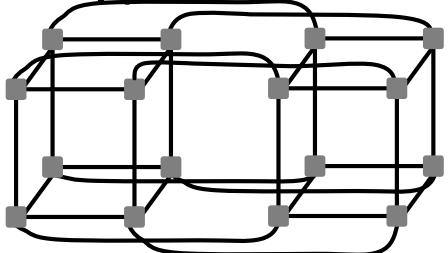
## LOW-RADIX TOPOLOGIES

Torus 3D



Cray XE6

Hypercube

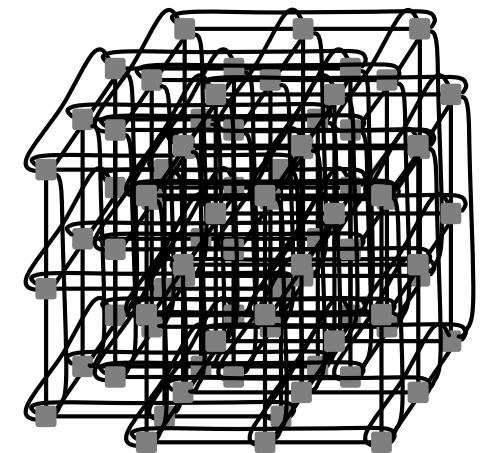


NASA Pleiades



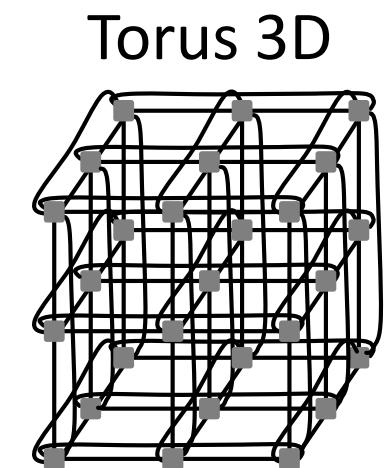
IBM BG/Q

Torus 5D

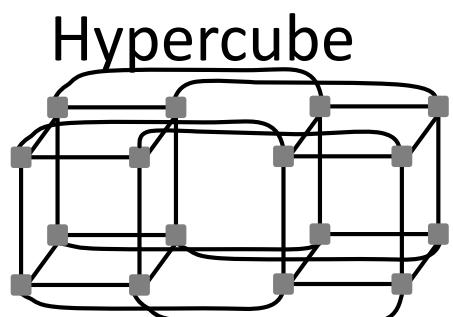


# COMPARISON TARGETS

## LOW-RADIX TOPOLOGIES



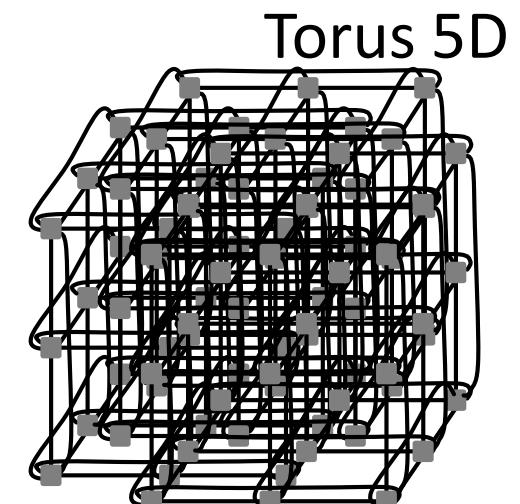
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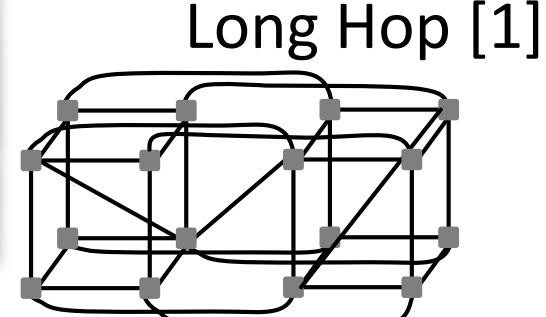
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IBM BG/Q



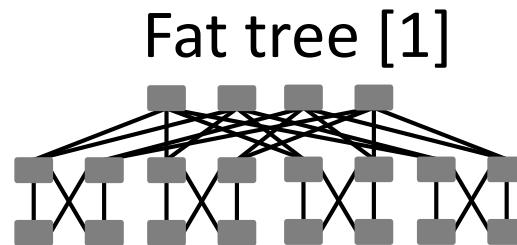
Infinetics



[1] Tomic, Ratko V. Optimal networks from error correcting codes. 2013 ACM/IEEE Symposium on Architectures for Networking and Communications Systems (ANCS)

# COMPARISON TARGETS HIGH-RADIX TOPOLOGIES

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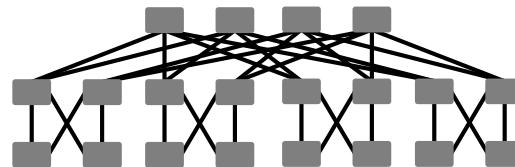


TSUBAME2.0

[1] C. E. Leiserson. Fat-trees: universal networks for hardware-efficient supercomputing. IEEE Transactions on Computers. 1985

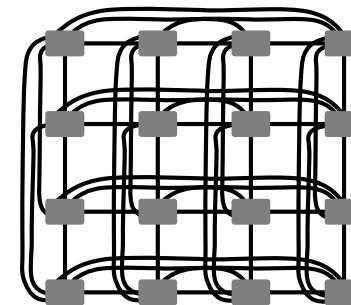
# COMPARISON TARGETS HIGH-RADIX TOPOLOGIES

Fat tree [1]



TSUBAME2.0

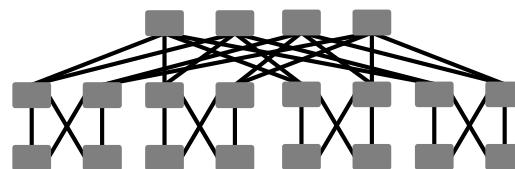
Flattened Butterfly [2]



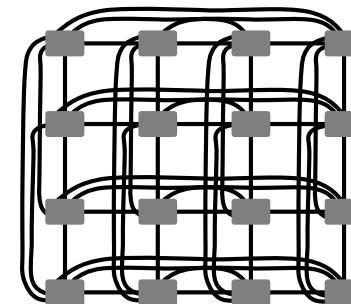
- [1] C. E. Leiserson. Fat-trees: universal networks for hardware-efficient supercomputing. IEEE Transactions on Computers. 1985  
[2] J. Kim, W. J. Dally, D. Abts. Flattened butterfly: a cost-efficient topology for high-radix networks. ISCA'07

# COMPARISON TARGETS HIGH-RADIX TOPOLOGIES

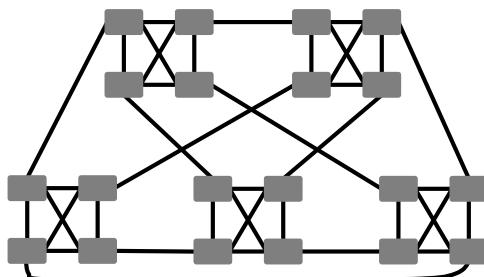
Fat tree [1]



Flattened Butterfly [2]



Dragonfly [3]

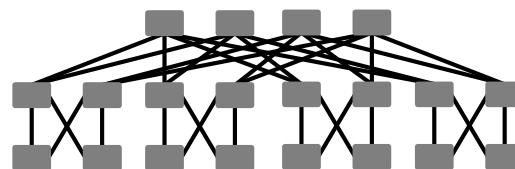


Cray Cascade

- [1] C. E. Leiserson. Fat-trees: universal networks for hardware-efficient supercomputing. *IEEE Transactions on Computers*. 1985
- [2] J. Kim, W. J. Dally, D. Abts. Flattened butterfly: a cost-efficient topology for high-radix networks. *ISCA'07*
- [3] J. Kim, W. J. Dally, S. Scott, D. Abts. Technology-Driven, Highly-Scalable Dragonfly Topology. *ISCA'08*

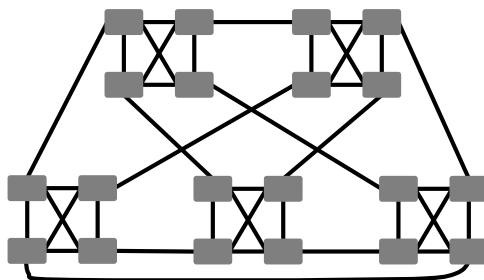
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Fat tree [1]



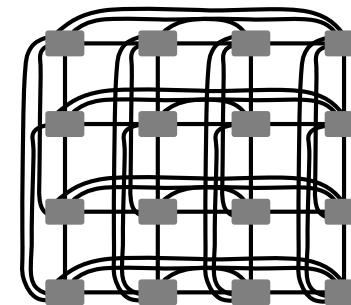
TSUBAME2.0

Dragonfly [3]

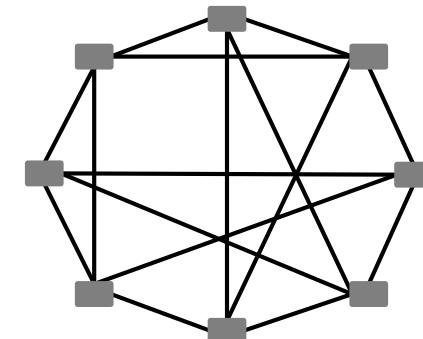


Cray Cascade

## Flattened Butterfly [2]



## Random Topologies [4]



[1] C. E. Leiserson. Fat-trees: universal networks for hardware-efficient supercomputing. *IEEE Transactions on Computers*. 1985

[2] J. Kim, W. J. Dally, D. Abts. Flattened butterfly: a cost-efficient topology for high-radix networks. *ISCA'07*

[3] J. Kim, W. J. Dally, S. Scott, D. Abts. Technology-Driven, Highly-Scalable Dragonfly Topology. *ISCA'08*

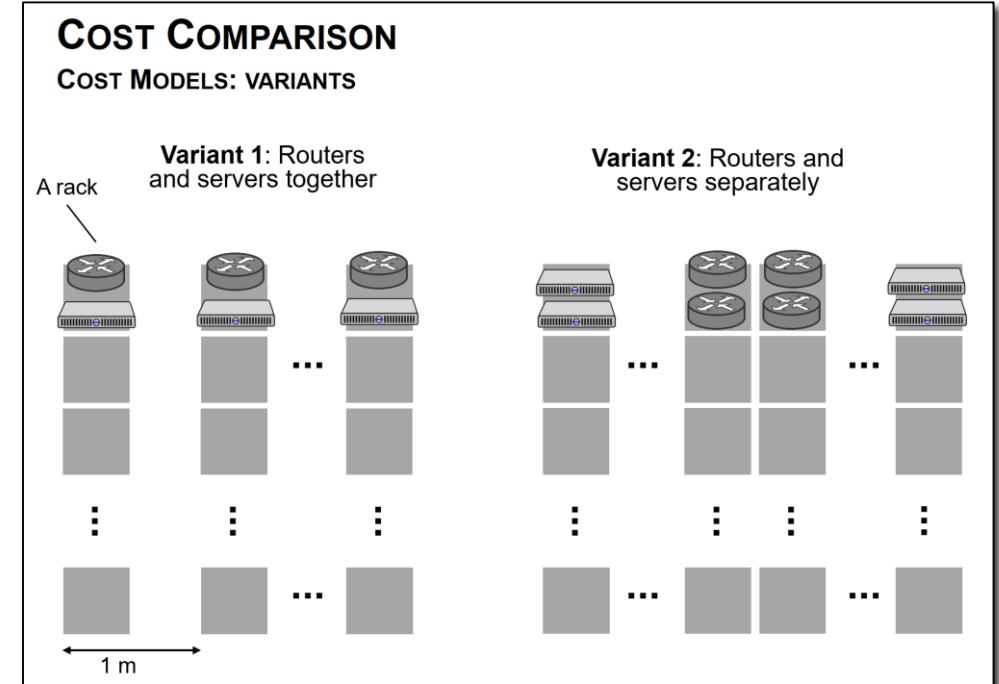
[4] M. Koibuchi, H. Matsutani, H. Amano, D. F. Hsu, H. Casanova. A case for random shortcut topologies for HPC interconnects. *ISCA'12*

# COST OF NETWORK CONSTRUCTION

## MODELS, VARIANTS

# COST OF NETWORK CONSTRUCTION MODELS, VARIANTS

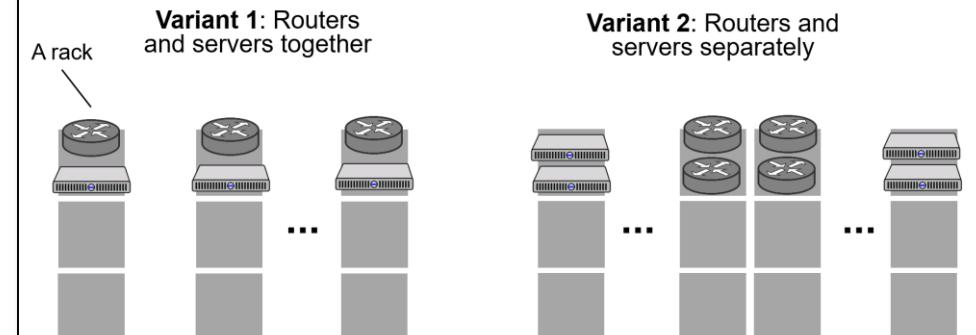
## Cluster structure



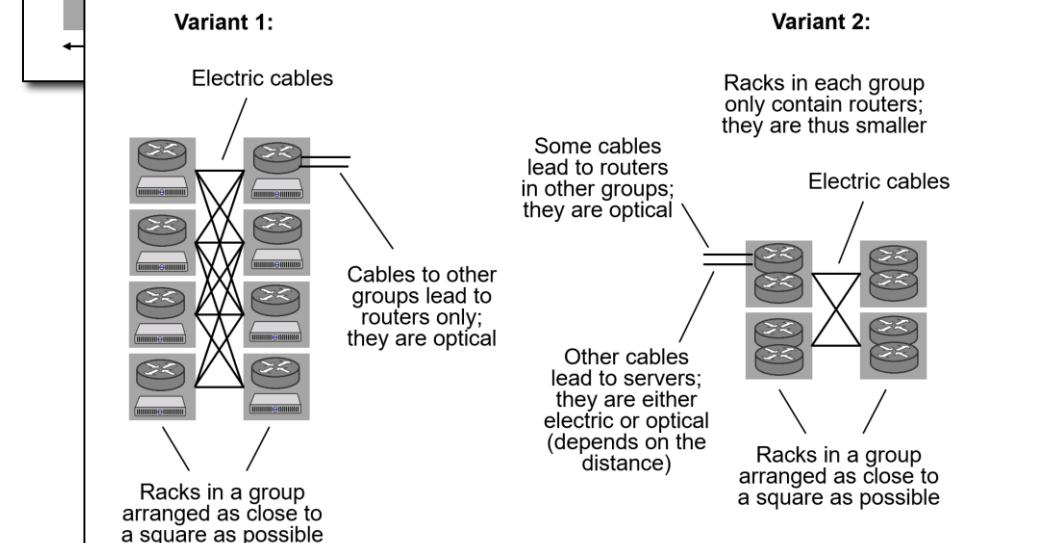
# COST OF NETWORK CONSTRUCTION MODELS, VARIANTS

## Cluster structure

### COST COMPARISON COST MODELS: VARIANTS



### COST COMPARISON COST MODELS: GROUPS

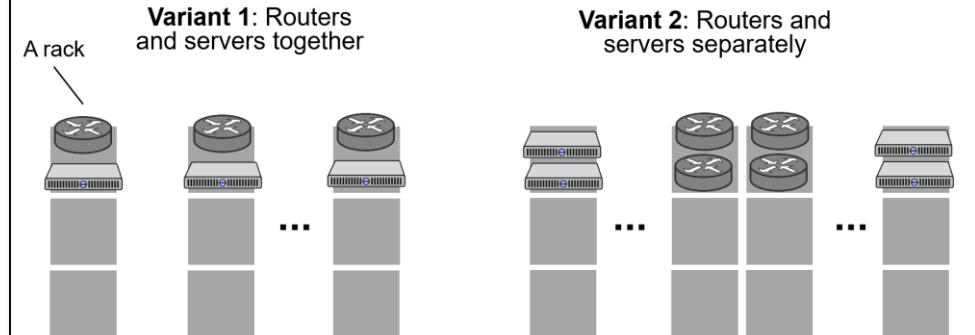


Structure of router groups

# COST OF NETWORK CONSTRUCTION MODELS, VARIANTS

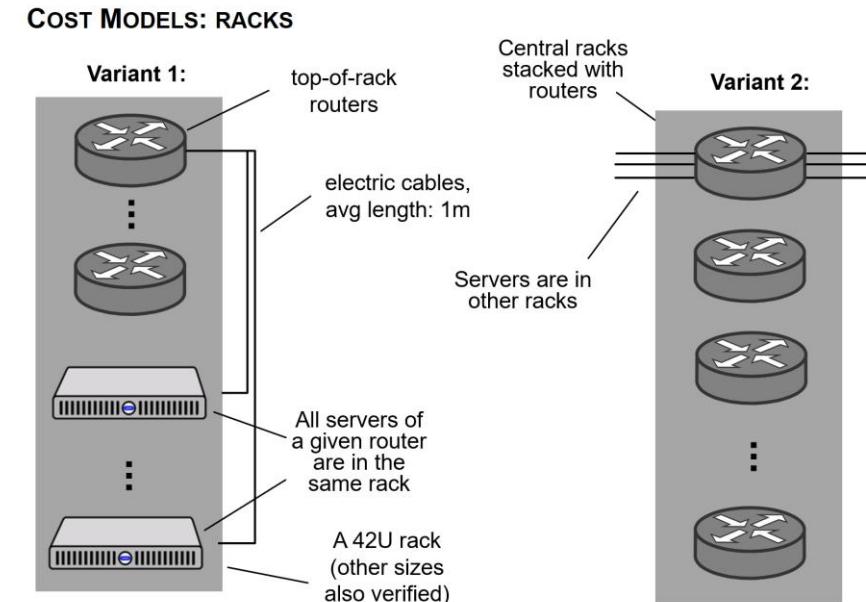
## Cluster structure

### COST COMPARISON COST MODELS: VARIANTS

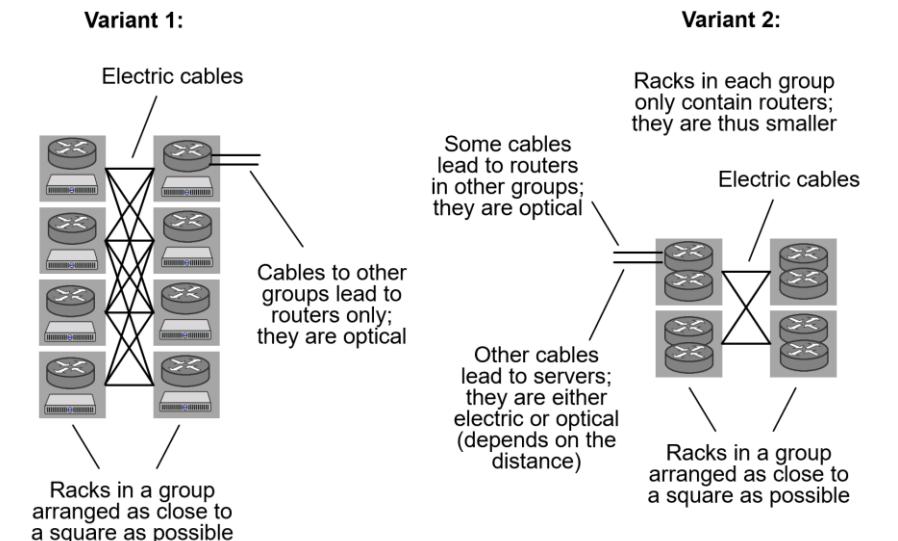


## Rack structure

### COST COMPARISON COST MODELS: RACKS



### COST COMPARISON COST MODELS: GROUPS

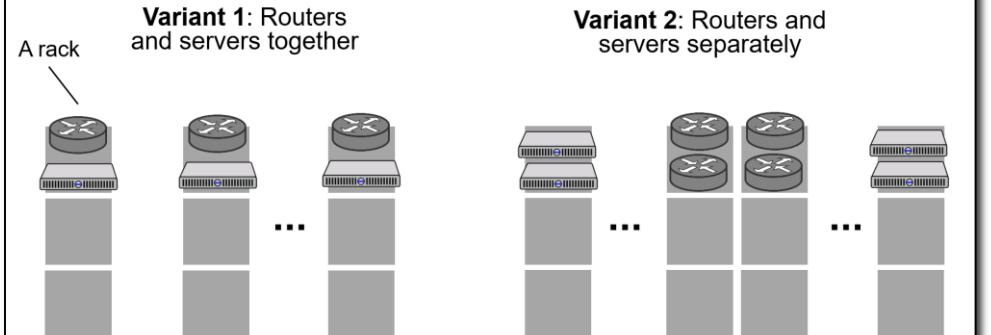


## Structure of router groups

# COST OF NETWORK CONSTRUCTION MODELS, VARIANTS

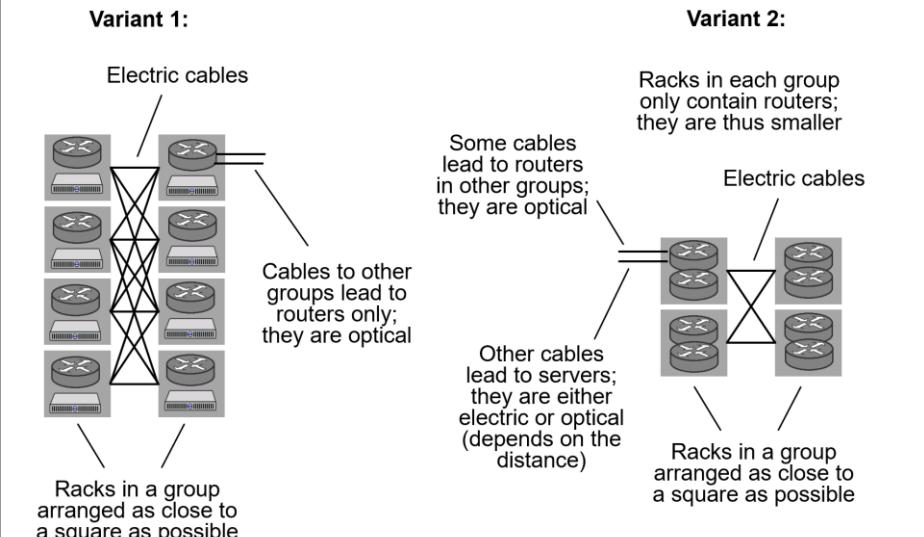
## Cluster structure

### COST COMPARISON COST MODELS: VARIANTS

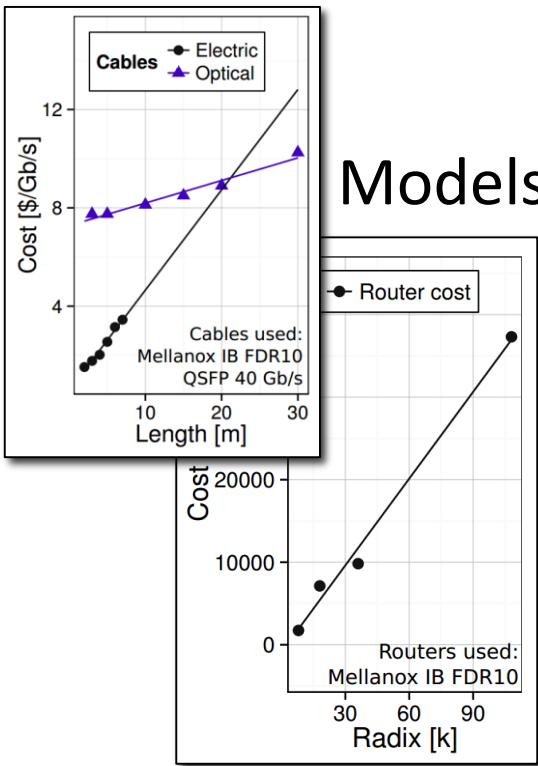


## Rack structure

### COST COMPARISON COST MODELS: GROUPS

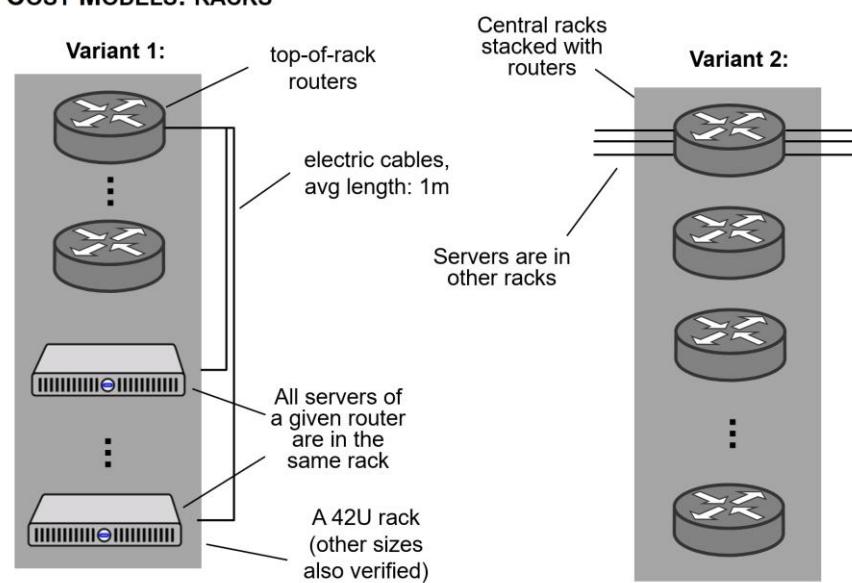


## Models



## Rack structure

### COST COMPARISON COST MODELS: RACKS



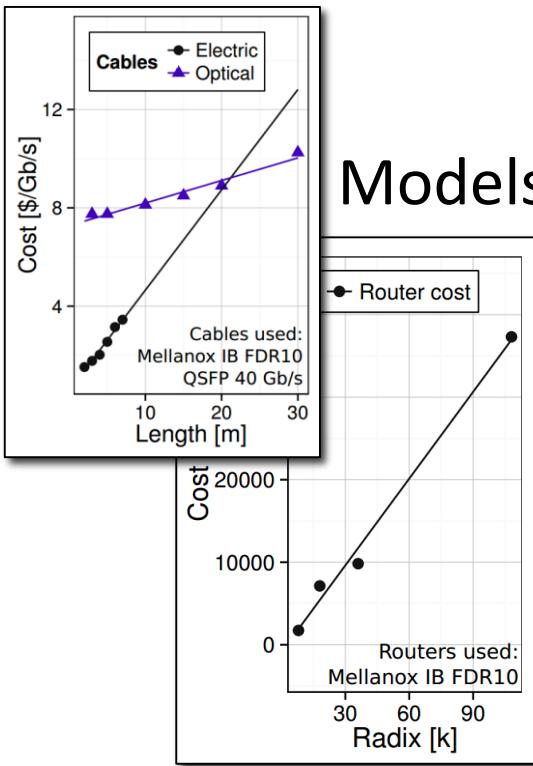
## Structure of router groups

# COST OF NETWORK CONSTRUCTION MODELS, VARIANTS

## Routers

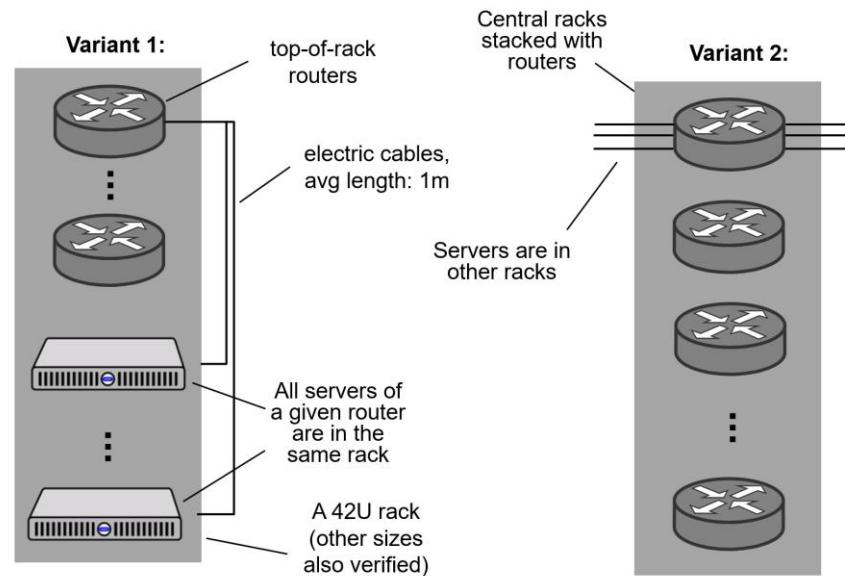


## Models

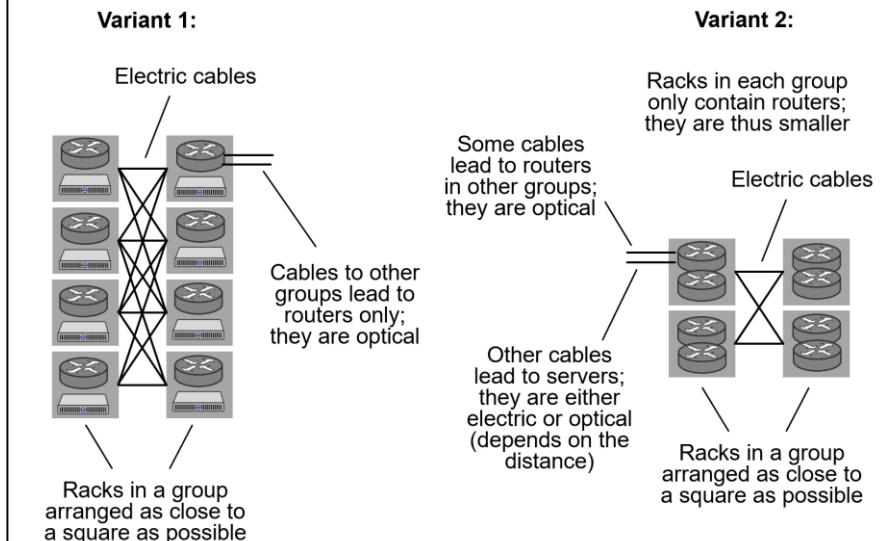


## Rack structure

### COST COMPARISON COST MODELS: RACKS



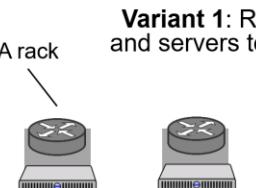
### COST COMPARISON COST MODELS: GROUPS



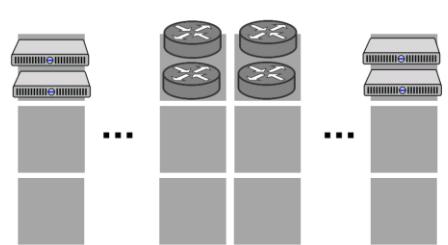
## Cluster structure

### COST COMPARISON COST MODELS: VARIANTS

**Variant 1:** Routers and servers together



**Variant 2:** Routers and servers separately



## Structure of router groups

# COST OF NETWORK CONSTRUCTION

## MODELS, VARIANTS

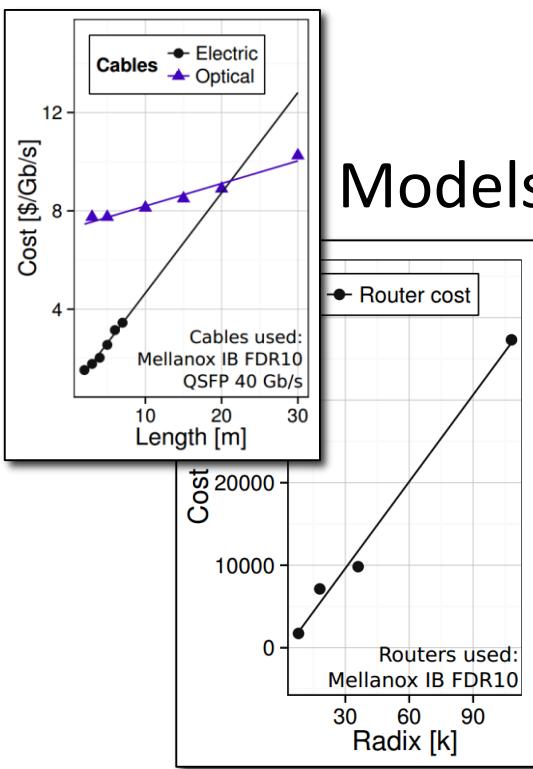
### Cables



### Routers



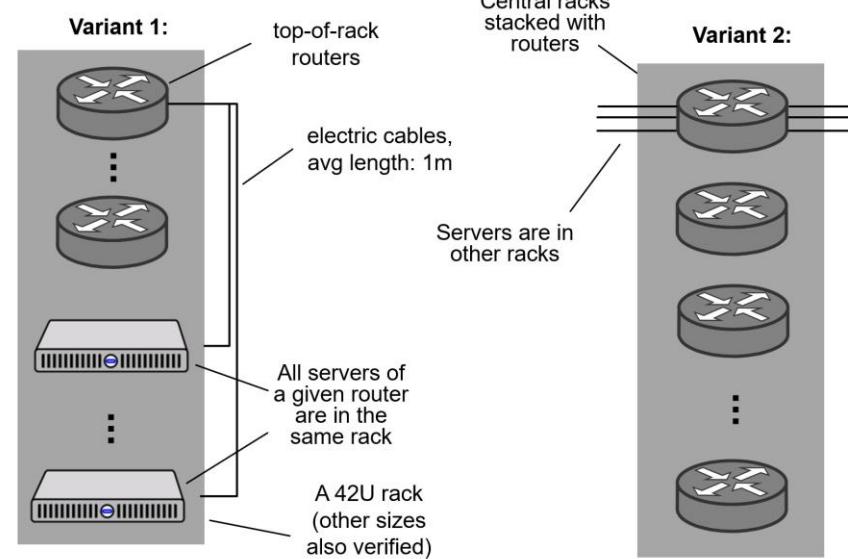
### Models



### Rack structure

#### COST COMPARISON

##### COST MODELS: RACKS



### Cluster structure

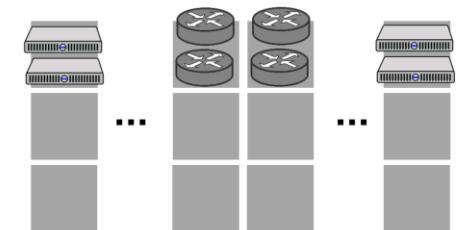
#### COST COMPARISON

##### COST MODELS: VARIANTS

**Variant 1:** Routers and servers together



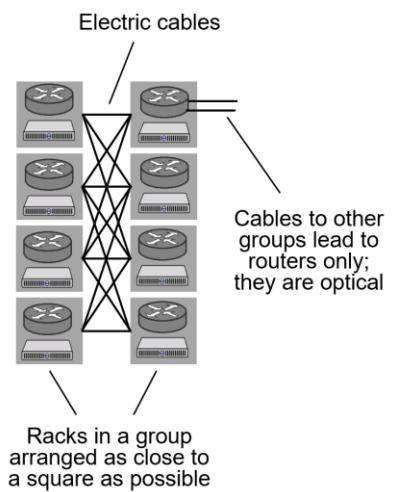
**Variant 2:** Routers and servers separately



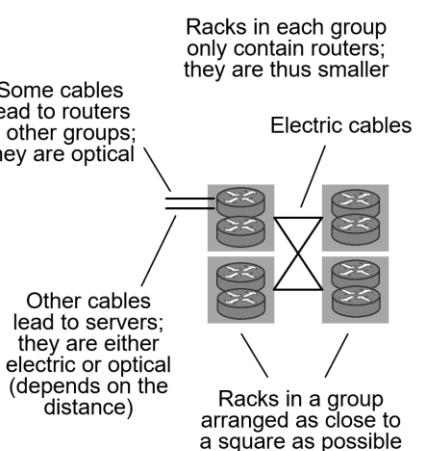
#### COST COMPARISON

##### COST MODELS: GROUPS

**Variant 1:**

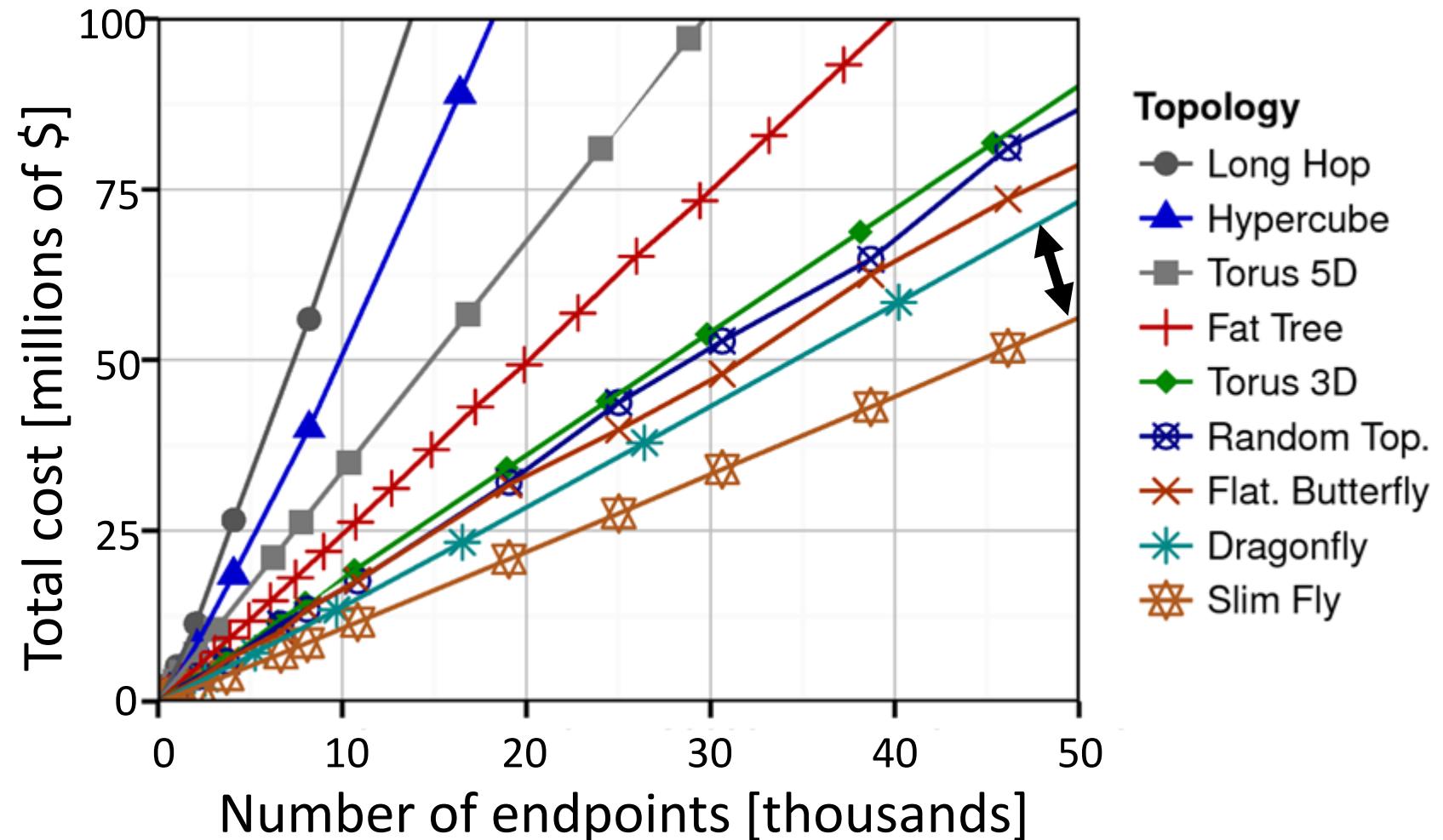


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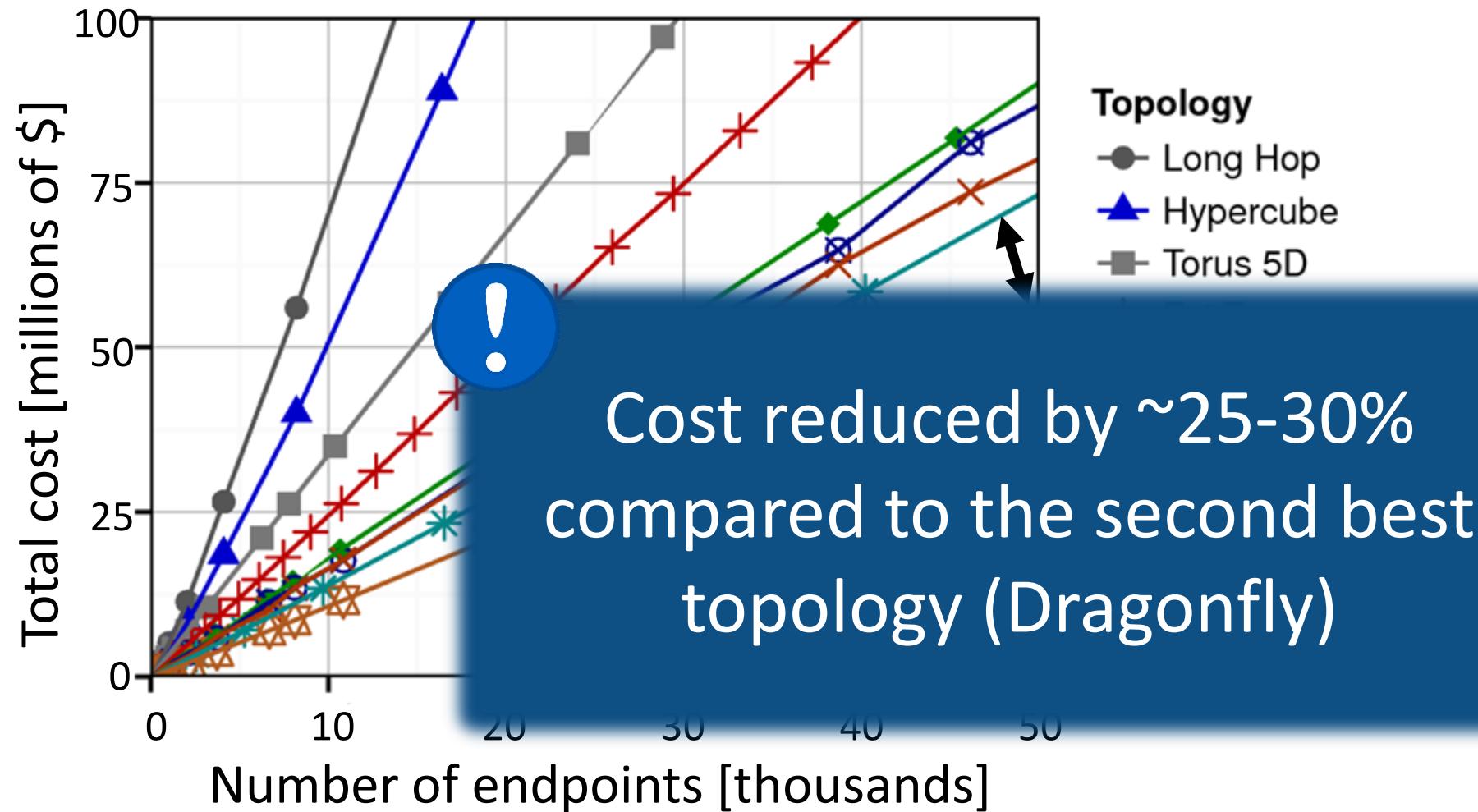


### Structure of router groups

# **RESULTS: COST OF NETWORK CONSTRUCTION**

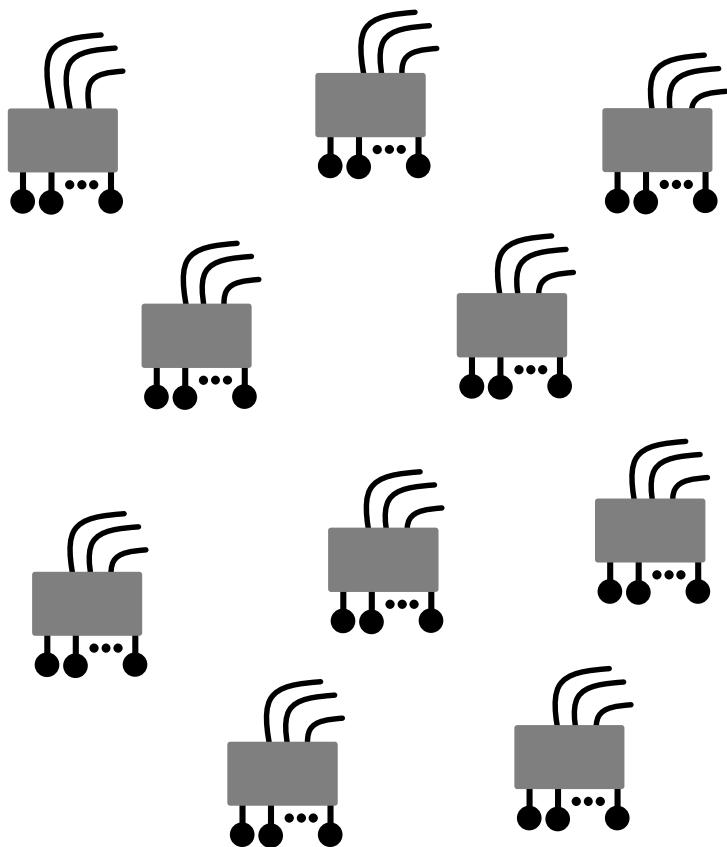


## RESULTS: COST OF NETWORK CONSTRUCTION

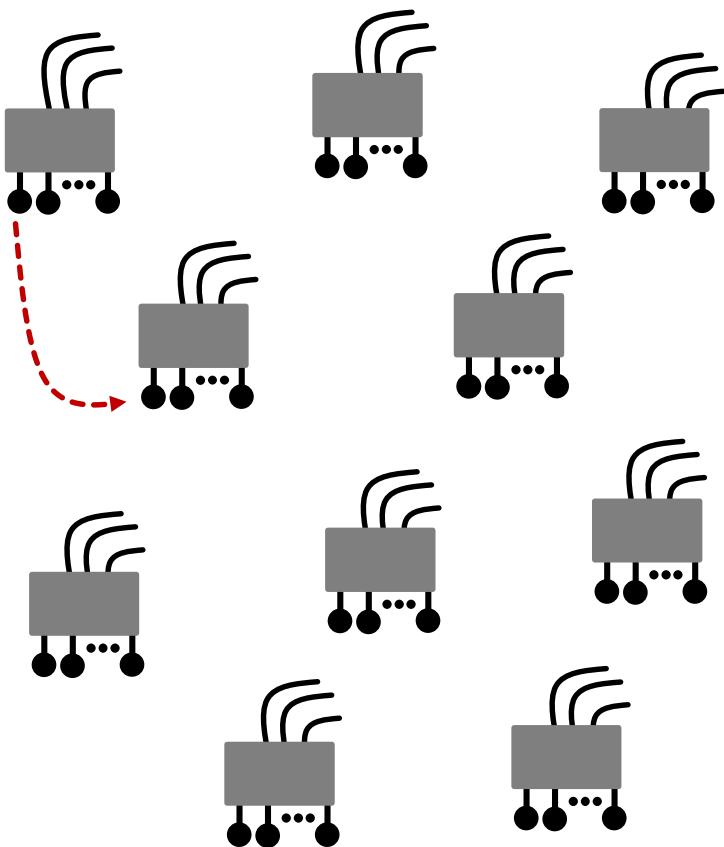


# RESULTS: PERFORMANCE

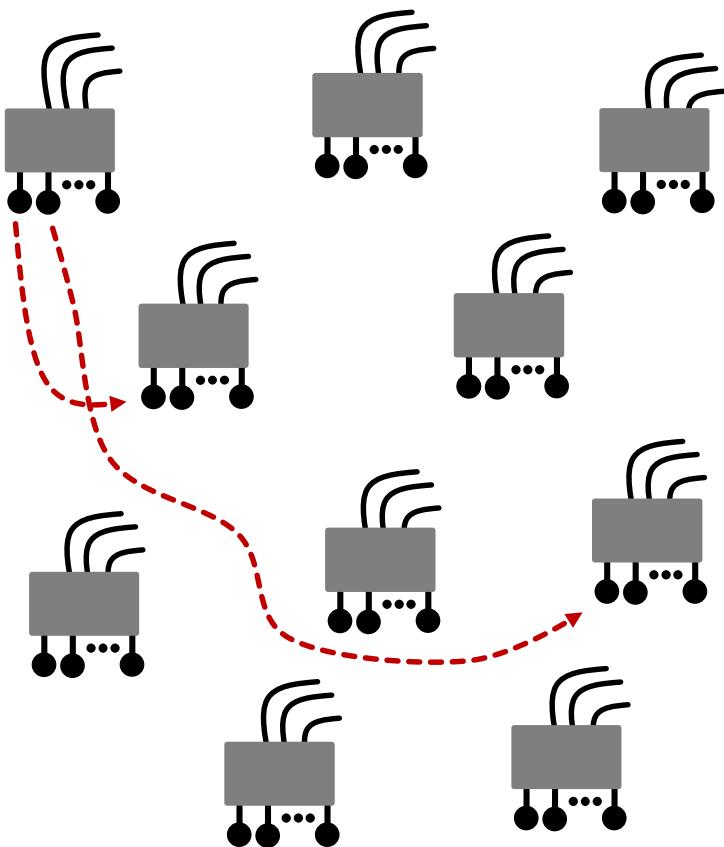
# RESULTS: PERFORMANCE



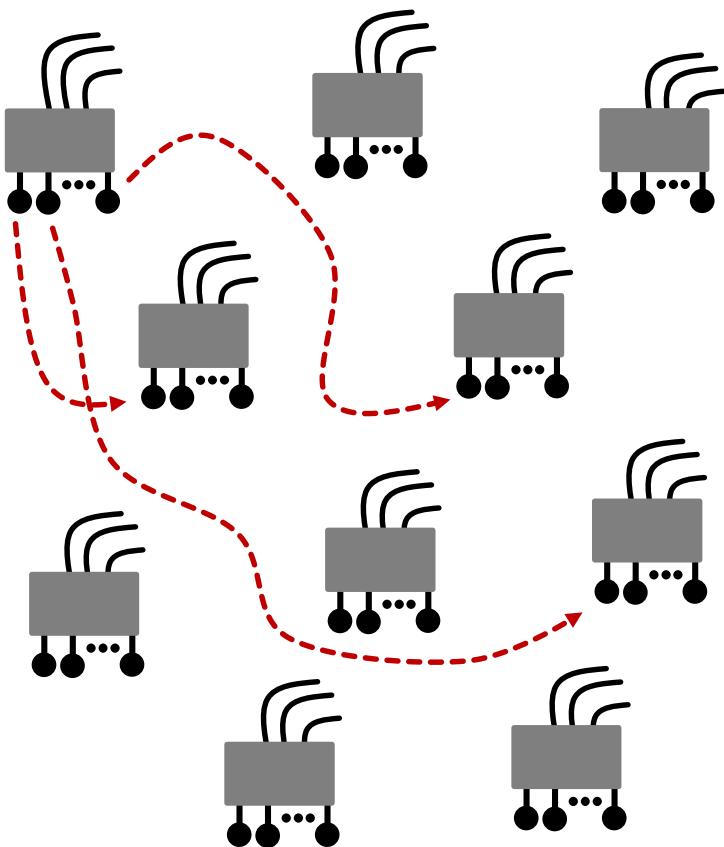
# RESULTS: PERFORMANCE



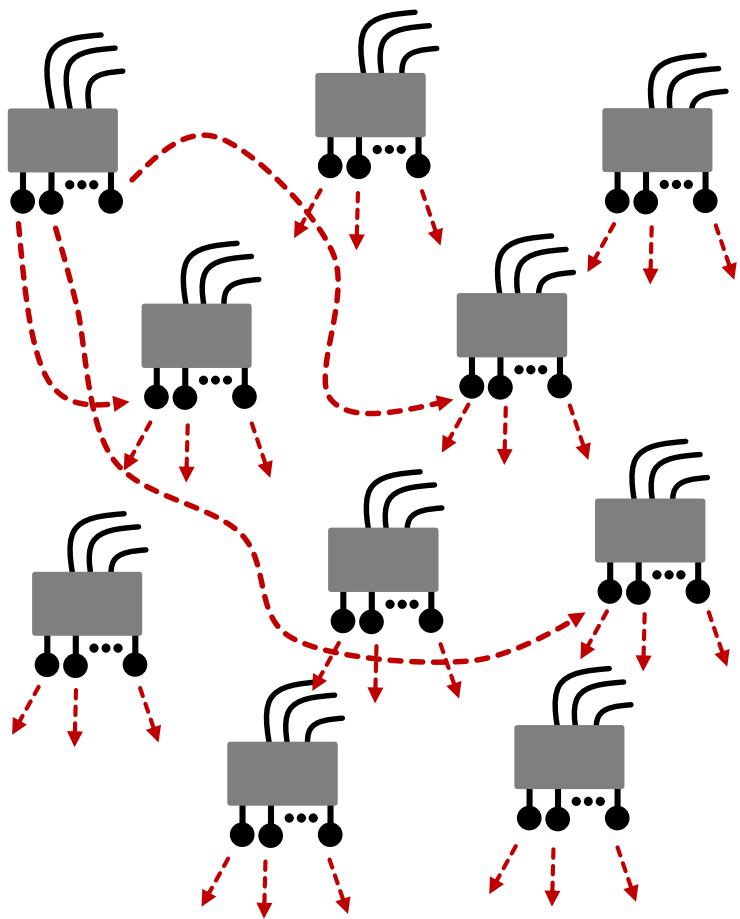
# RESULTS: PERFORMANCE



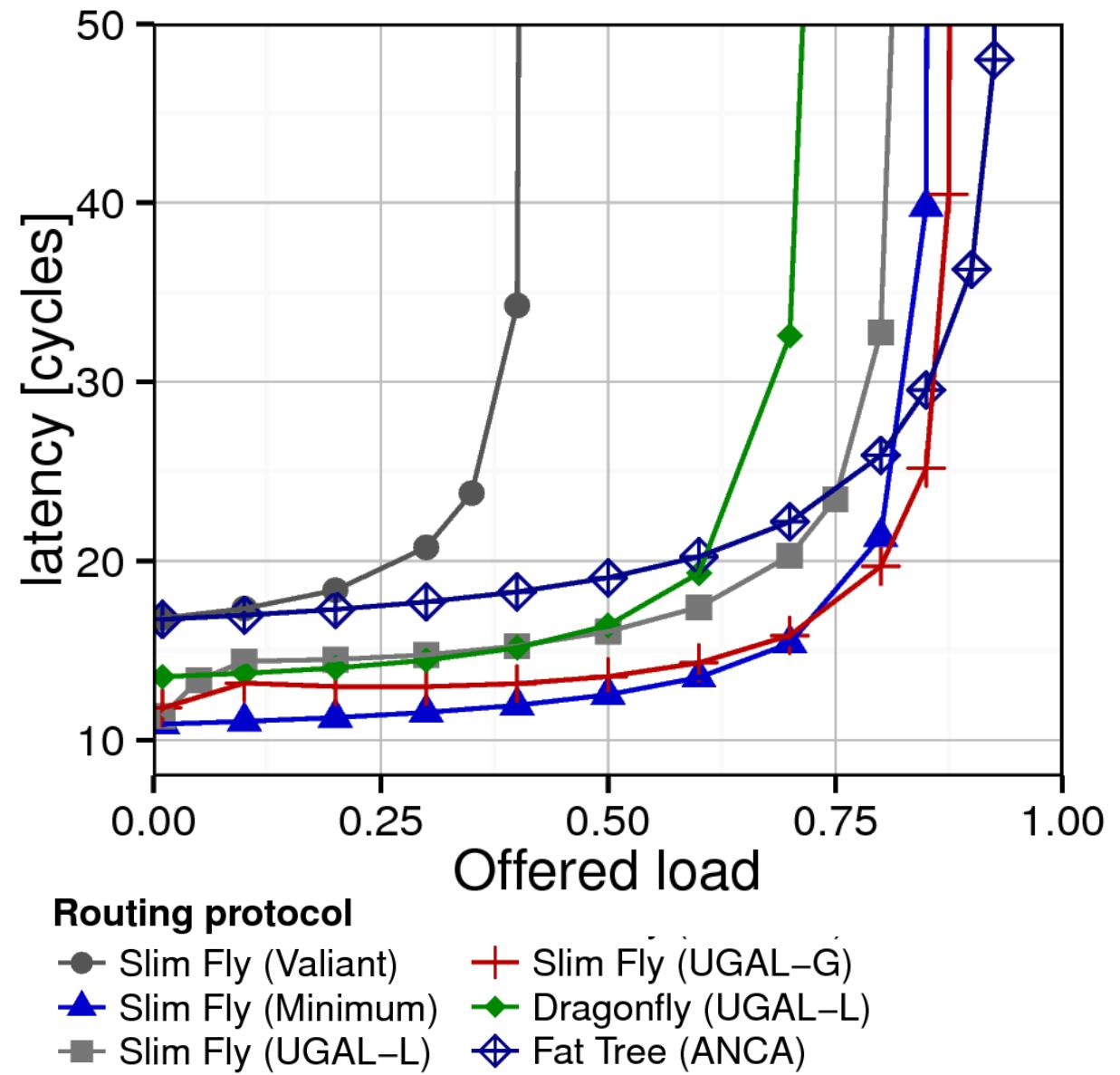
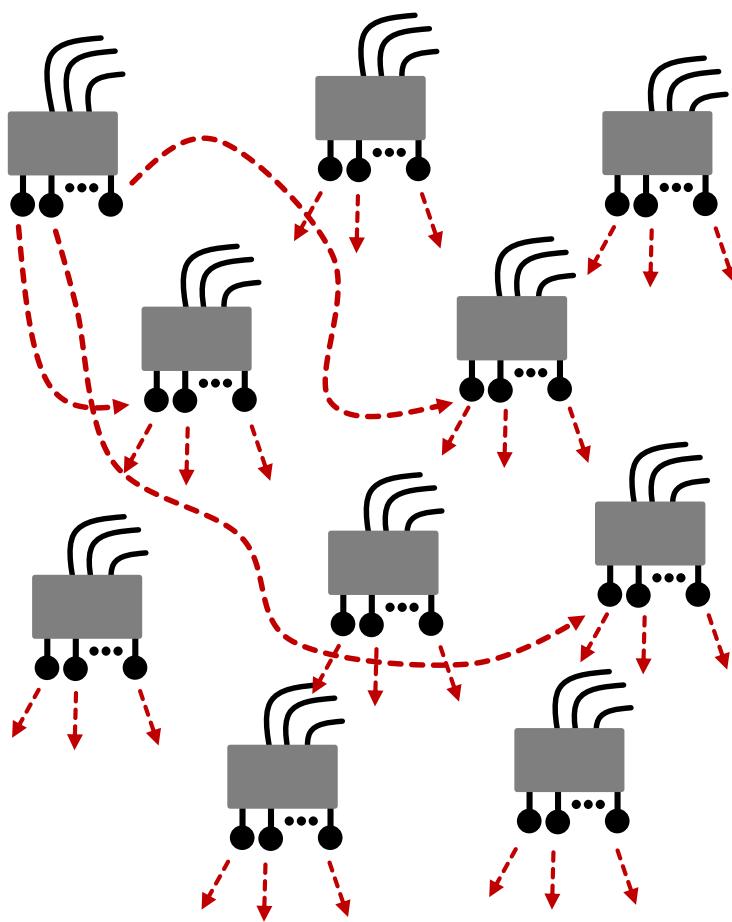
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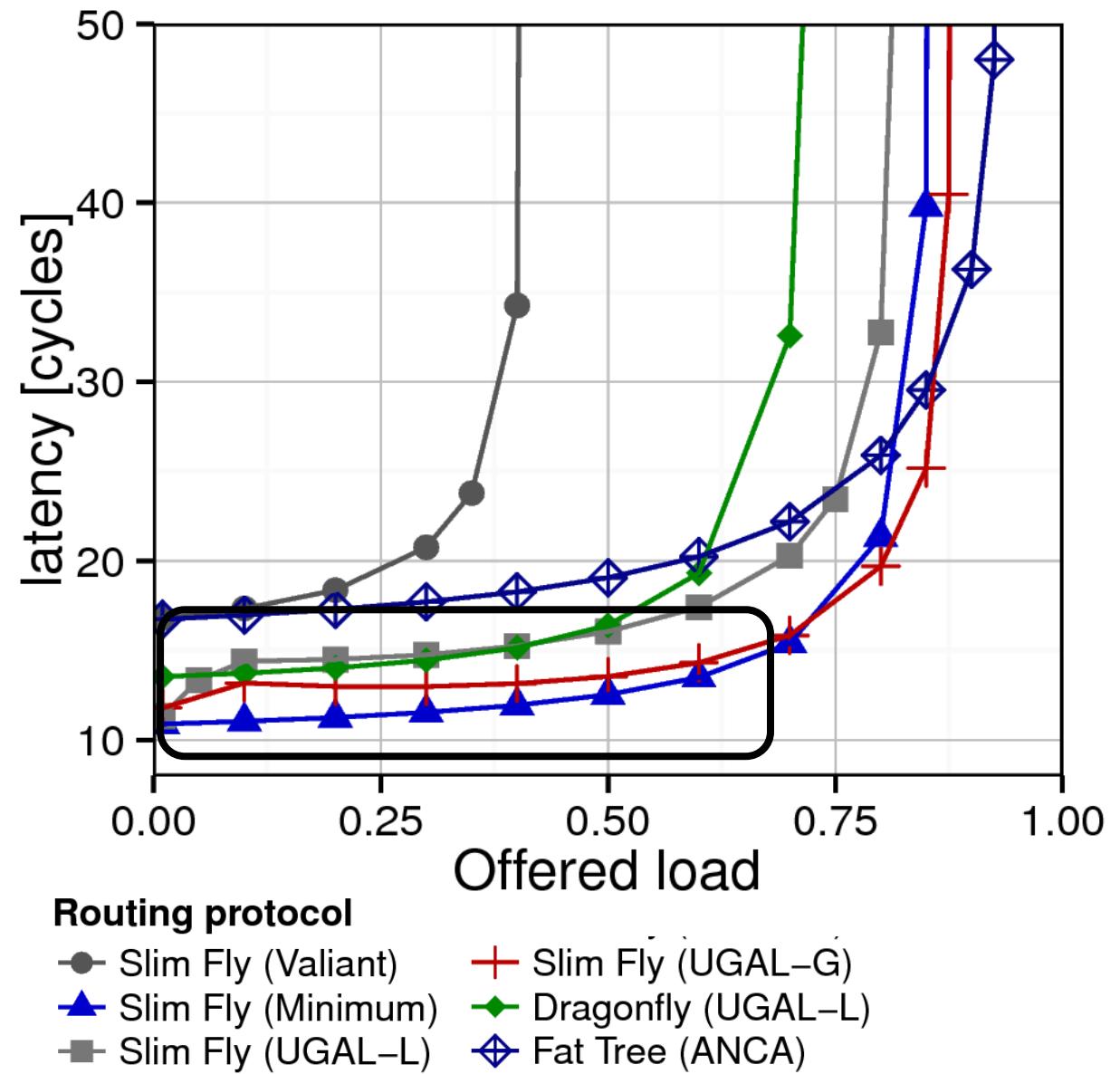
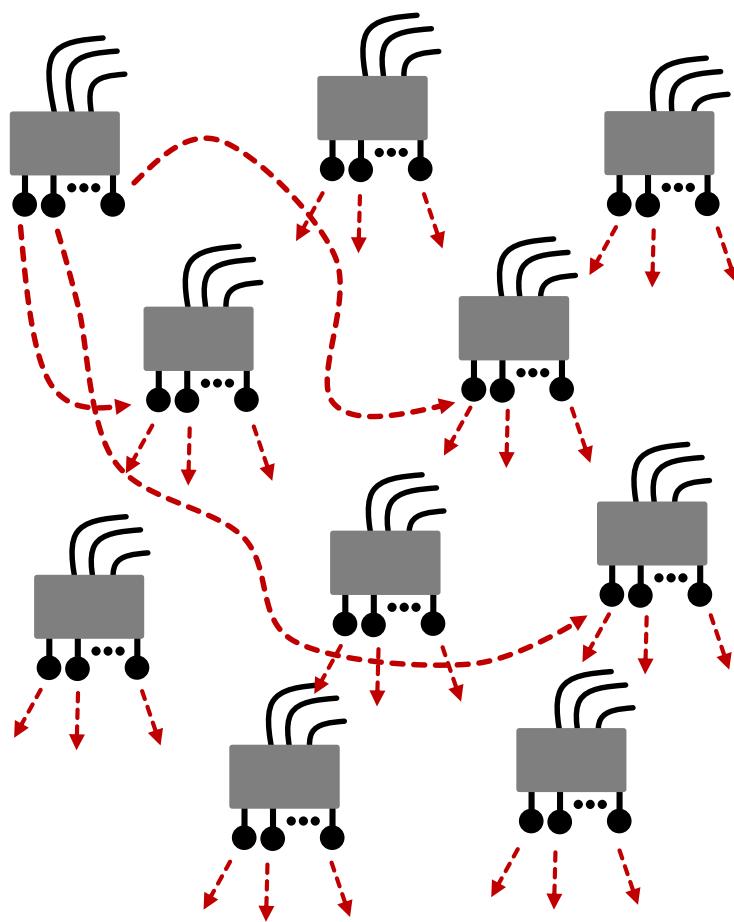
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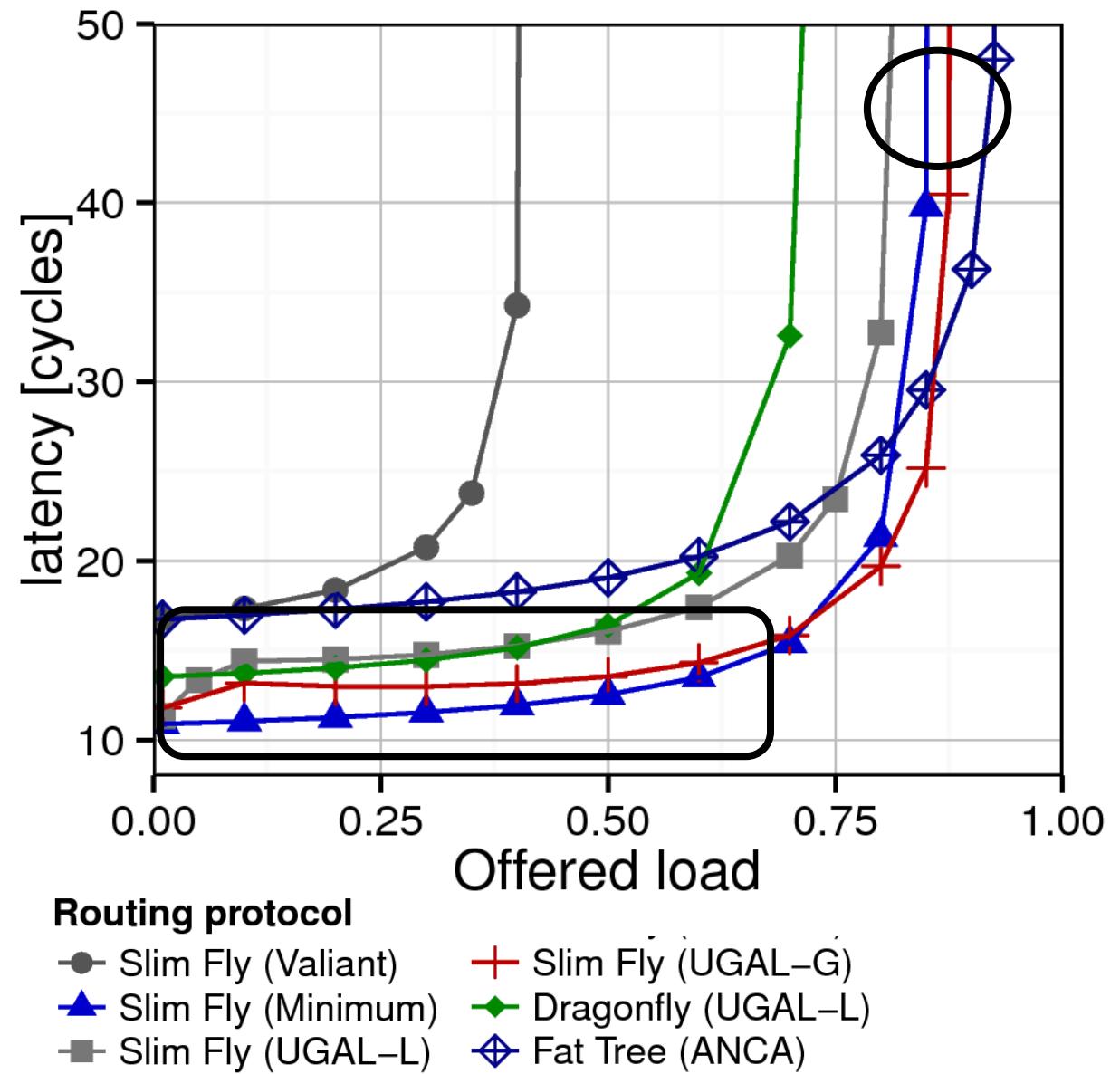
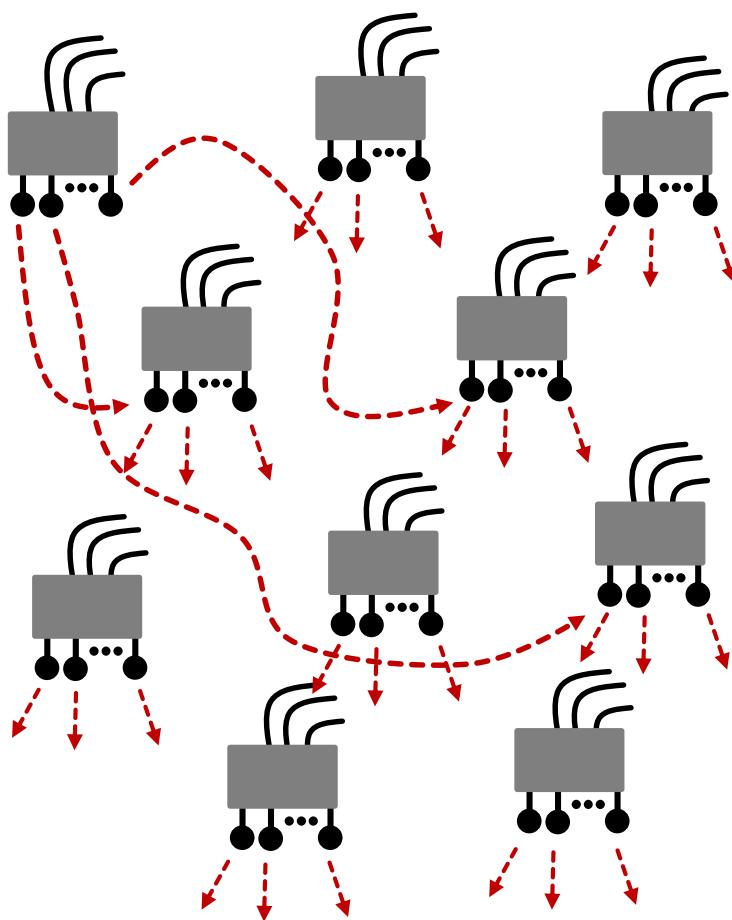
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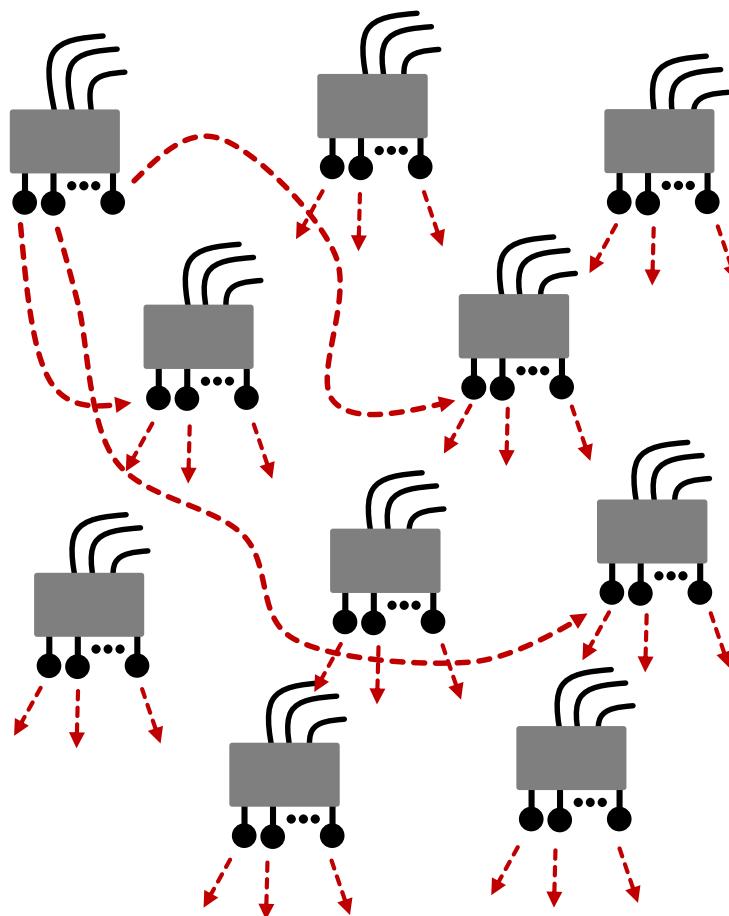
# RESULTS: PERFORMANCE



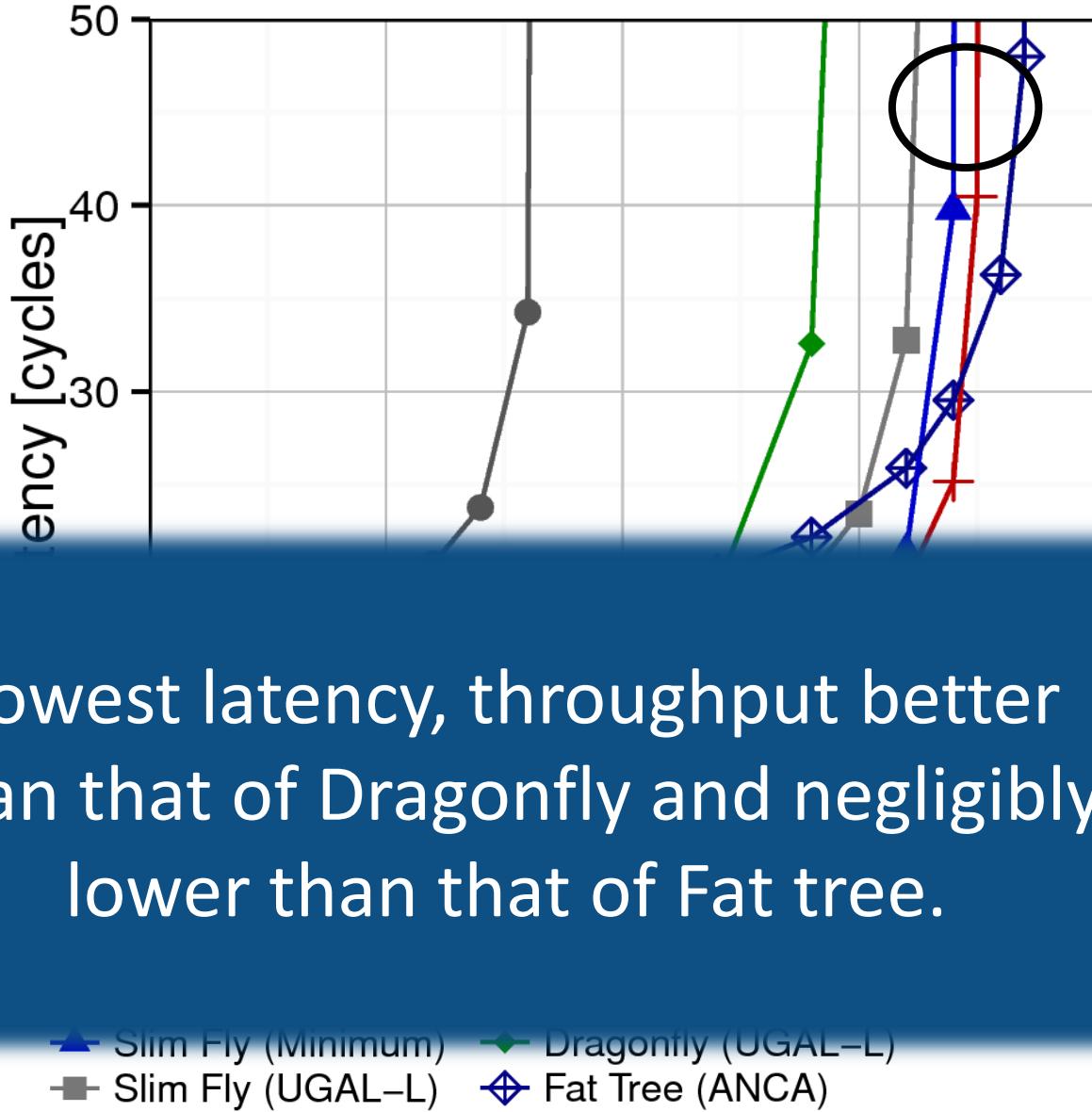
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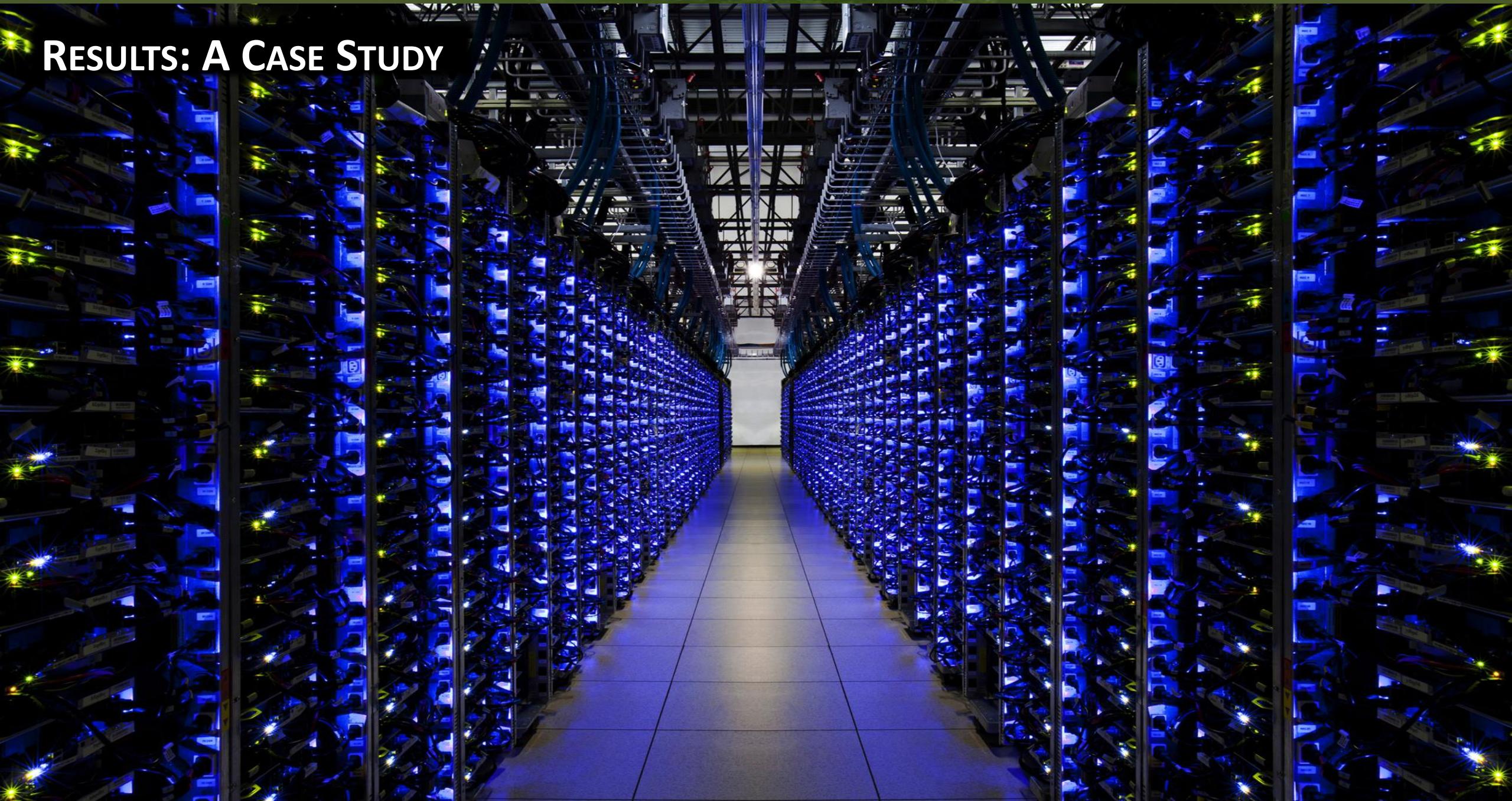
## RESULTS: PERFORMANCE



Lowest latency, throughput better than that of Dragonfly and negligibly lower than that of Fat tree.

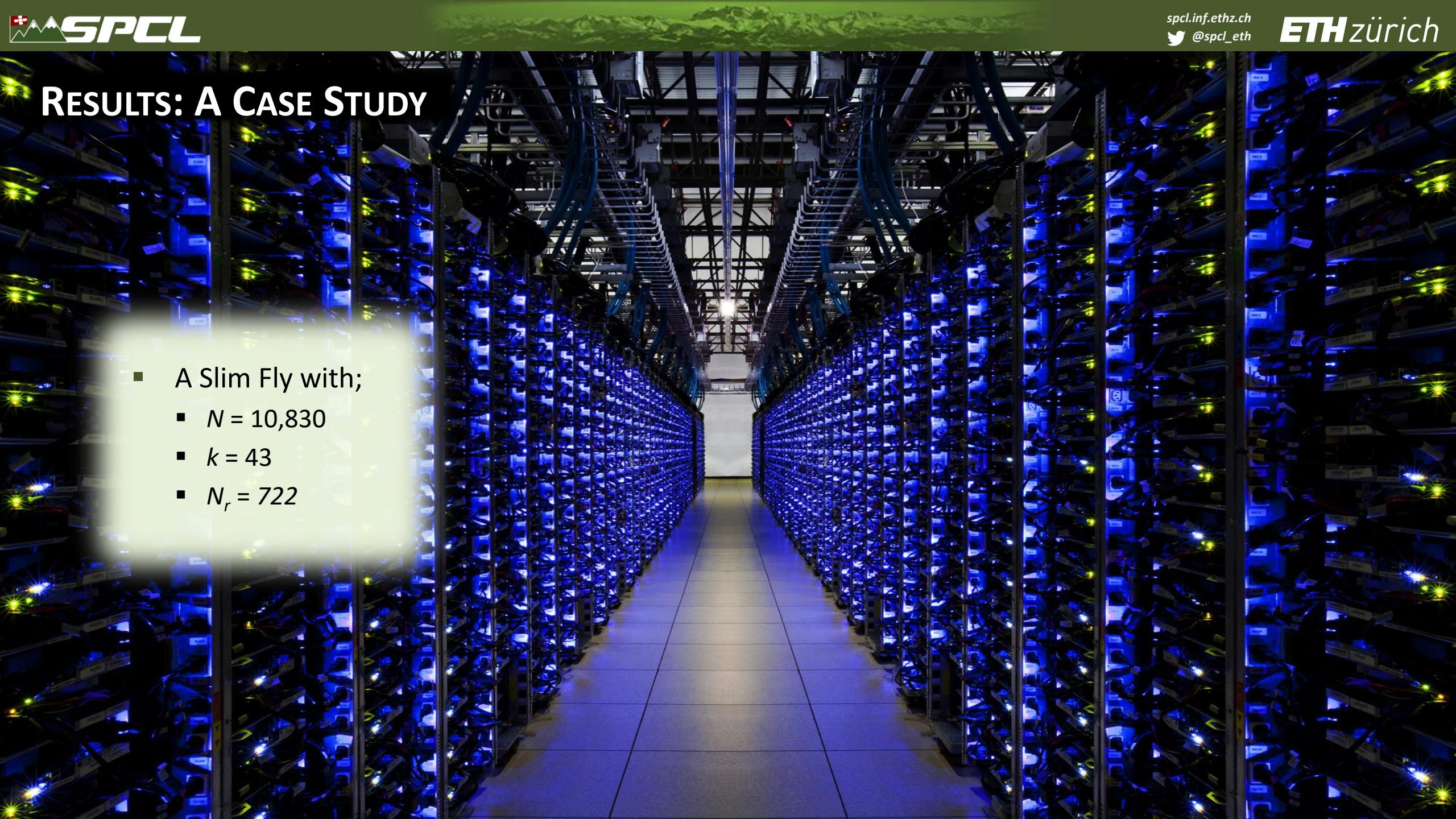


# RESULTS: A CASE STUDY



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- A Slim Fly with;
  - $N = 10,830$
  - $k = 43$
  - $N_r = 722$



# RESULTS: A CASE STUDY

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Topology					
Endpoints ( $N$ )	19,876	40,200	20,736	58,806	<b>10,830</b>
Routers ( $N_r$ )	2,311	4,020	1,728	5,346	<b>722</b>
Radix ( $k$ )	<b>43</b>	<b>43</b>	<b>43</b>	<b>43</b>	<b>43</b>
Electric cables	19,414	32,488	9,504	56,133	<b>6,669</b>
Fiber cables	40,215	33,842	20,736	29,524	<b>6,869</b>
Cost per node [\\$]	2,346	1,743	1,570	1,438	<b>1,033</b>
Power per node [W]	14.0	12.04	10.8	10.9	<b>8.02</b>

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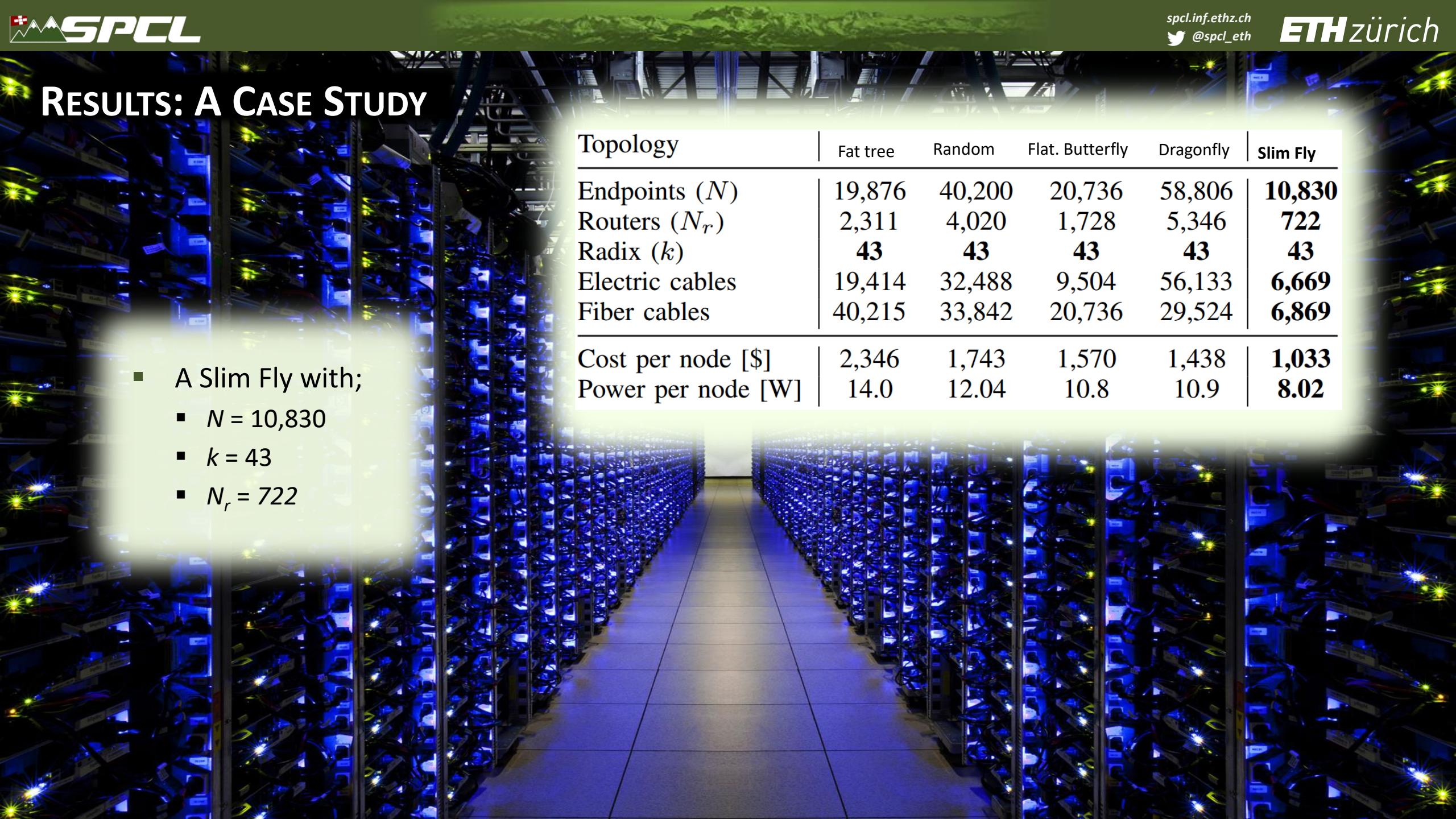
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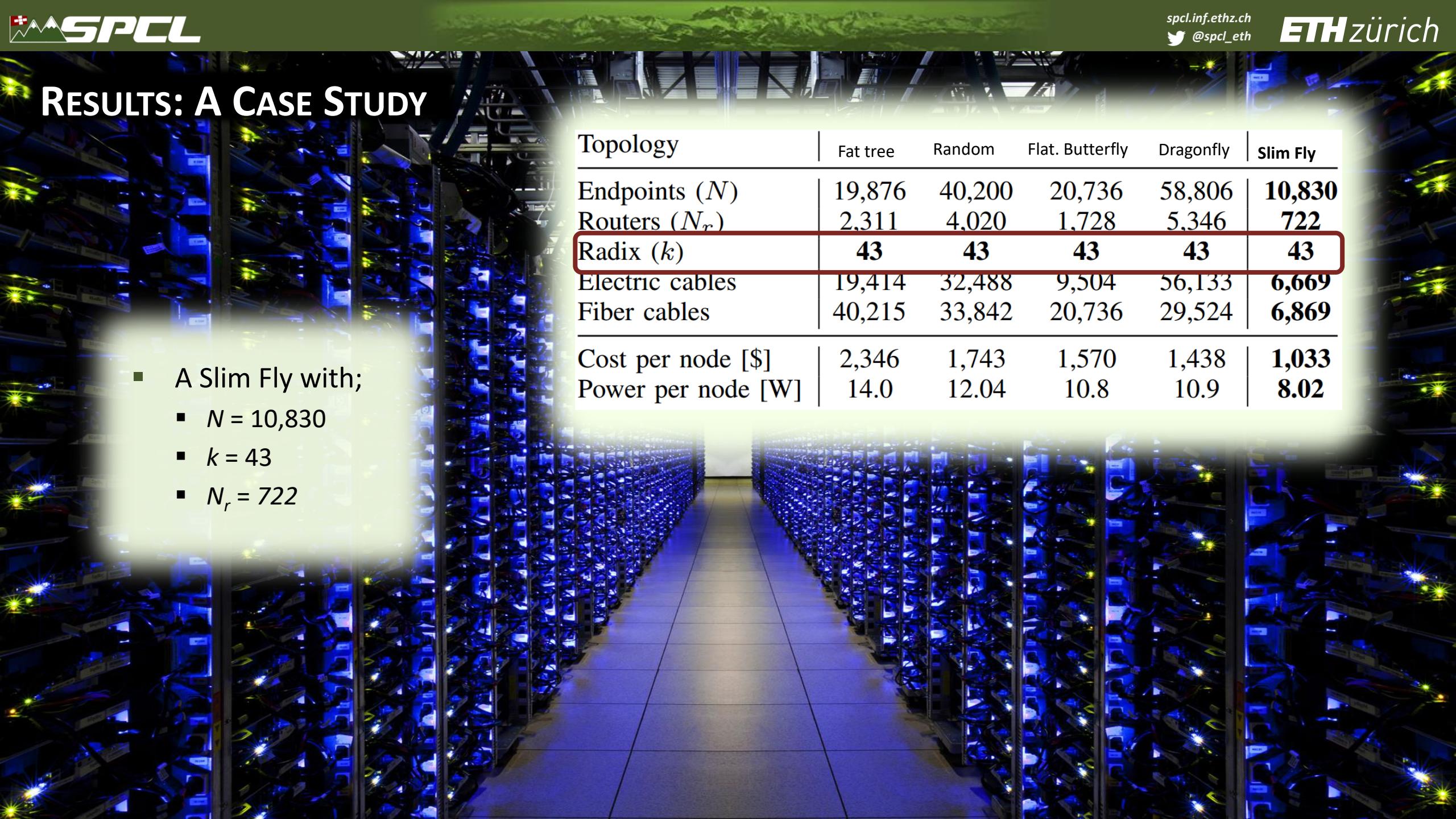
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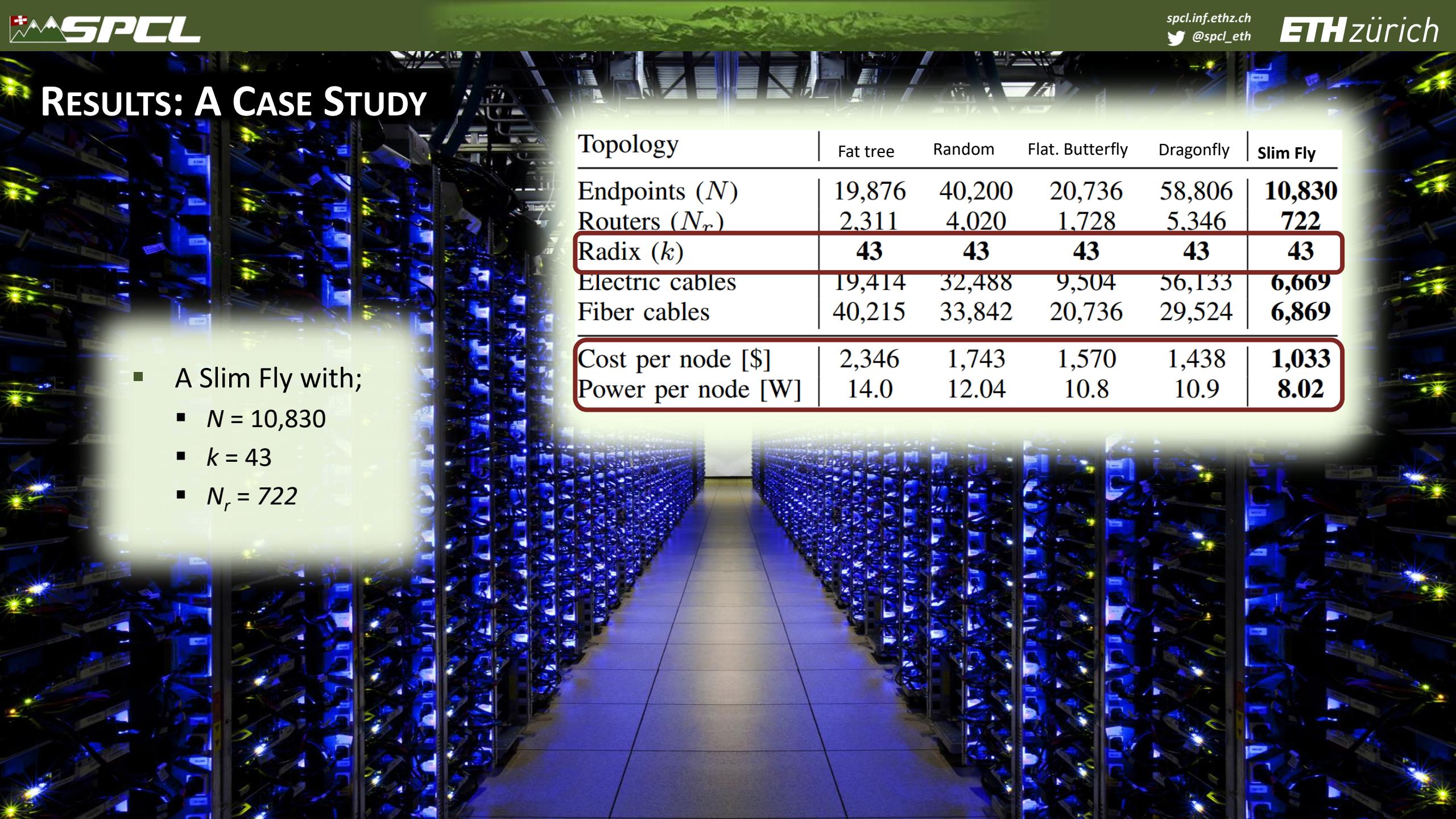
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  - $N = 10,830$
  - $k = 43$
  - $N_r = 722$

Topology	Fat tree	Random	Flat. Butterfly	Dragonfly	Slim Fly
Endpoints ( $N$ )	19,876	40,200	20,736	58,806	<b>10,830</b>
Routers ( $N_r$ )	2,311	4,020	1,728	5,346	<b>722</b>
Radix ( $k$ )	<b>43</b>	<b>43</b>	<b>43</b>	<b>43</b>	<b>43</b>
Electric cables	19,414	32,488	9,504	56,133	<b>6,669</b>
Fiber cables	40,215	33,842	20,736	29,524	<b>6,869</b>
Cost per node [\\$]	2,346	1,743	1,570	1,438	<b>1,033</b>
Power per node [W]	14.0	12.04	10.8	10.9	<b>8.02</b>

Topology					
Endpoints ( $N$ )	<b>10,718</b>	<b>9,702</b>	<b>10,000</b>	<b>9,702</b>	<b>10,830</b>
Routers ( $N_r$ )	1,531	1,386	1,000	1,386	<b>722</b>
Radix ( $k$ )	35	28	33	27	<b>43</b>
Electric cables	7,350	6,837	4,500	9,009	<b>6,669</b>
Fiber cables	24,806	7,716	10,000	4,900	<b>6,869</b>
Cost per node [\\$]	2,315	1,566	1,535	1,342	<b>1,033</b>
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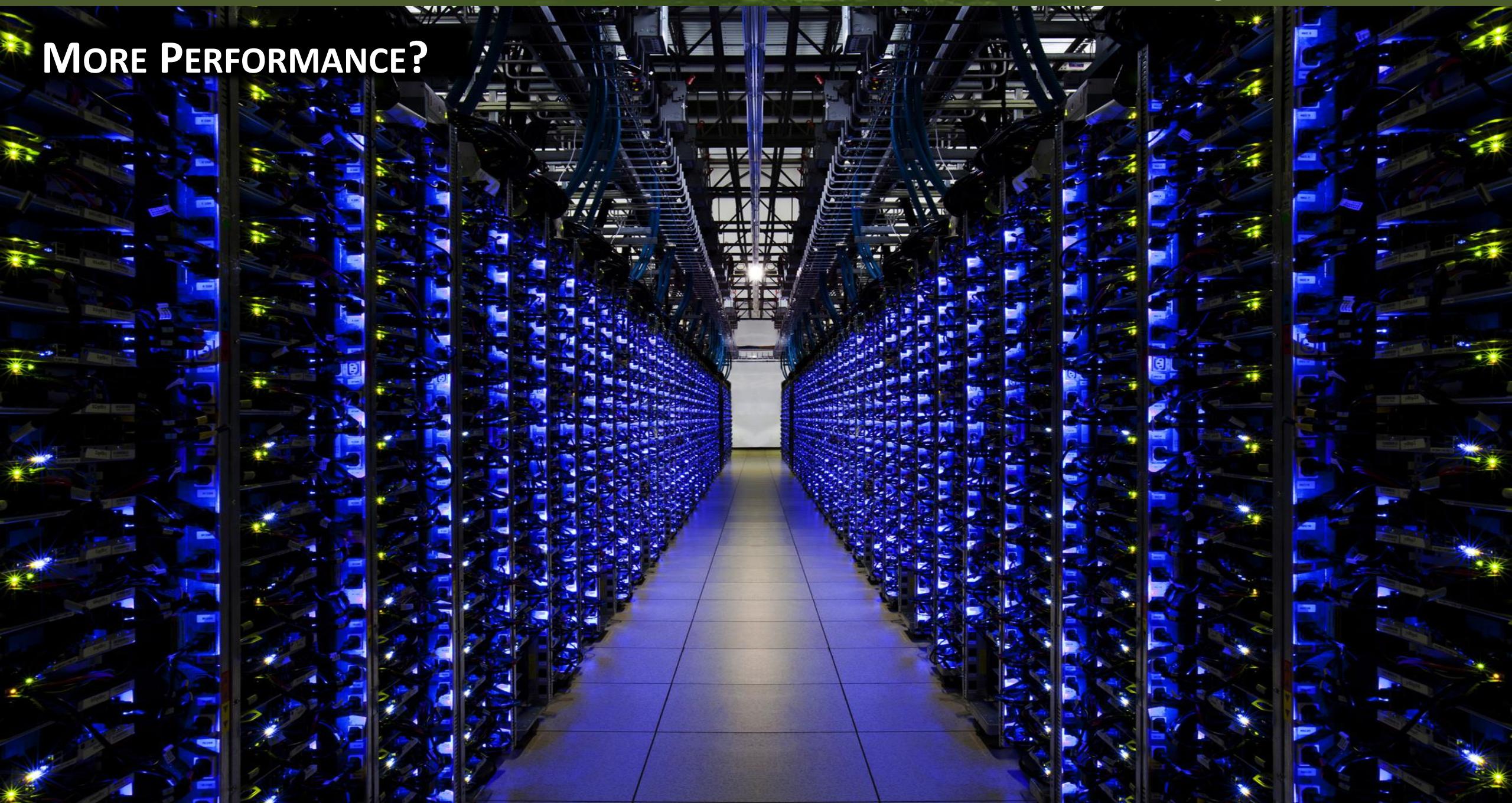
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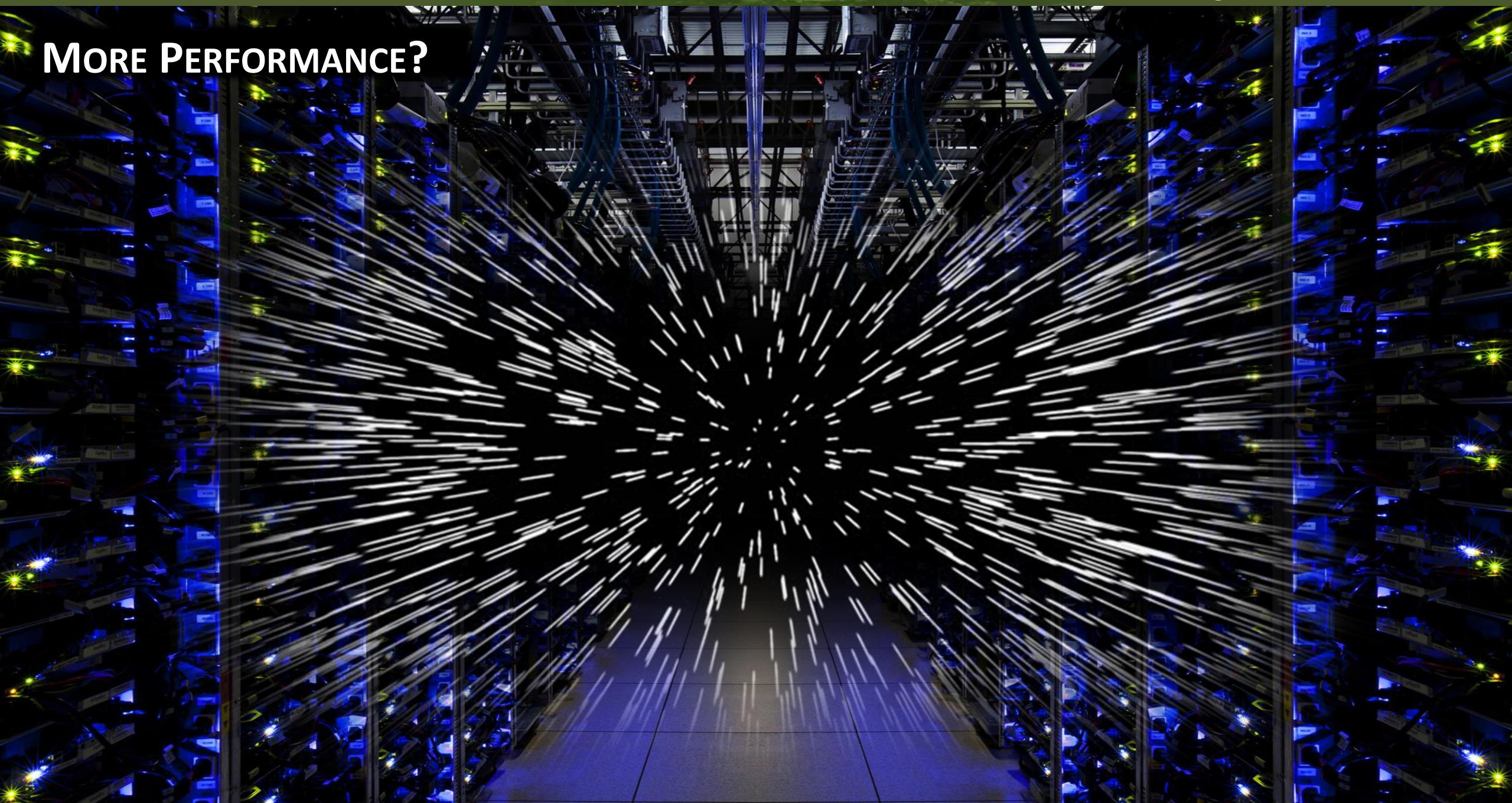
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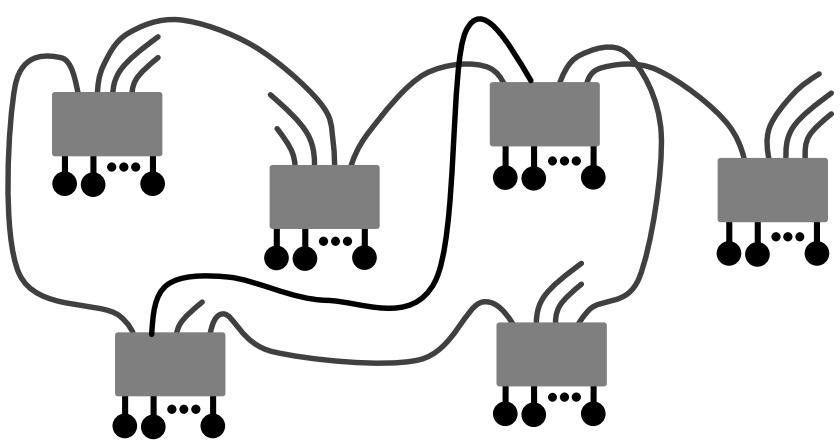
# MORE PERFORMANCE?



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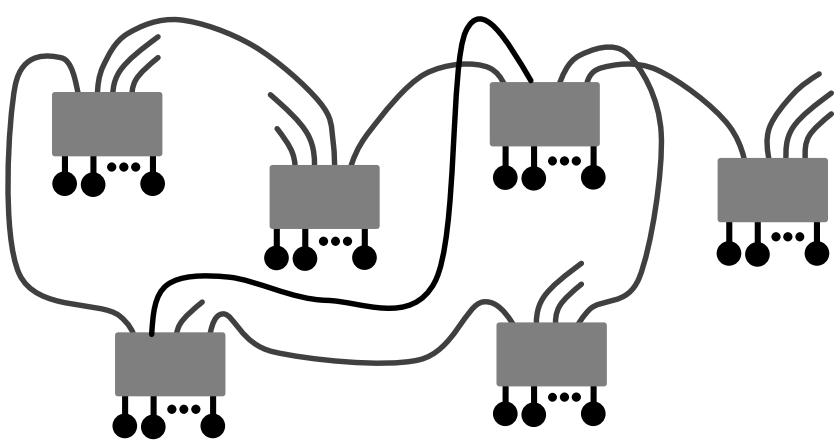


# MULTI PATHING?



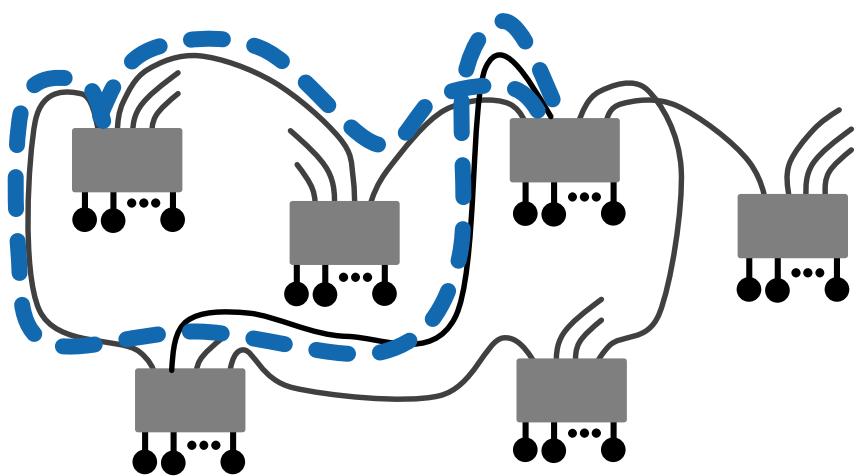
# MULTI PATHING?

Minimal paths?



# MULTI PATHING?

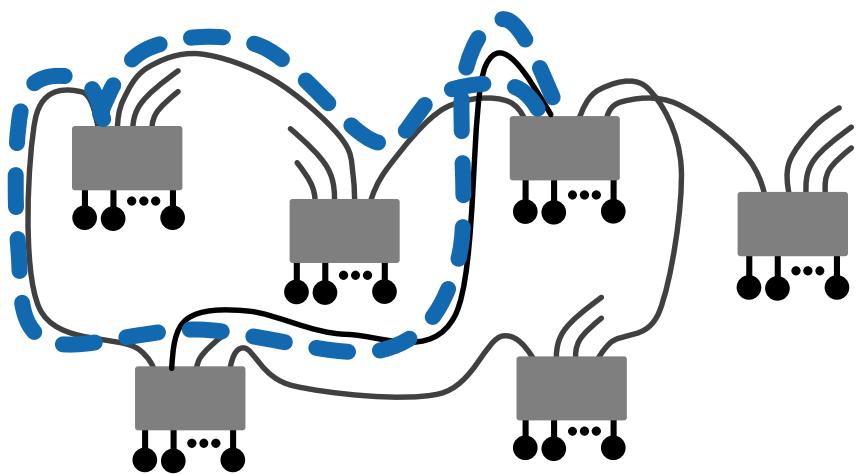
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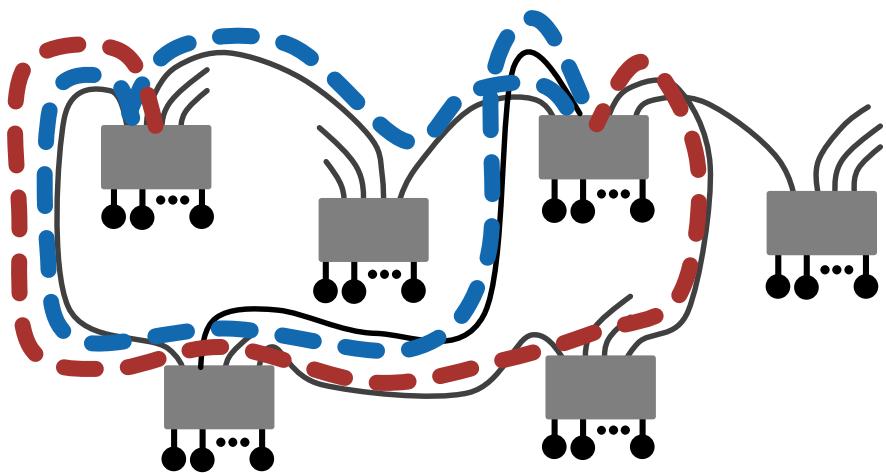
Non-minimal paths?



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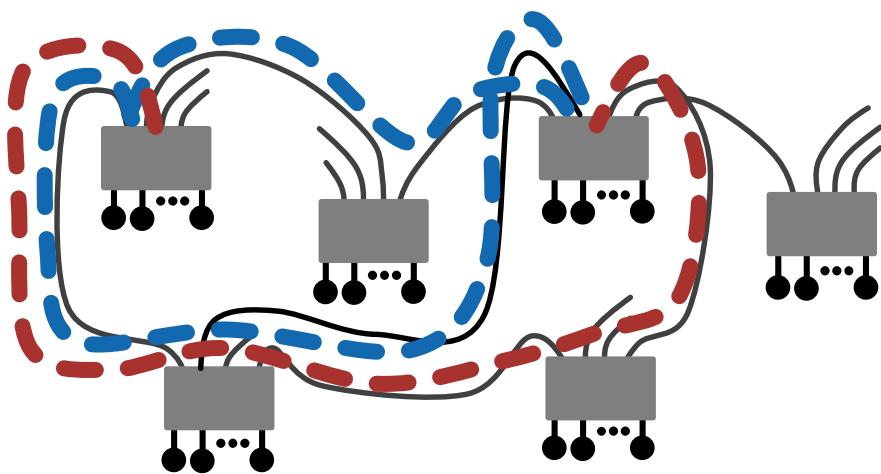
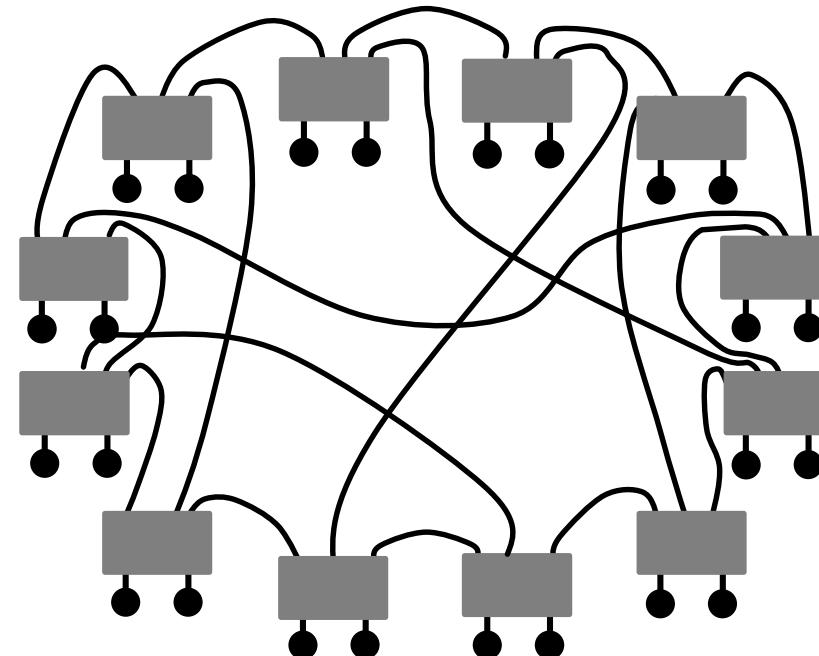
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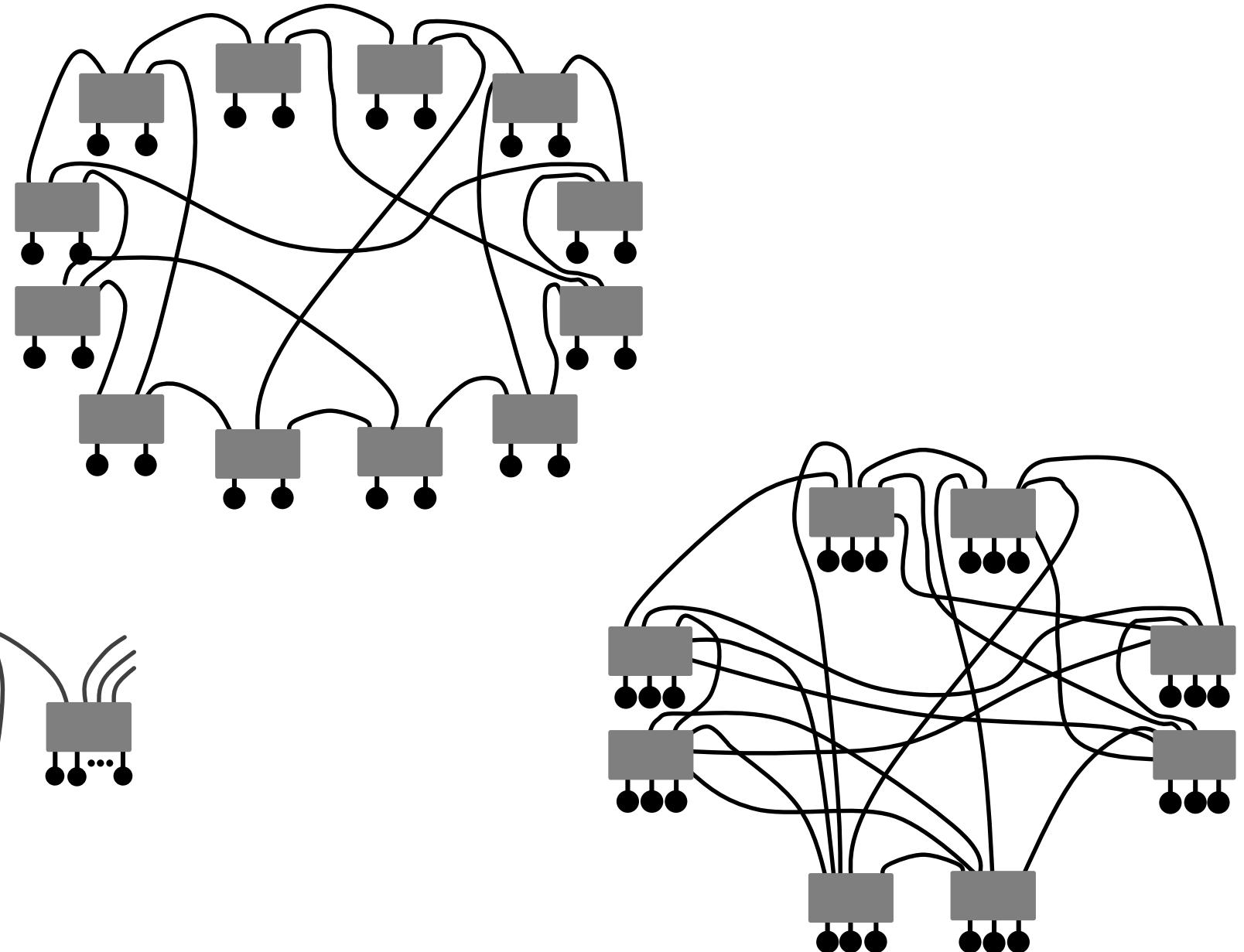
Non-minimal paths?



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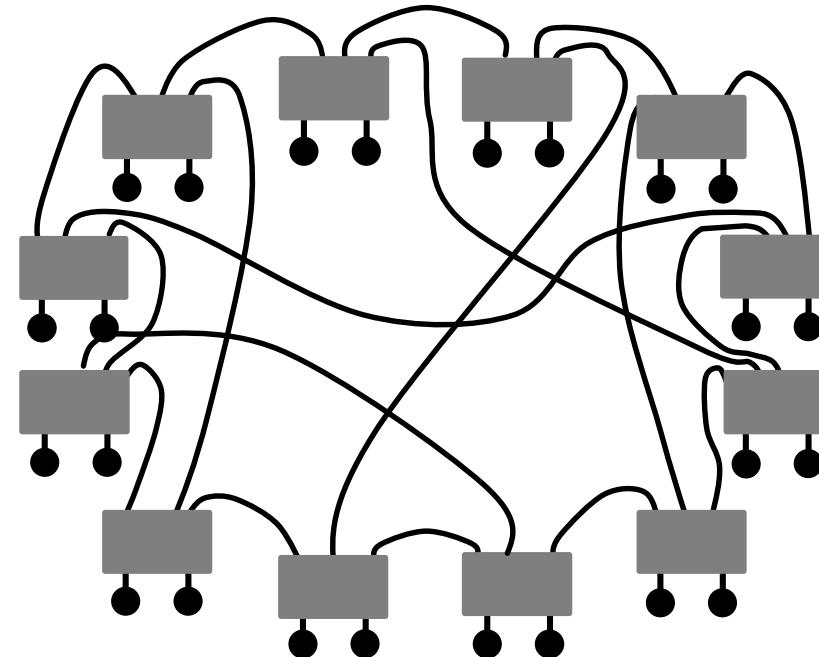
Non-minimal paths?



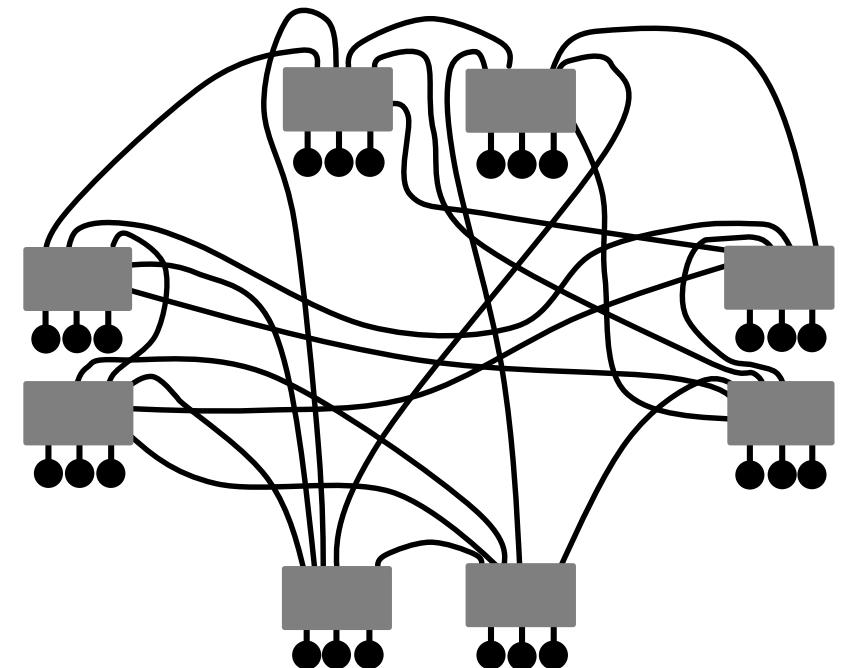
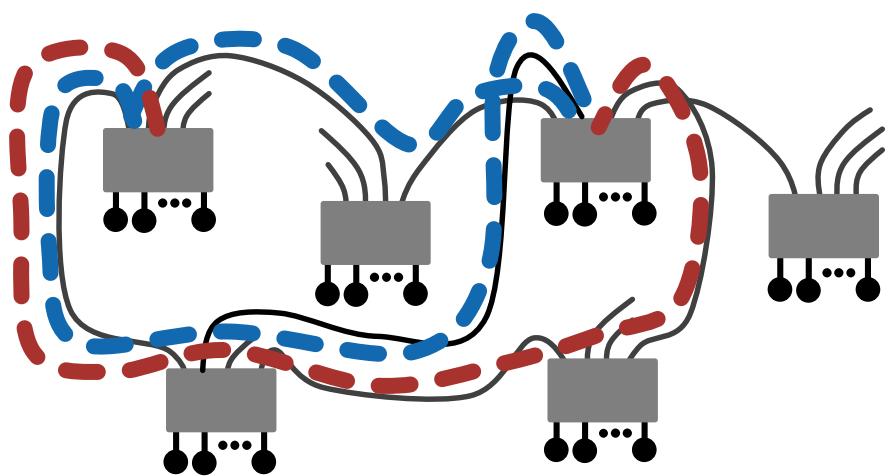
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Non-minimal paths?



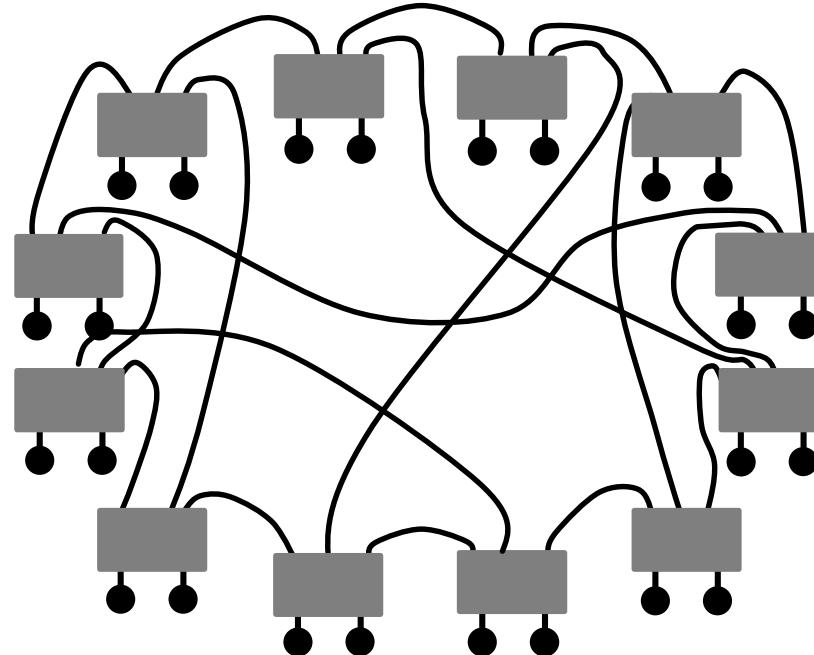
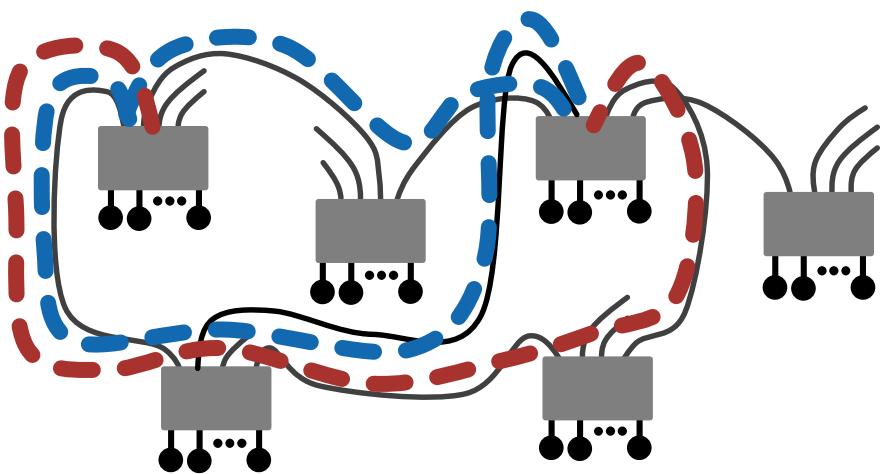
Expanders  
are sparse...



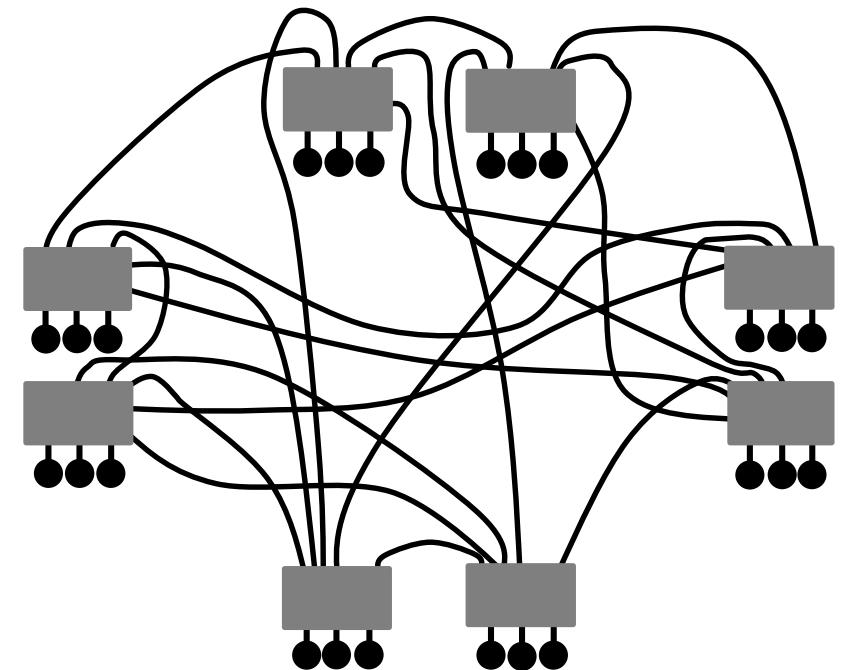
# MULTI PATHING?

Minimal paths?

Non-minimal paths?



Is there  
enough  
path  
diversity?

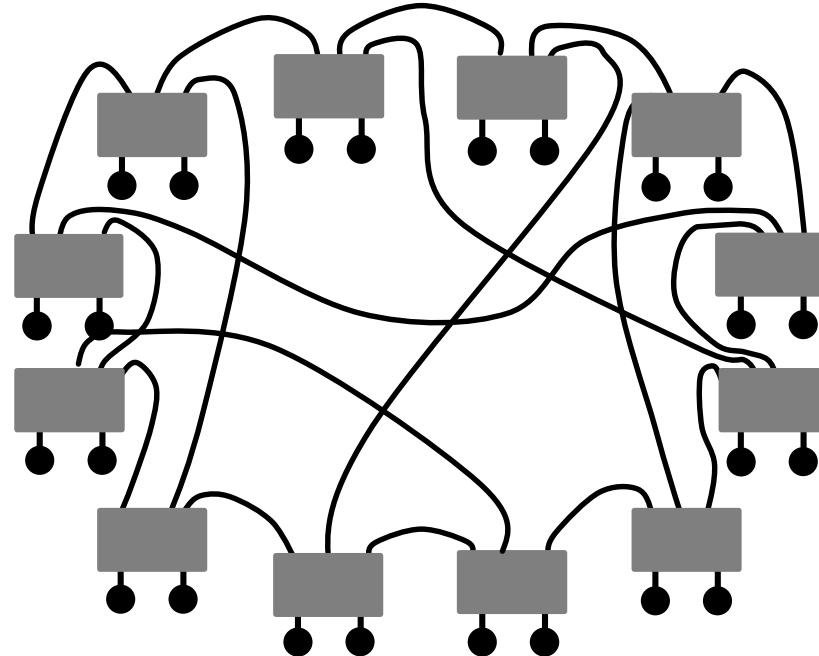
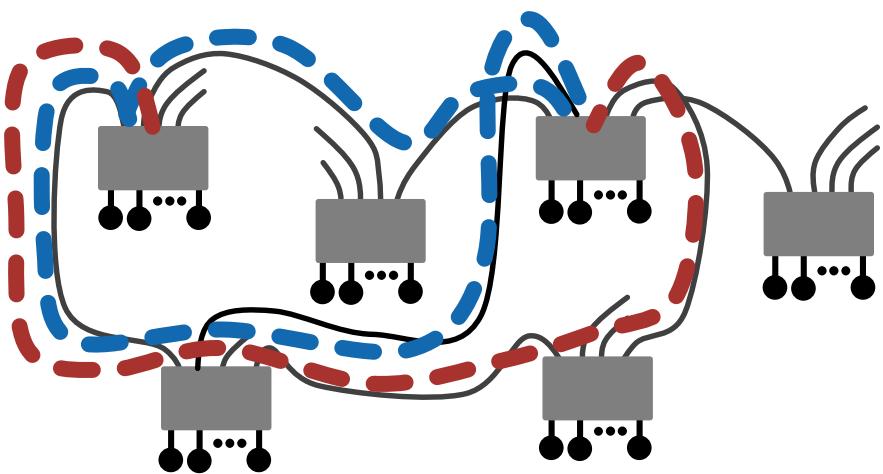


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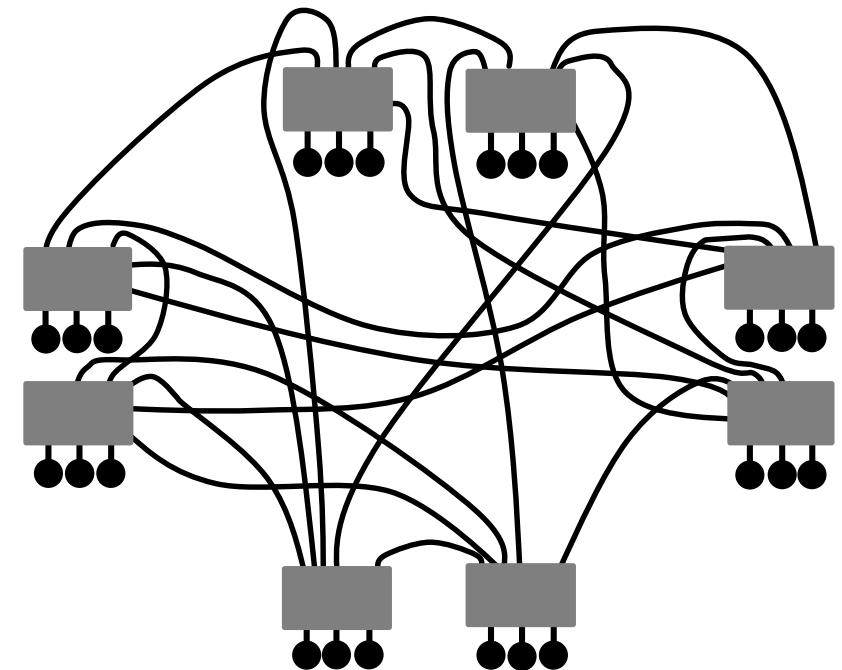
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Non-minimal paths?



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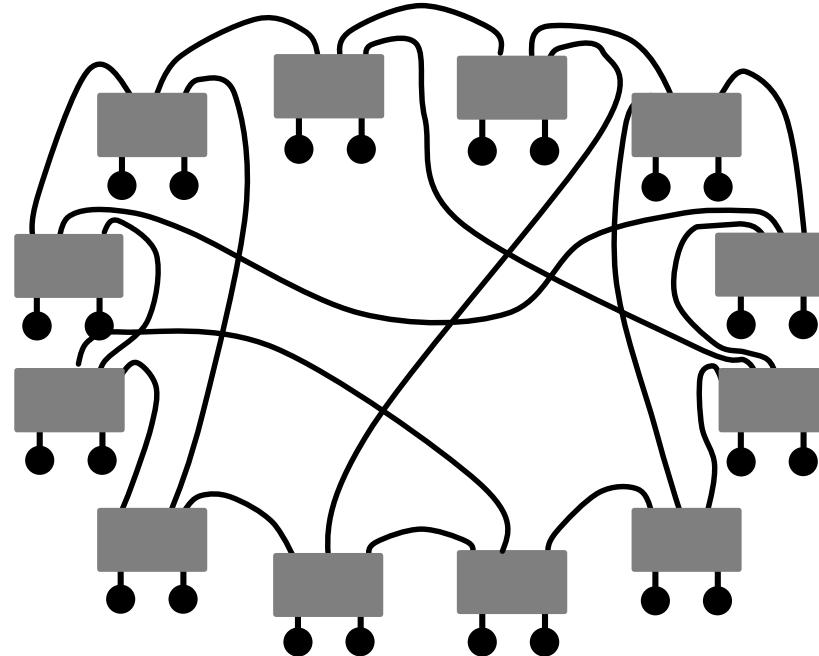
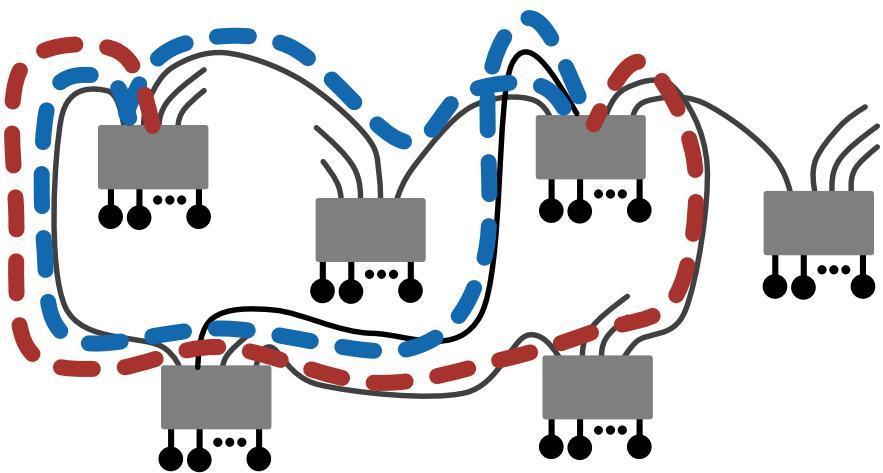


Expanders  
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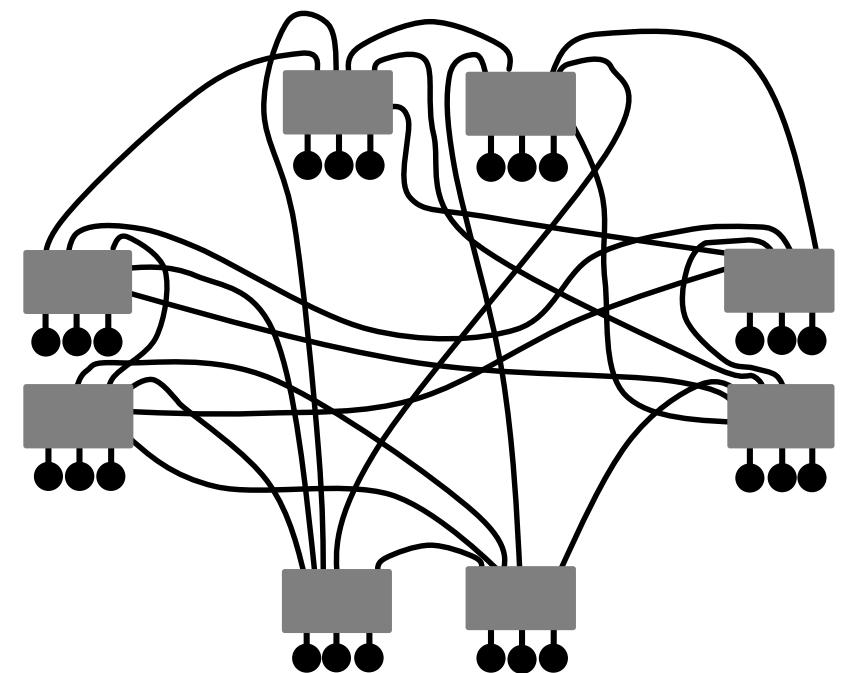
Non-minimal paths?



# HOW TO USE MULTIPATHING?

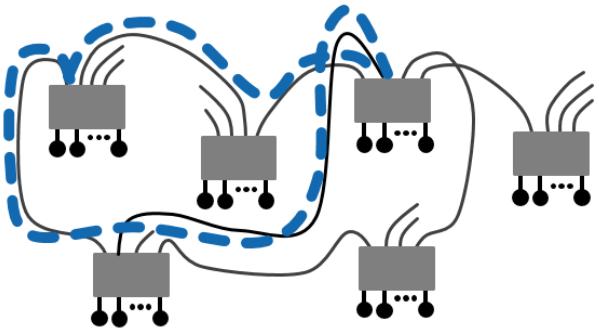
Expanders  
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# IS THERE ENOUGH PATH DIVERSITY?

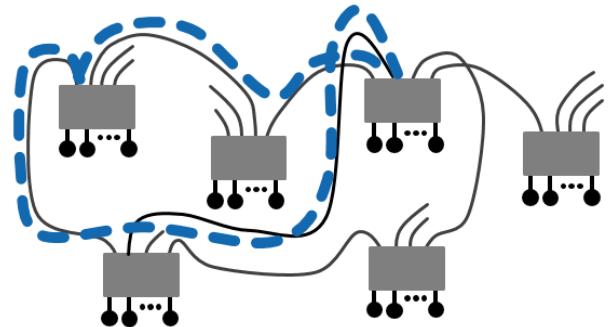
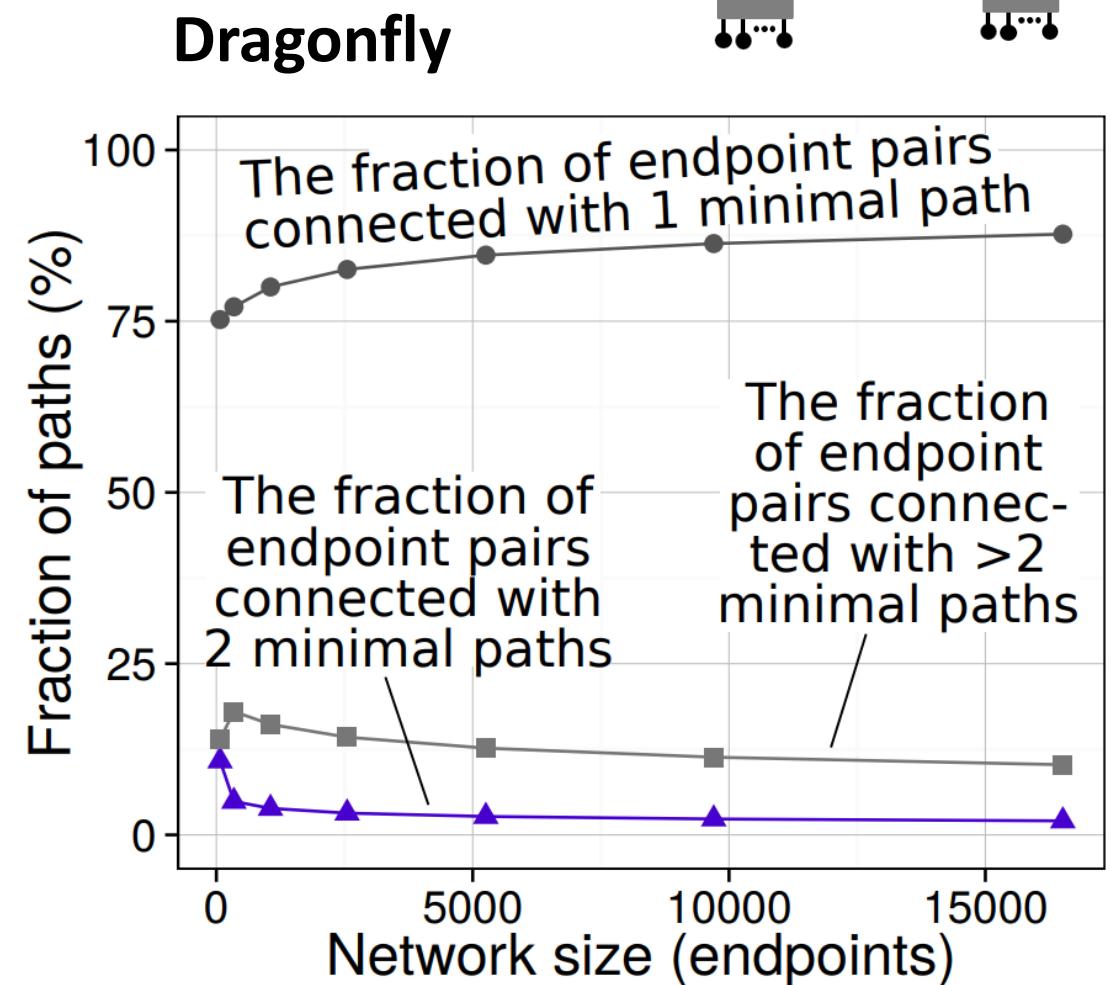
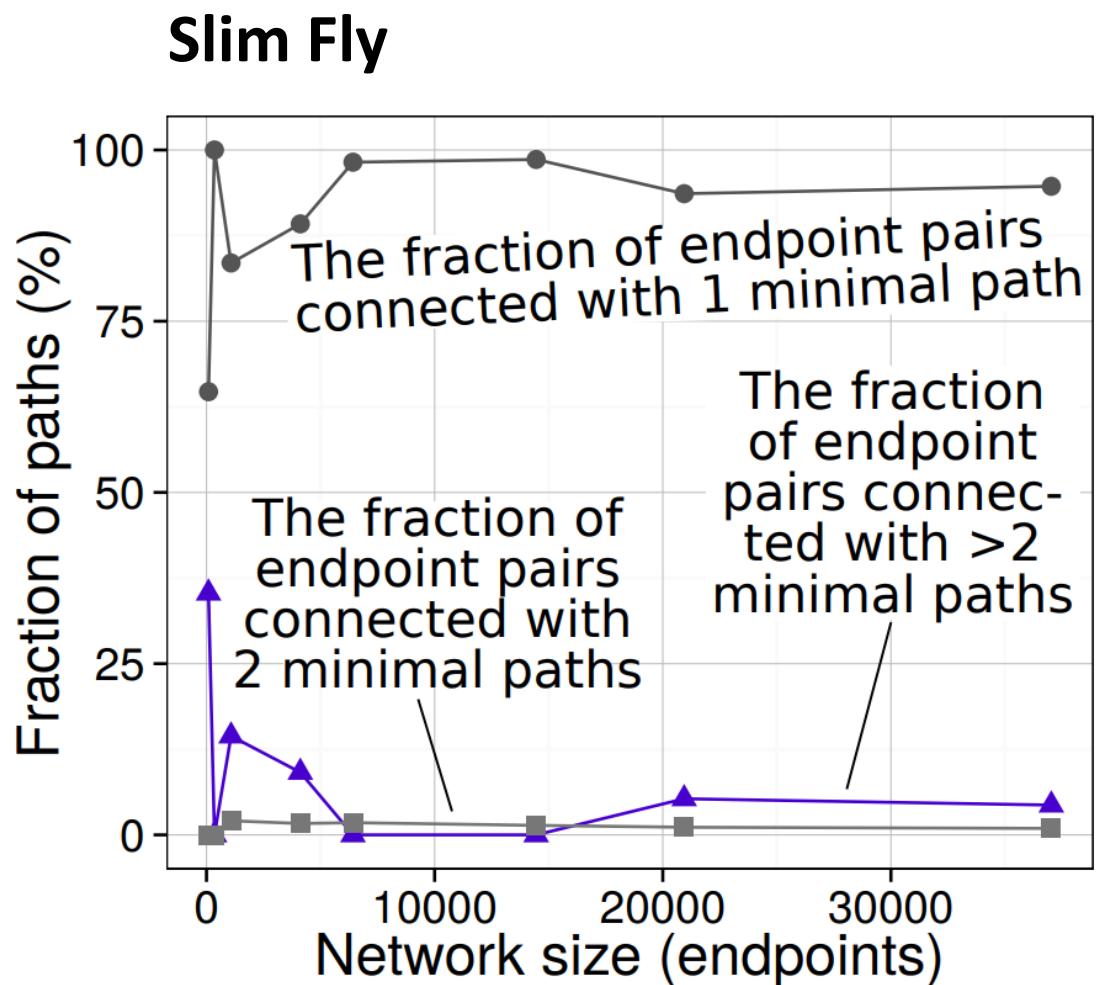


# RESULTS: MINIMAL PATHS

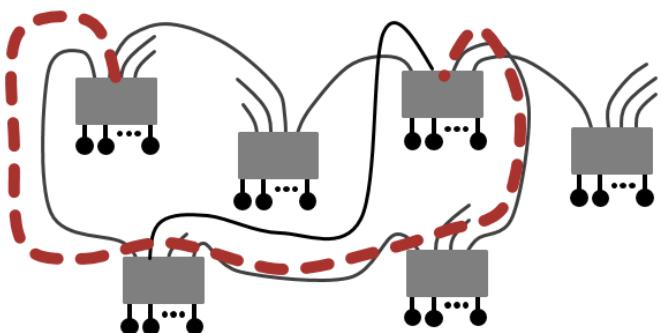
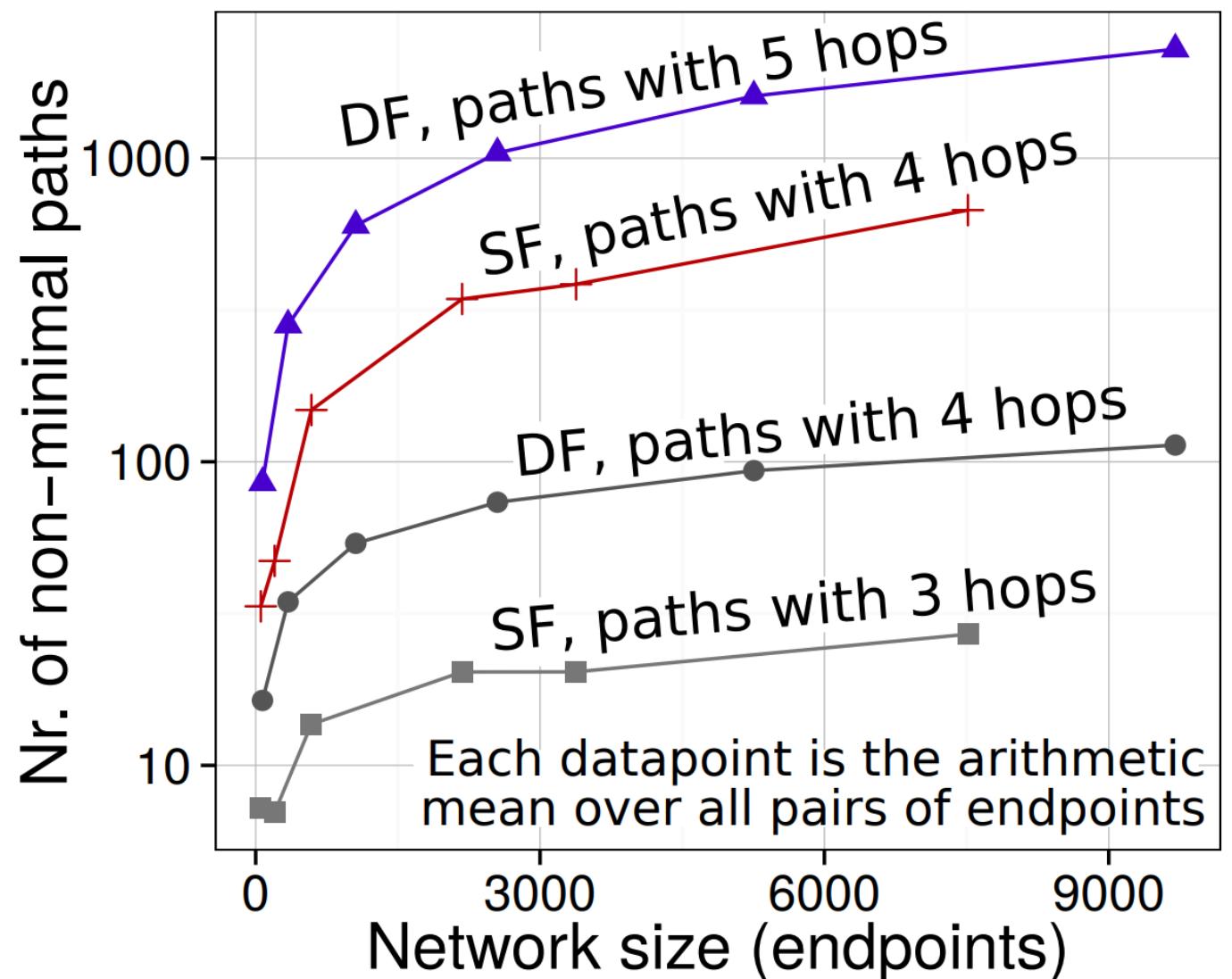
**Slim Fly**



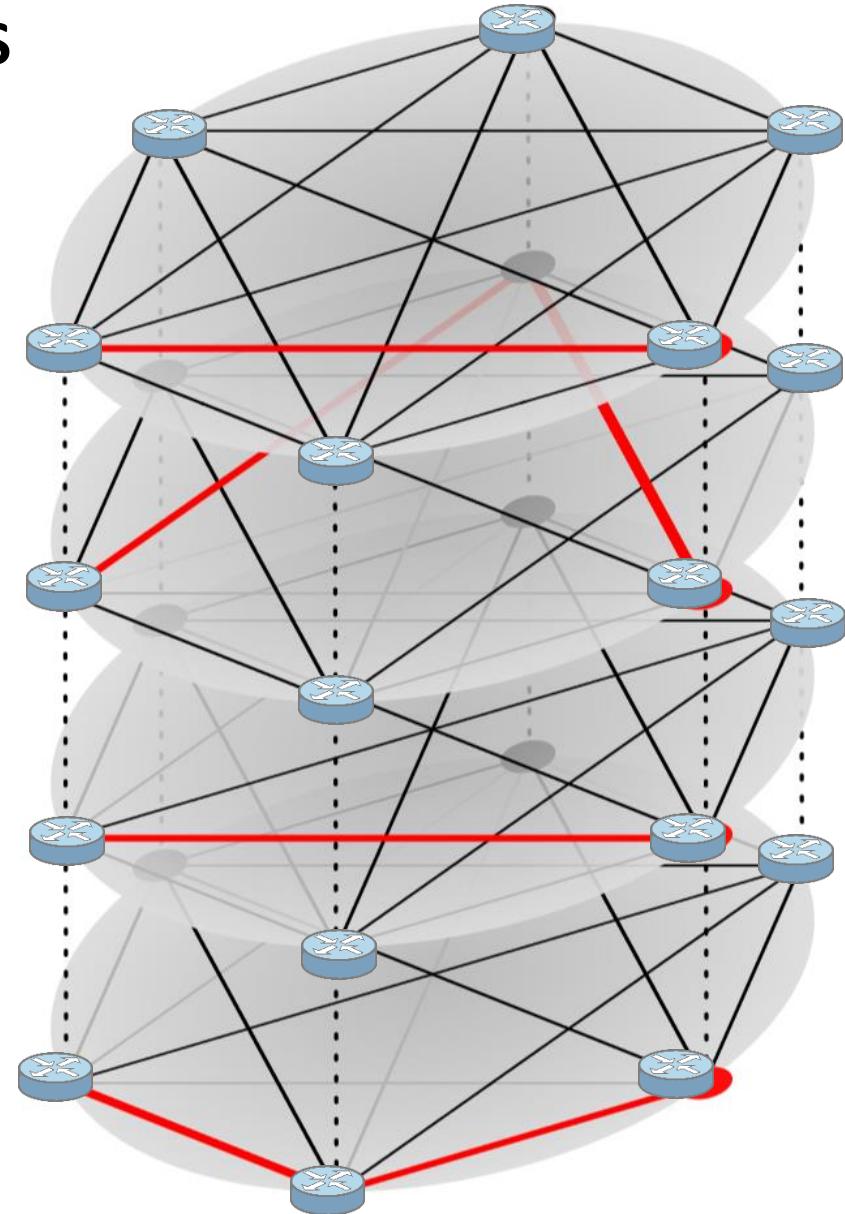
## RESULTS: MINIMAL PATHS



## RESULTS: NON-MINIMAL PATHS

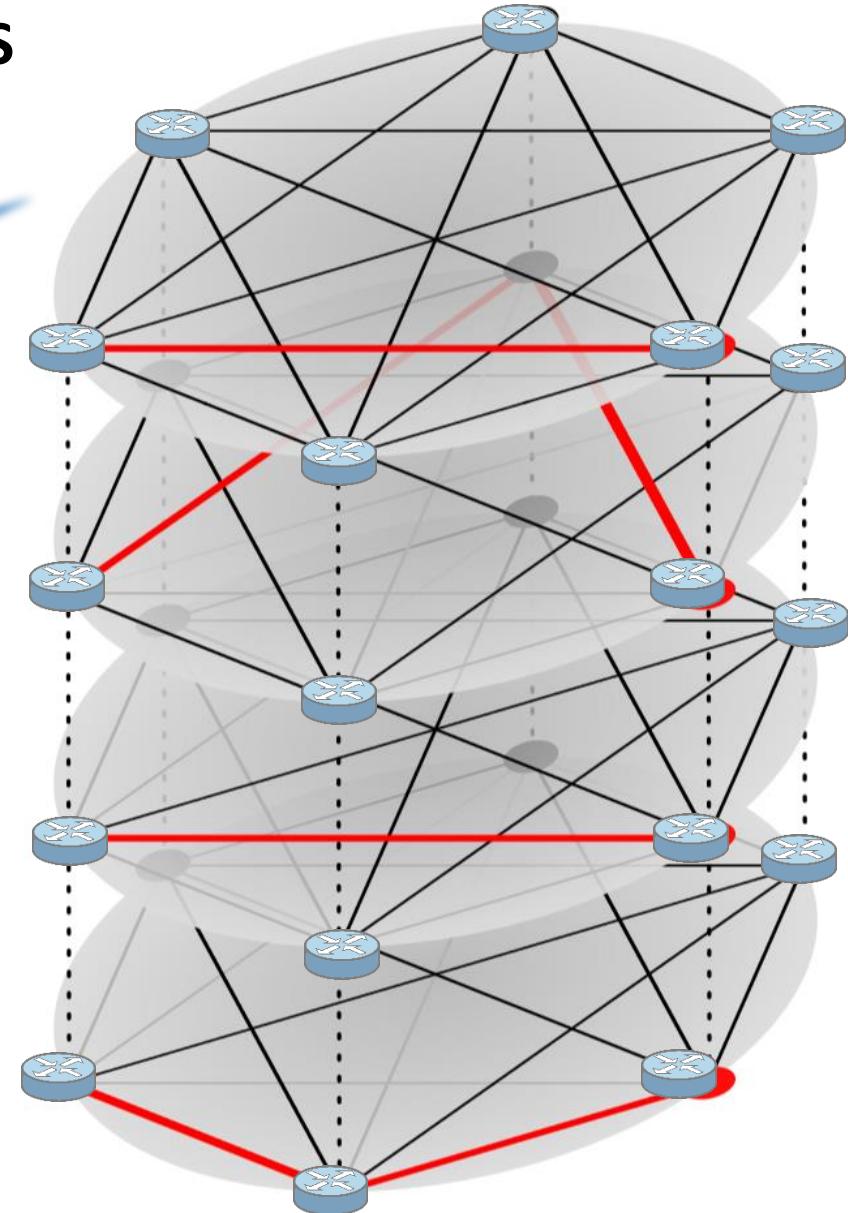


# DEPLOYMENT: LAYERS OF “ALMOST” MINIMAL PATHS



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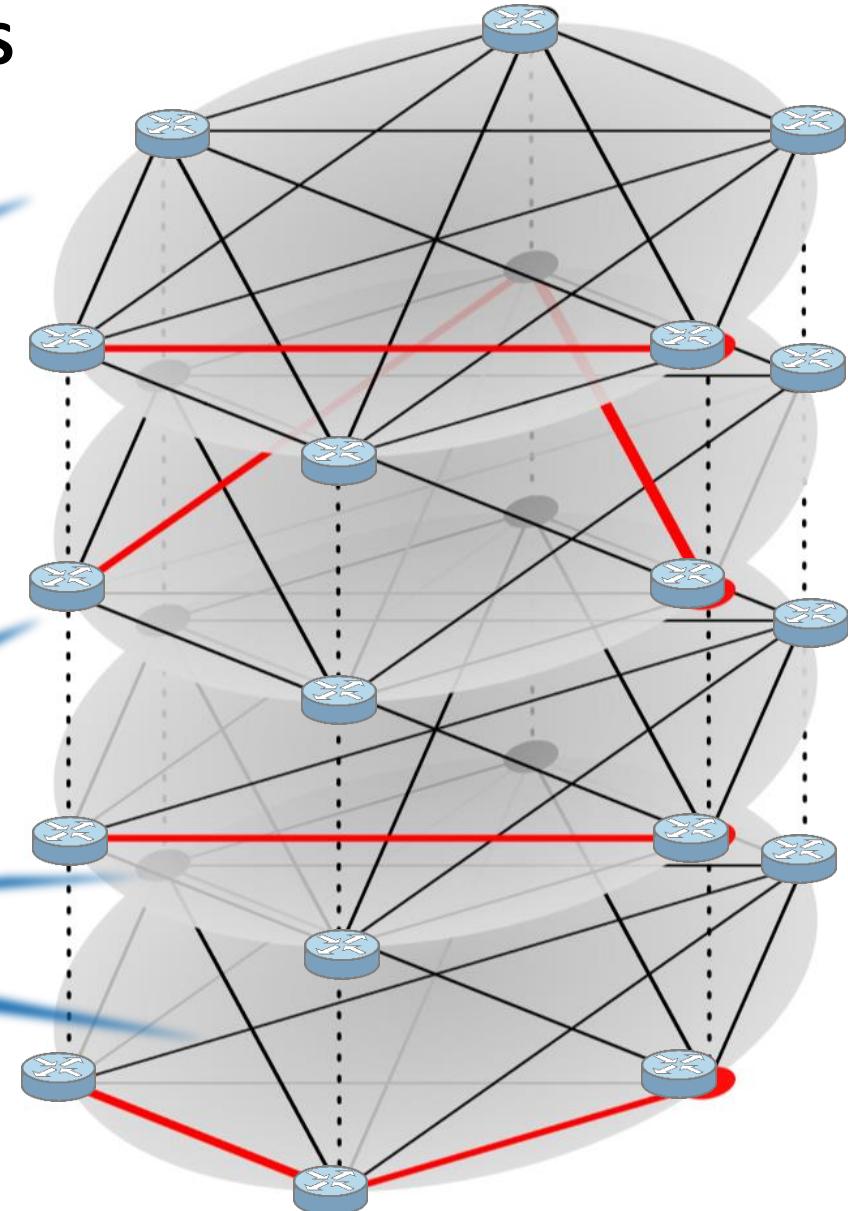
Layer 1: include all links and route minimally



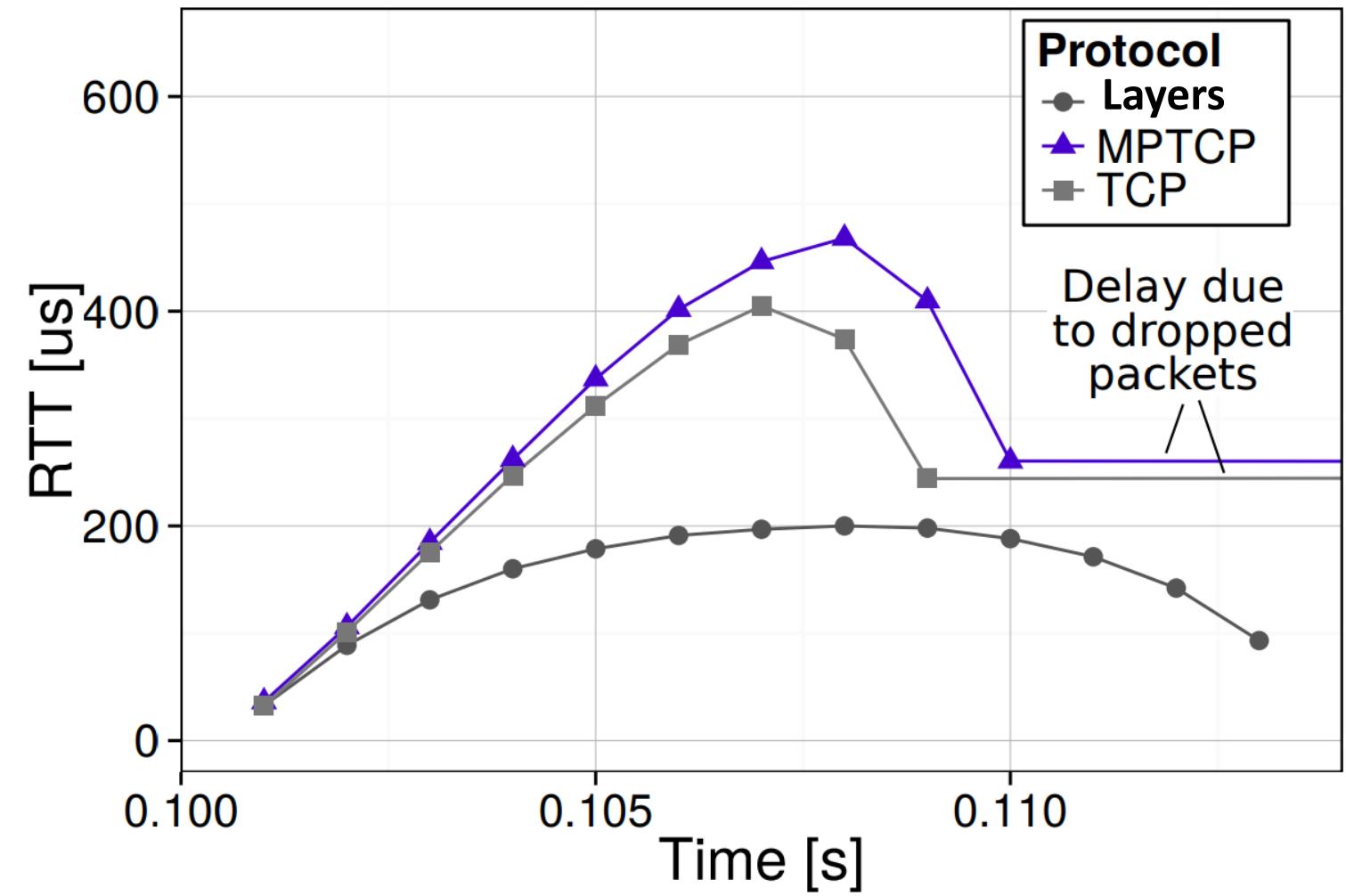
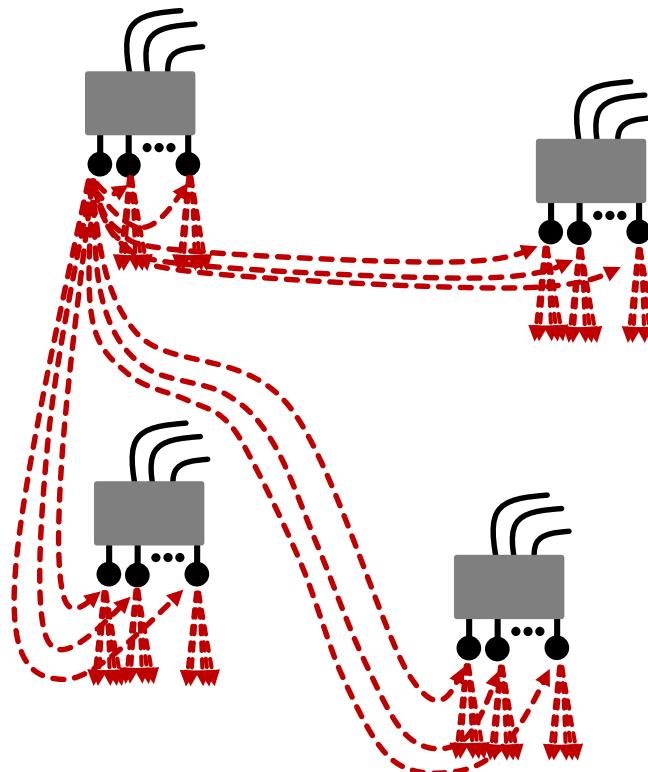
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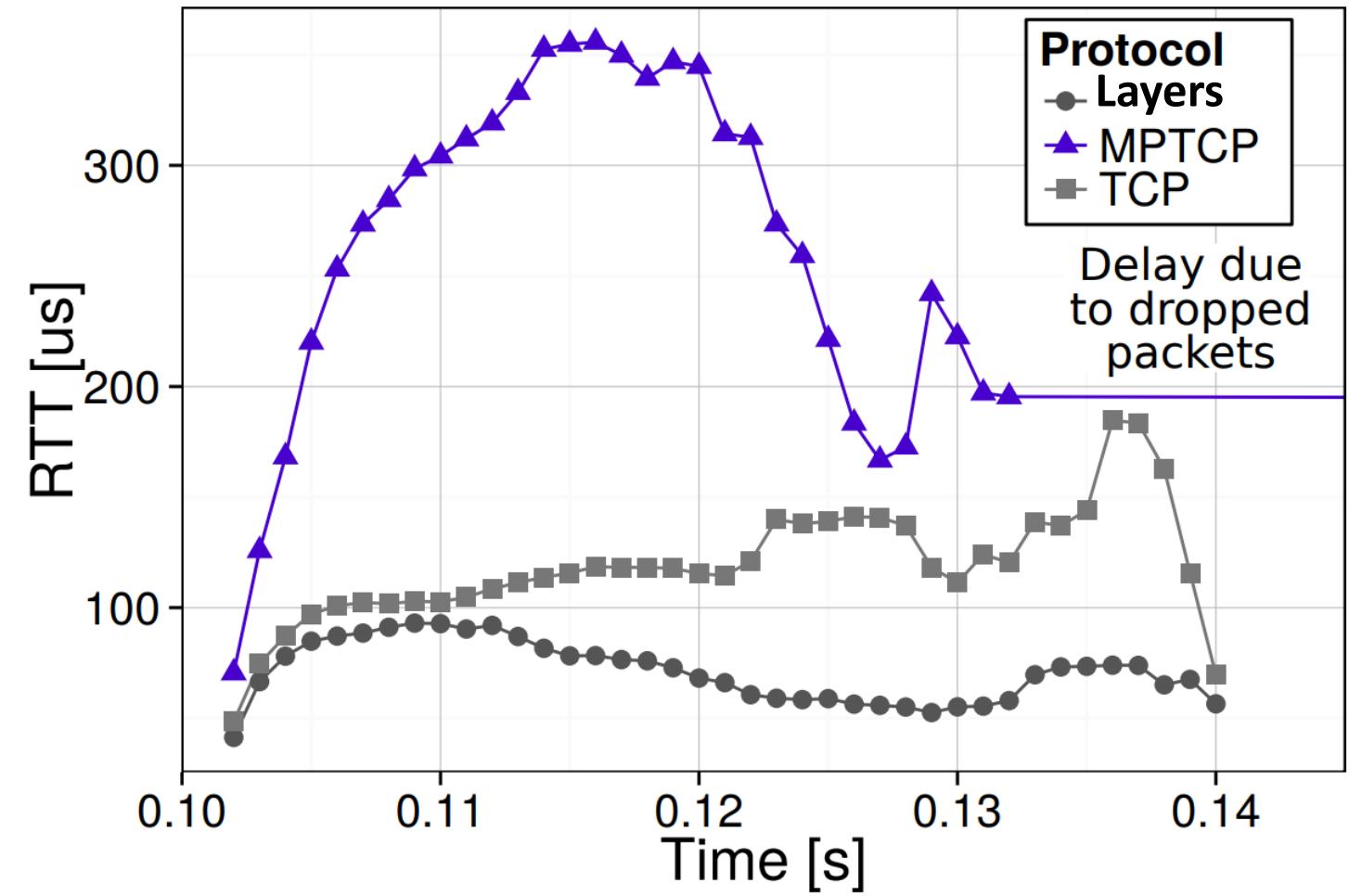
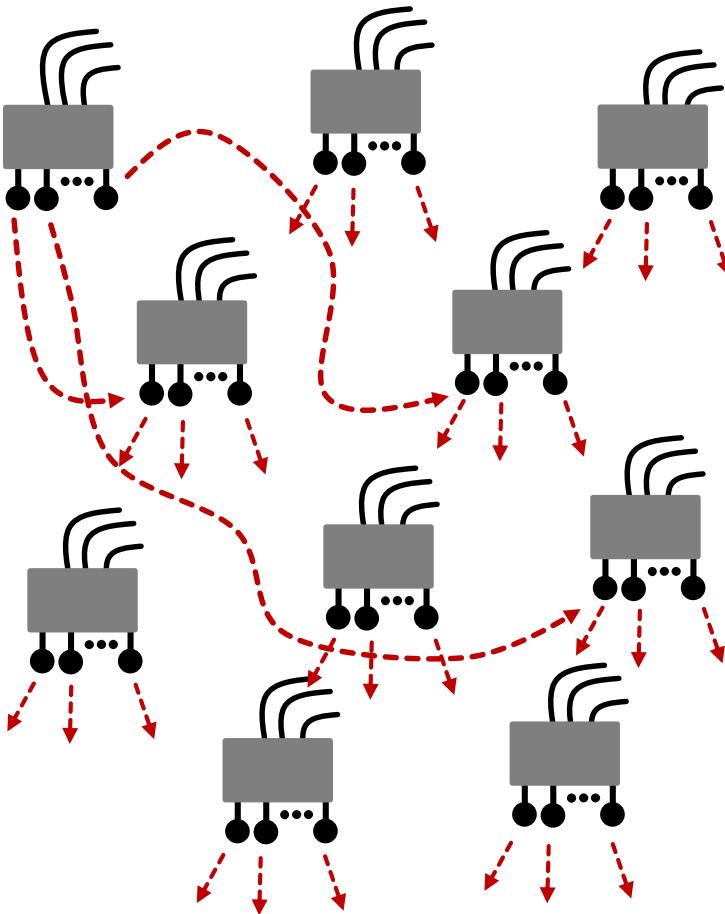
Layers 2-n: remove some links (e.g., select uniformly at random) and route minimally



# RESULTS: ALL-TO-ALL

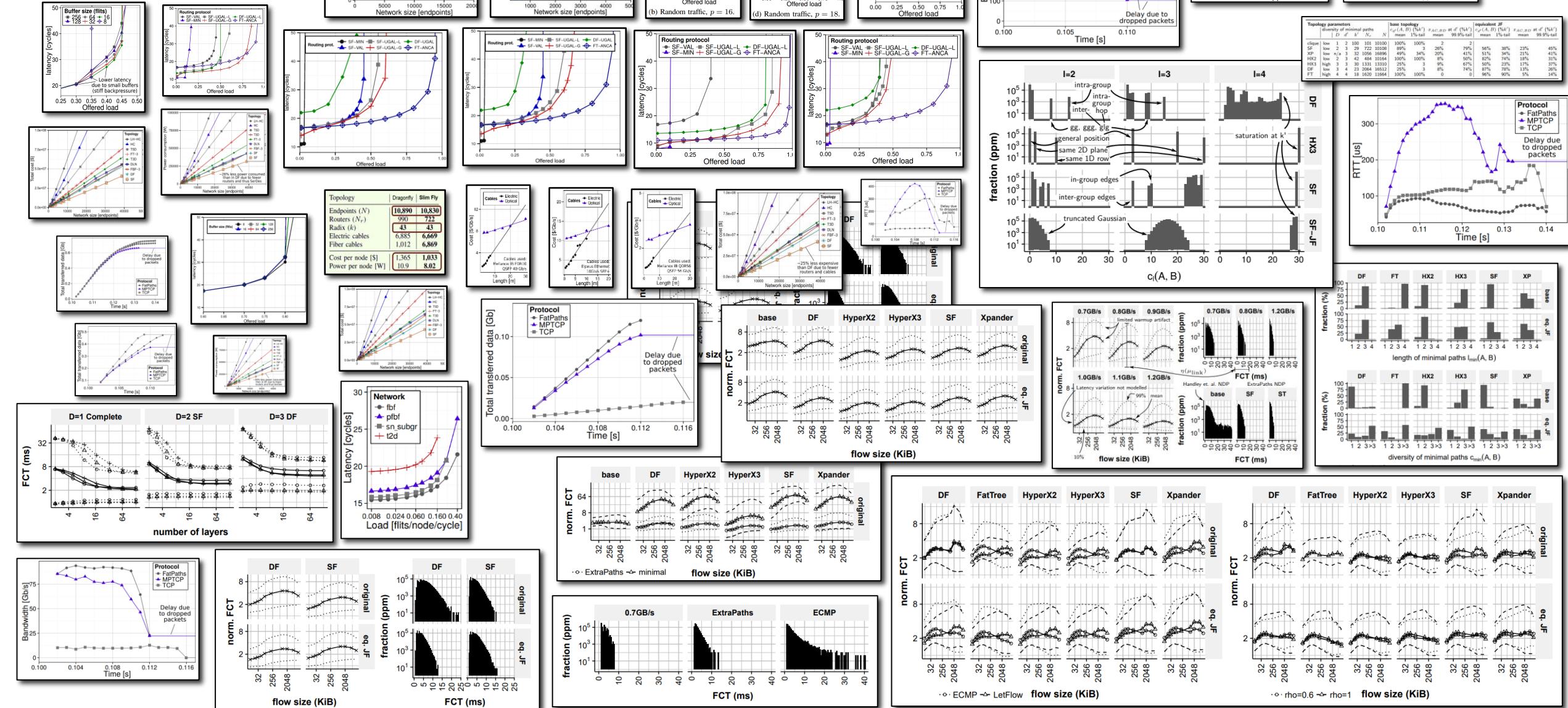


# RESULTS: RANDOM UNIFORM

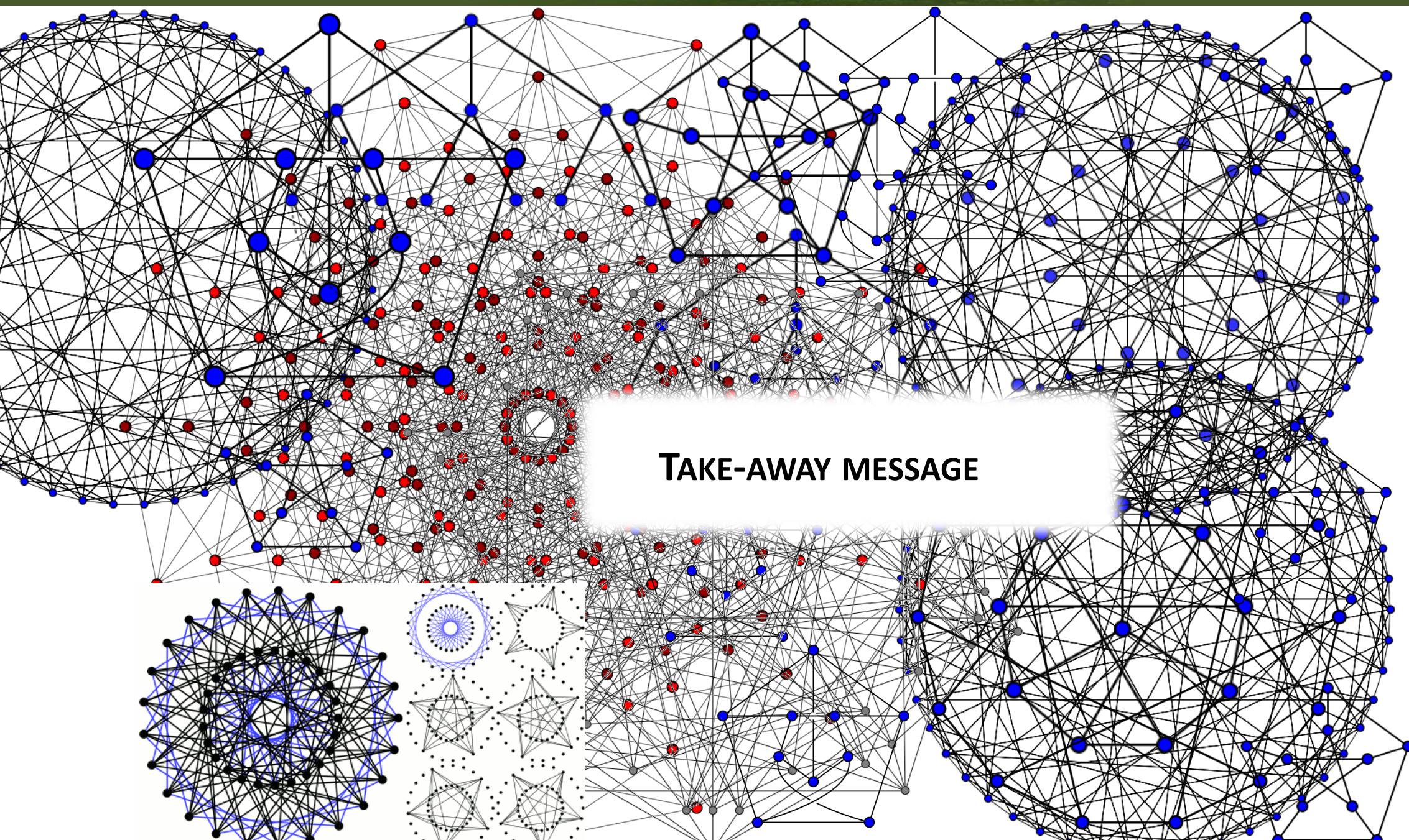


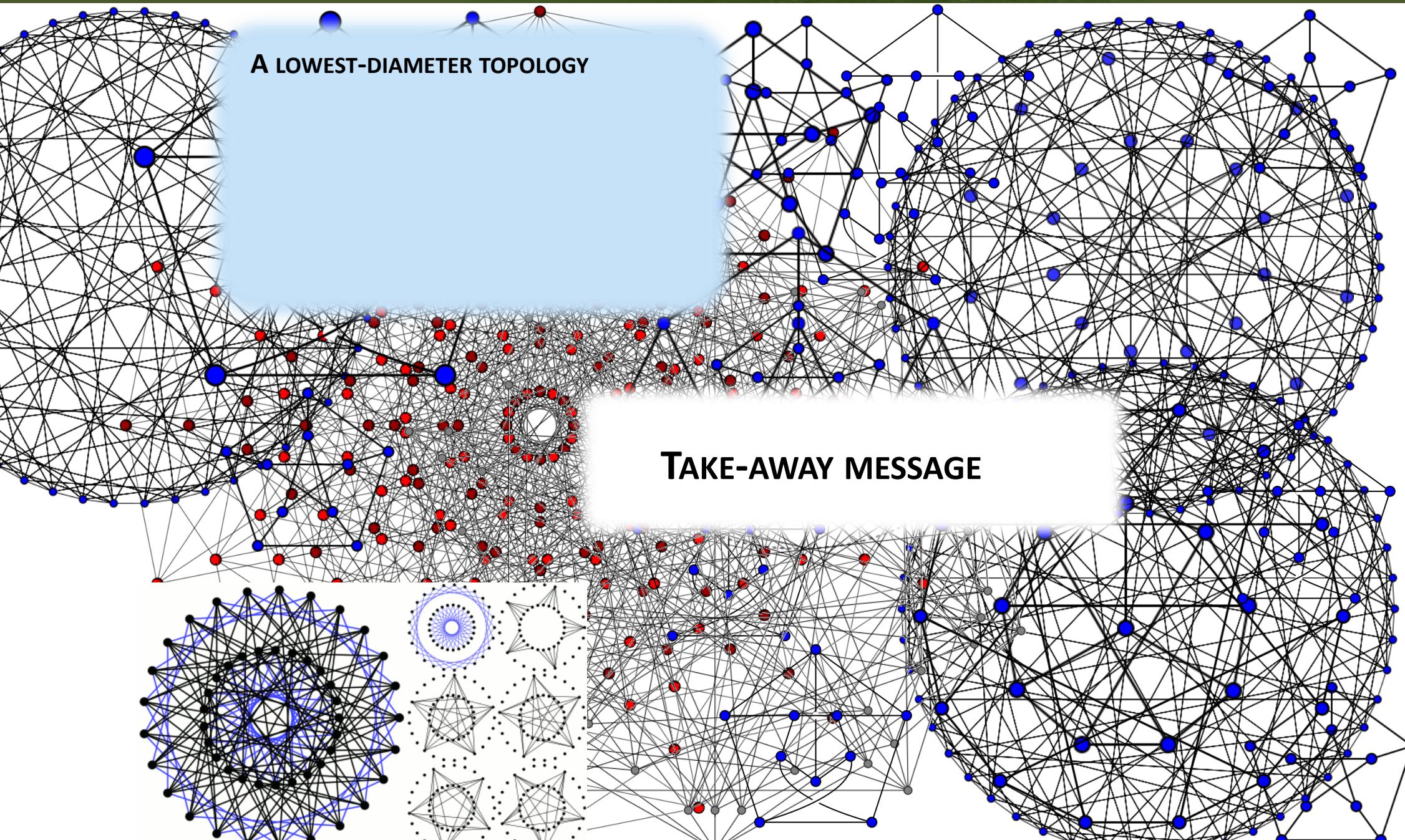
# OTHER RESULTS

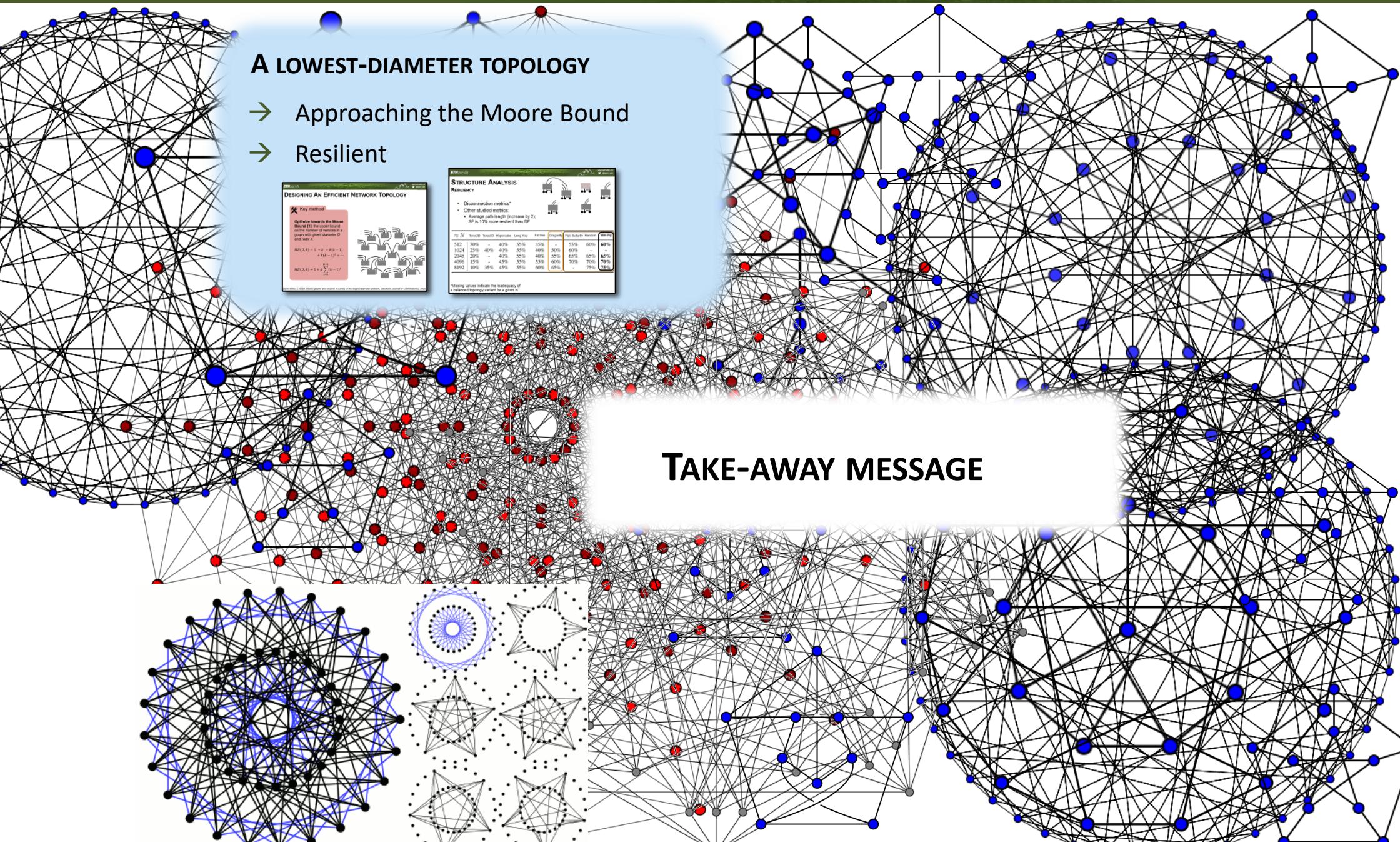
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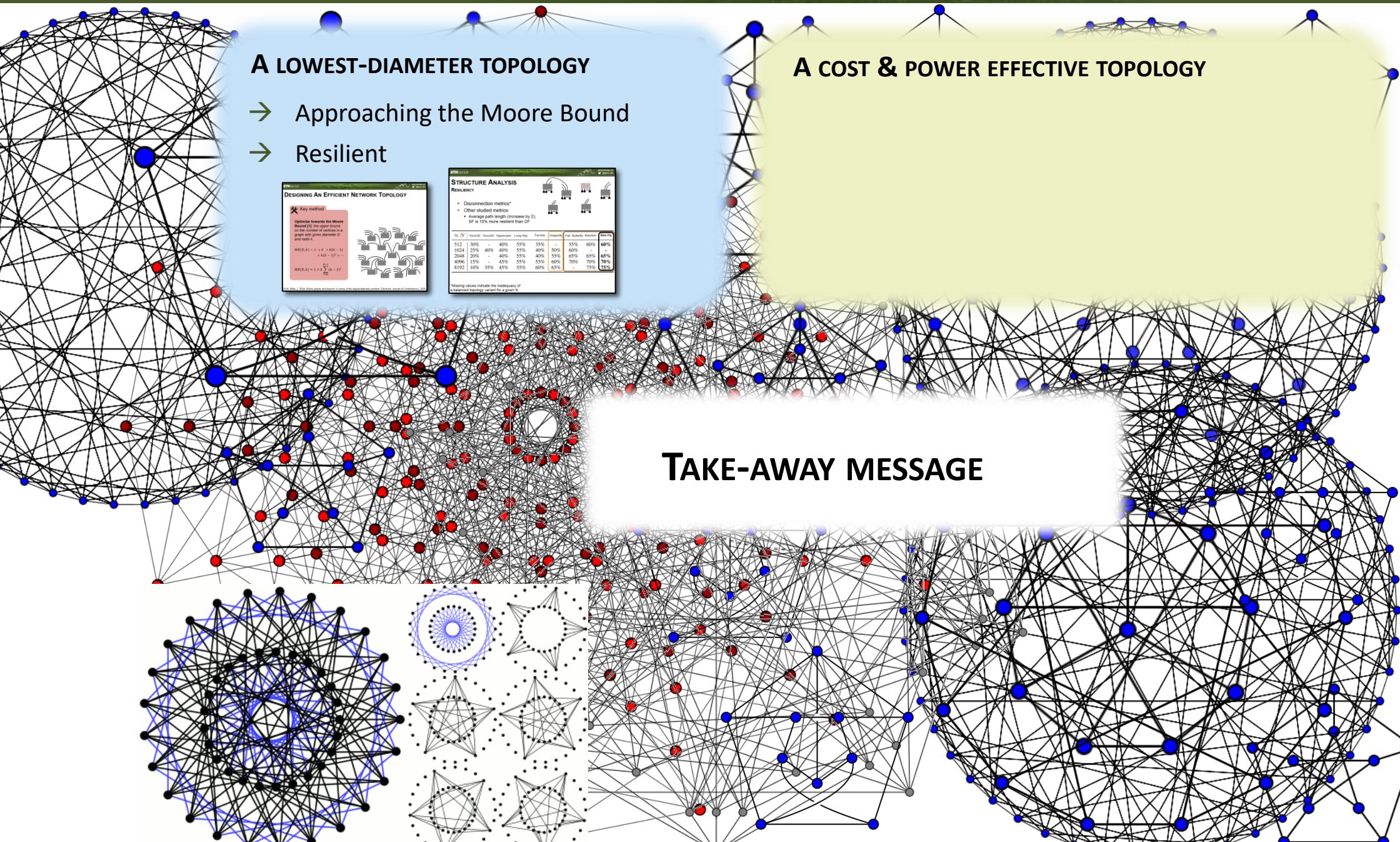






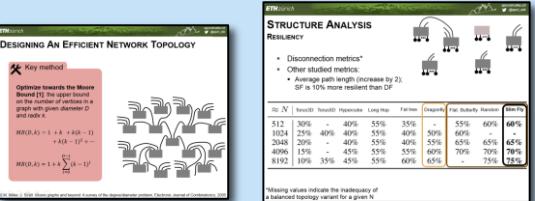






## A LOWEST-DIAMETER TOPOLOGY

- Approaching the Moore Bound
- Resilient

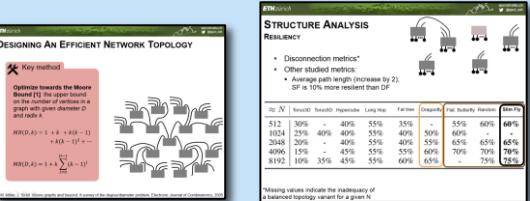


## A COST & POWER EFFECTIVE TOPOLOGY

## TAKE-AWAY MESSAGE

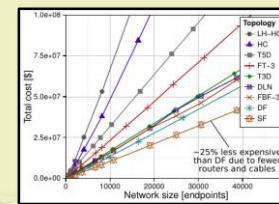
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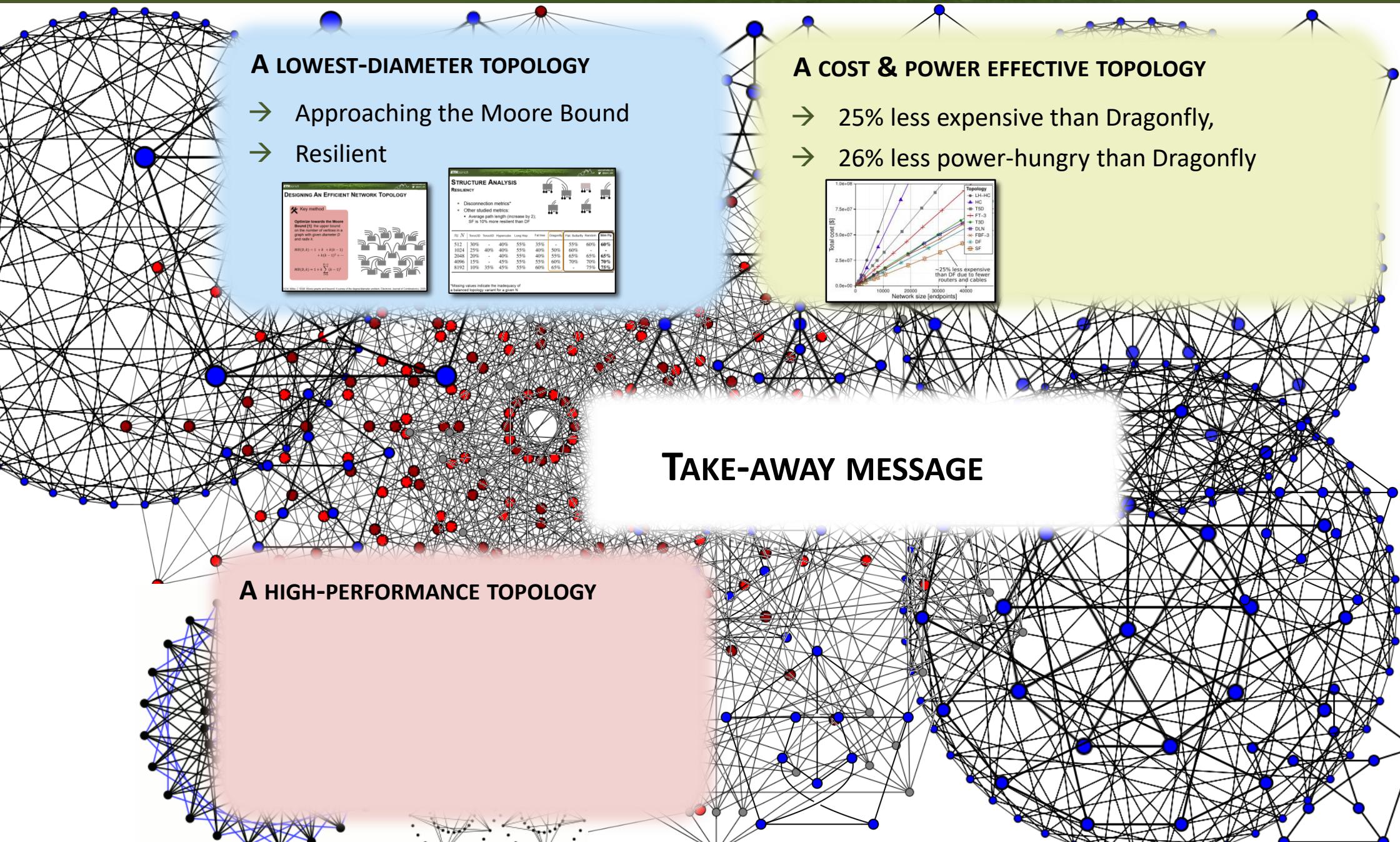


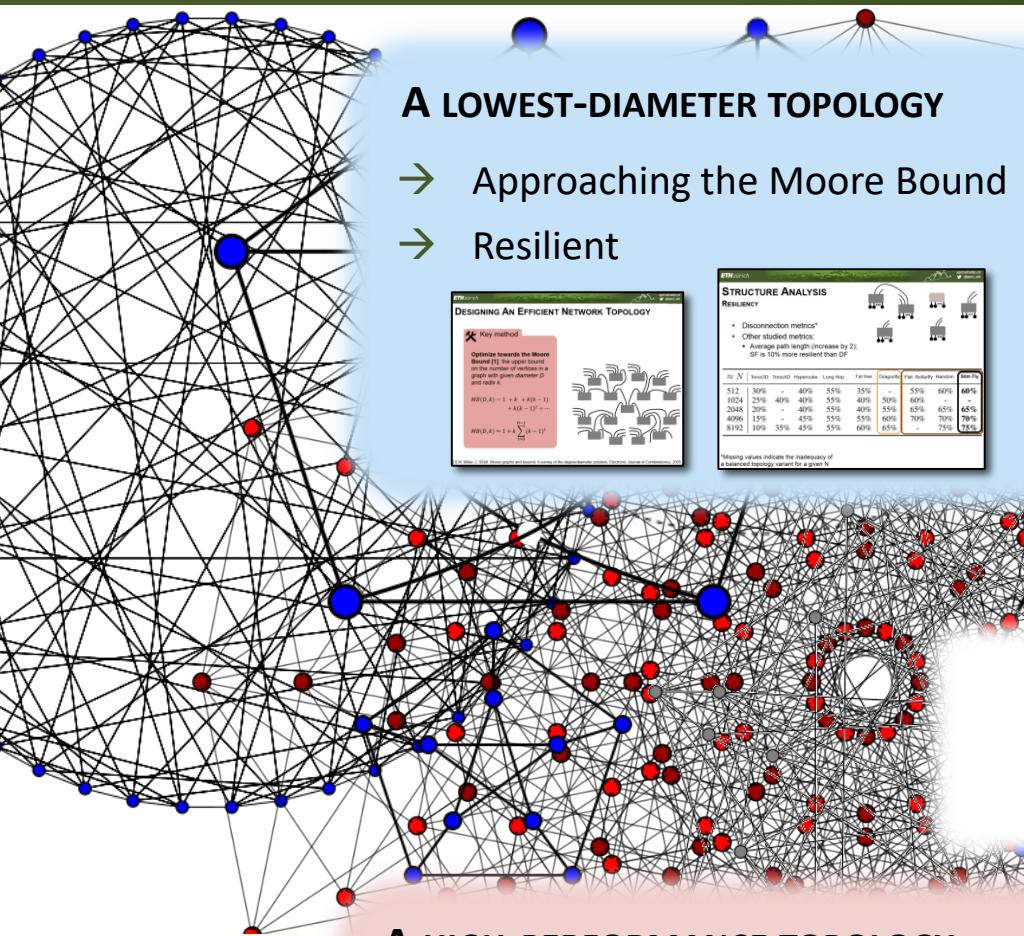
## A COST & POWER EFFECTIVE TOPOLOGY

- 25% less expensive than Dragonfly,
- 26% less power-hungry than Dragonfly



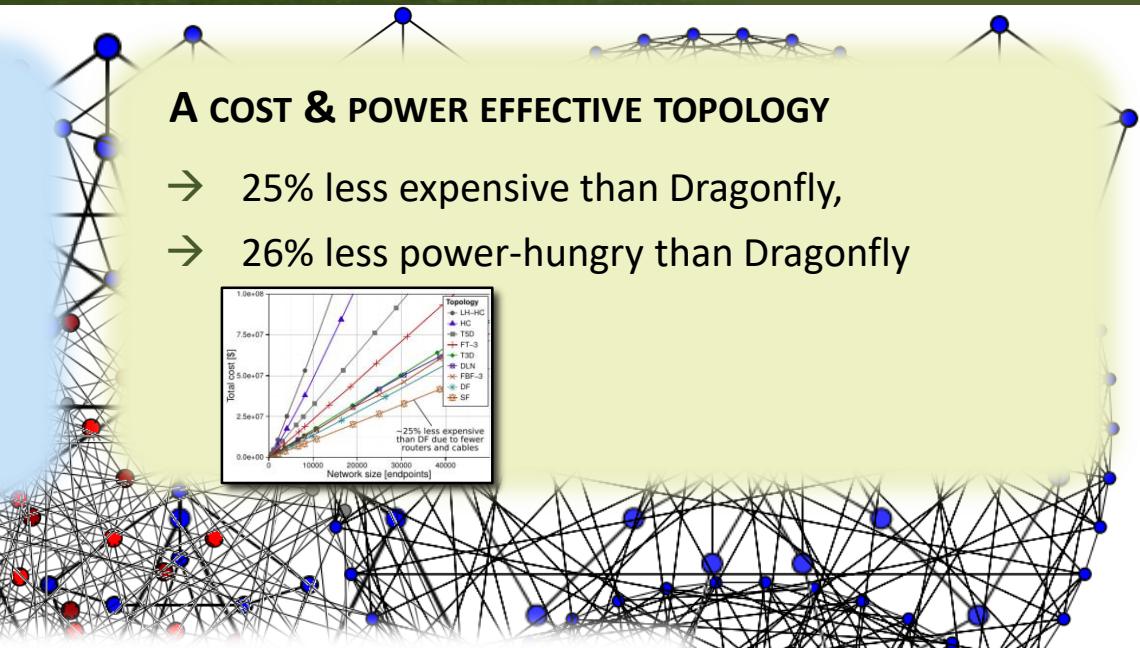
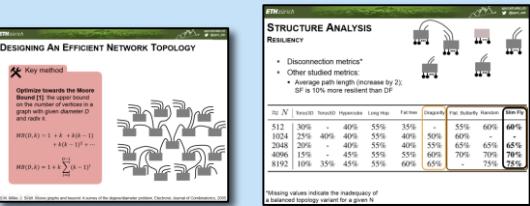
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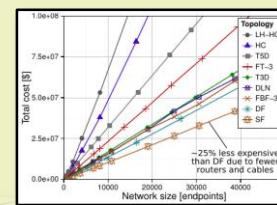
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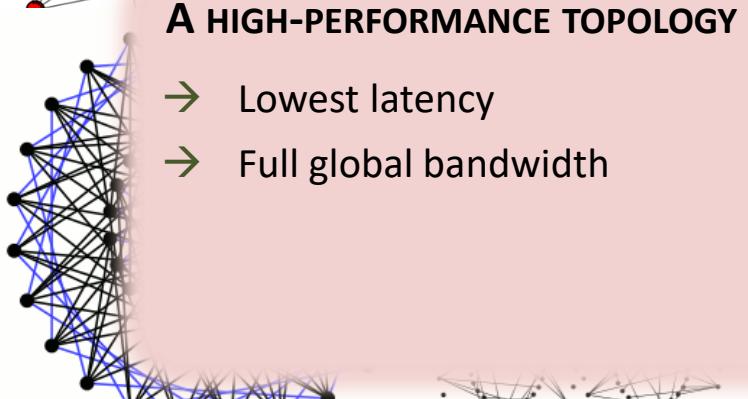


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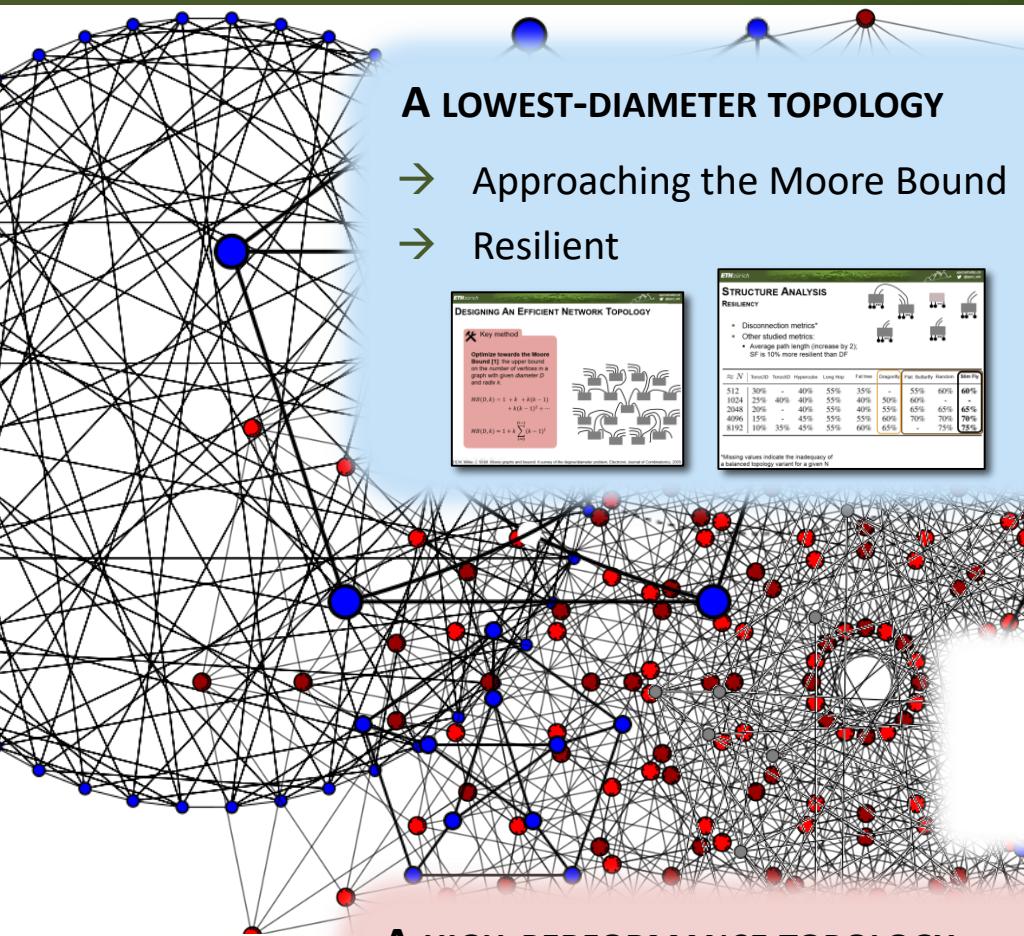


### TAKE-AWAY MESSAGE



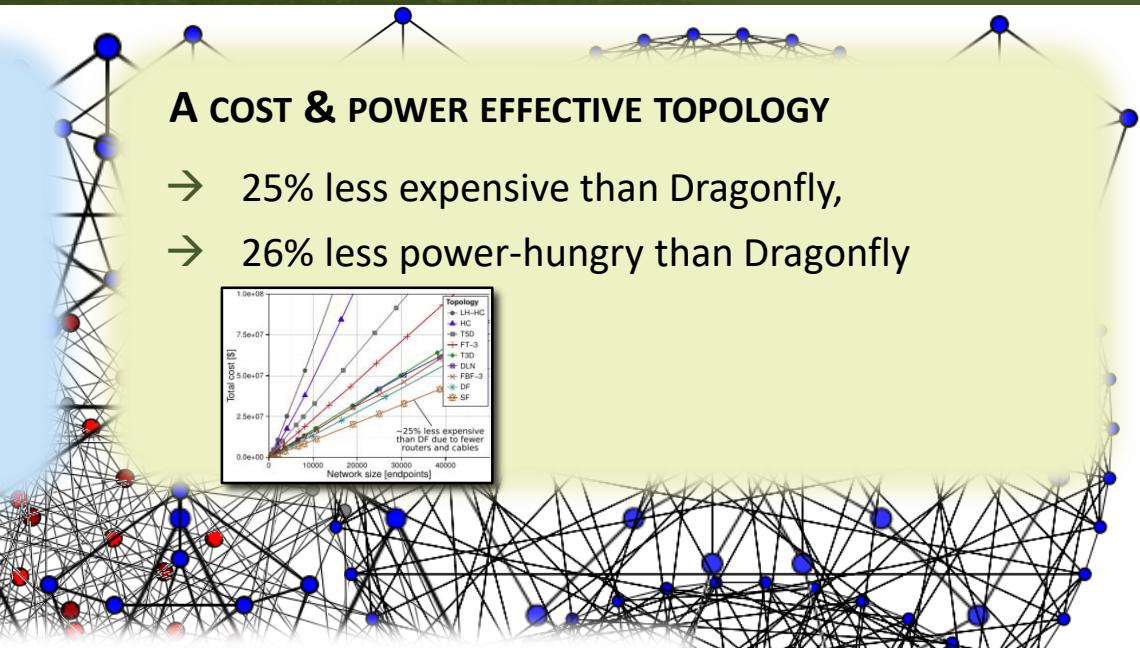
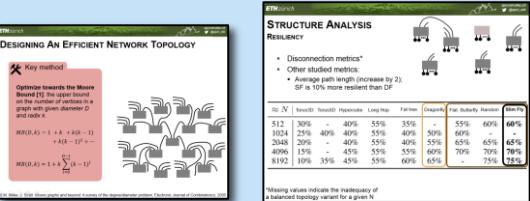
### A HIGH-PERFORMANCE TOPOLOGY

- Lowest latency
- Full global bandwidth



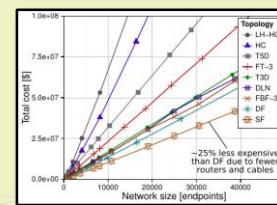
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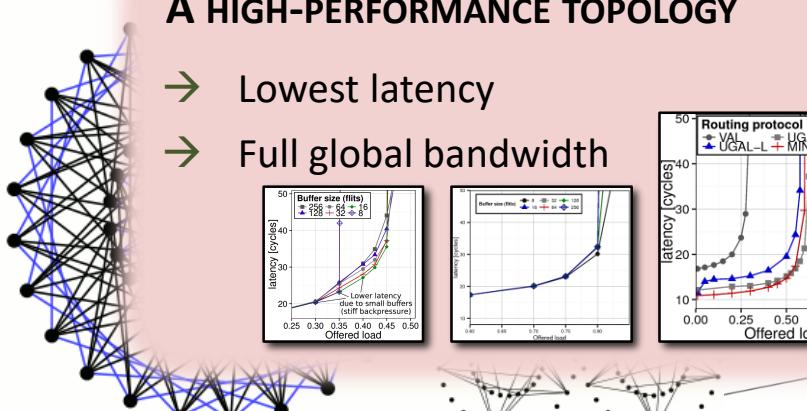


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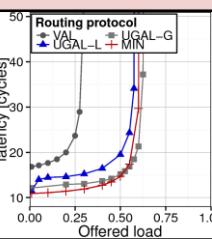


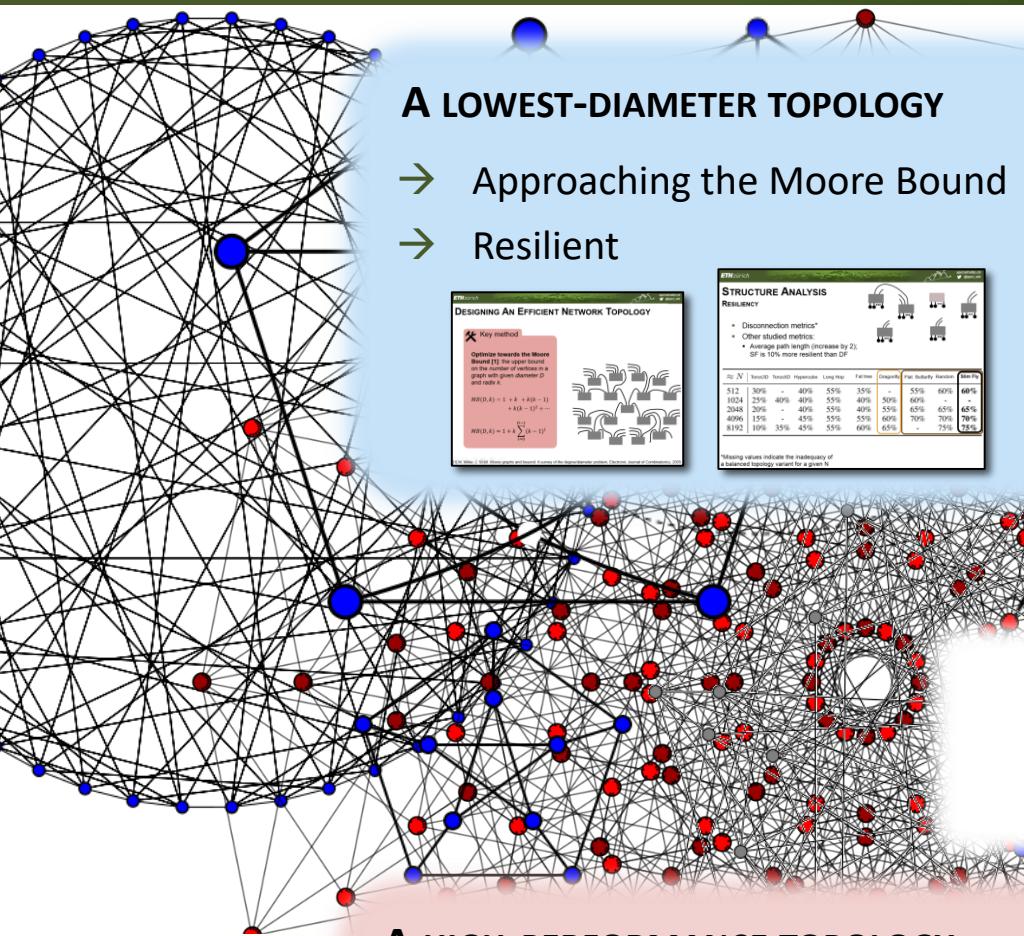
### TAKE-AWAY MESSAGE



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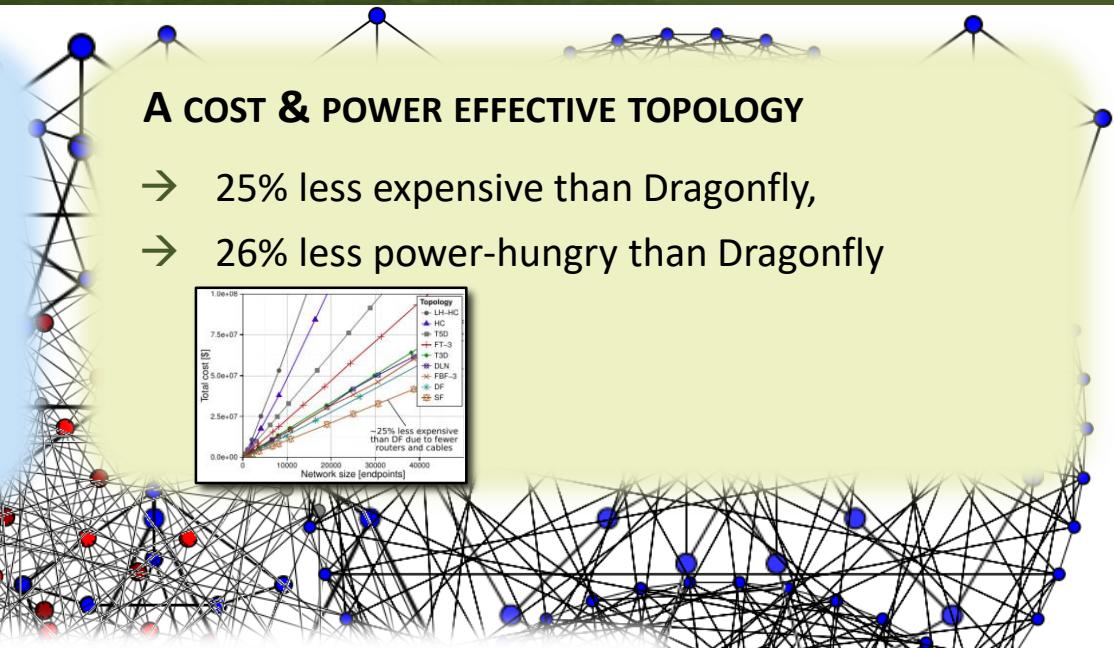
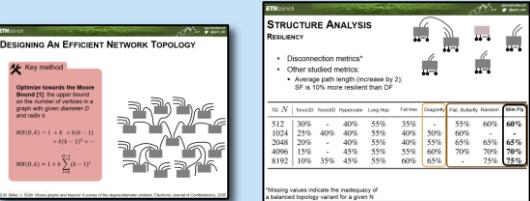
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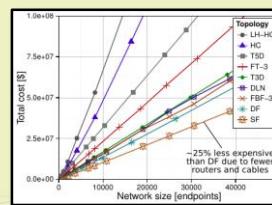
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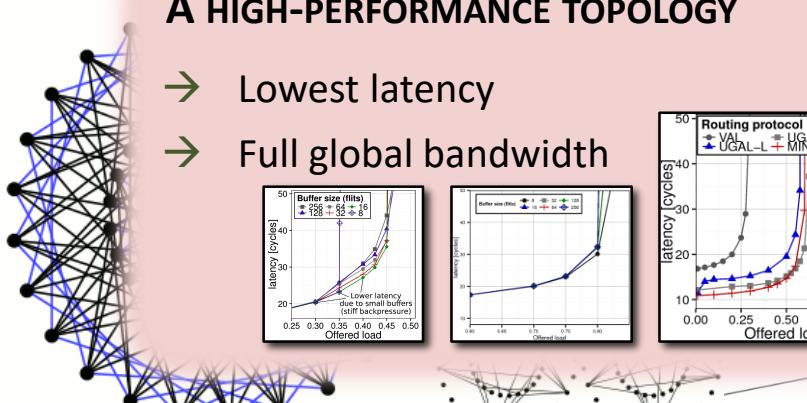


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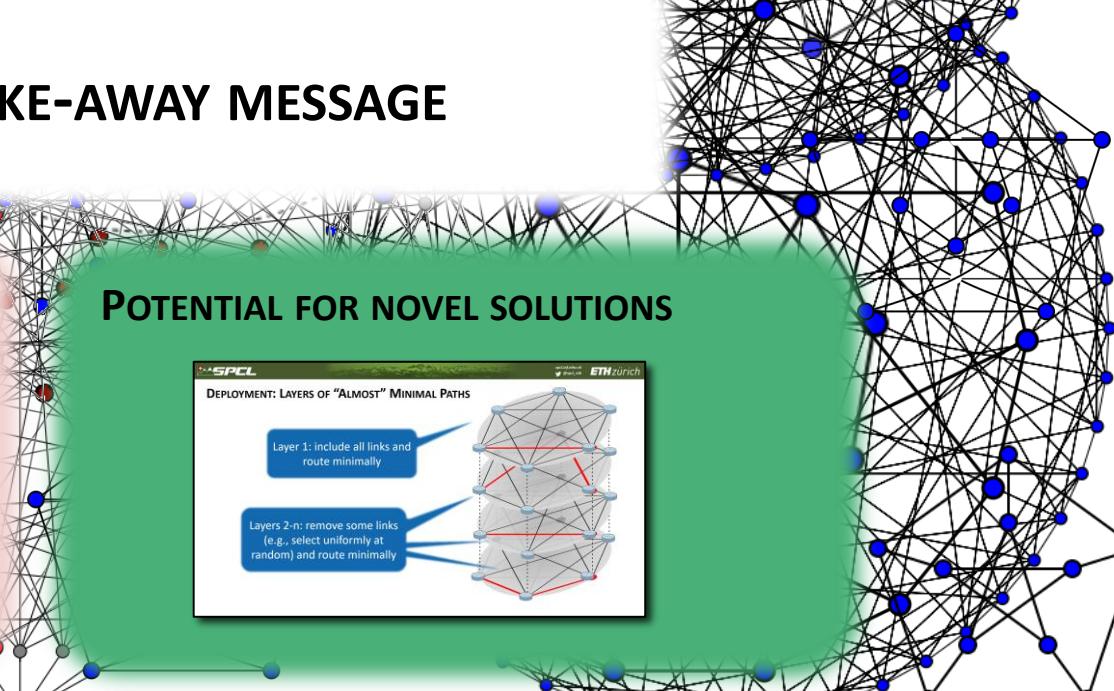
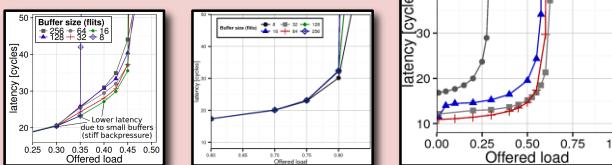


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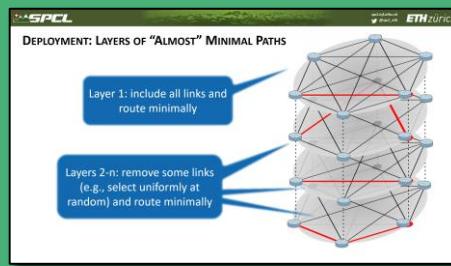


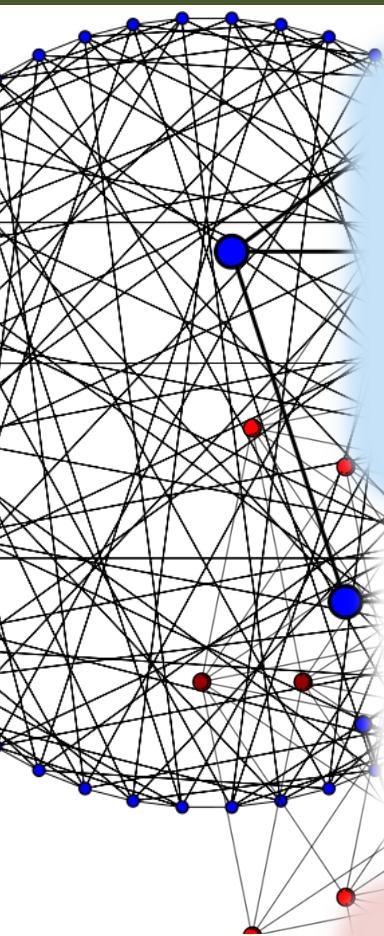
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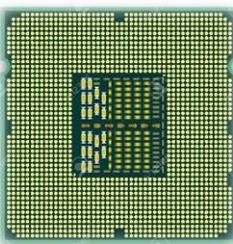
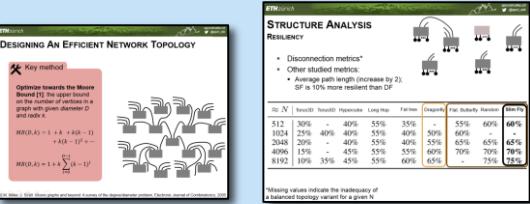
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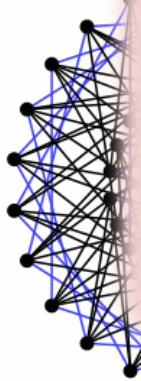


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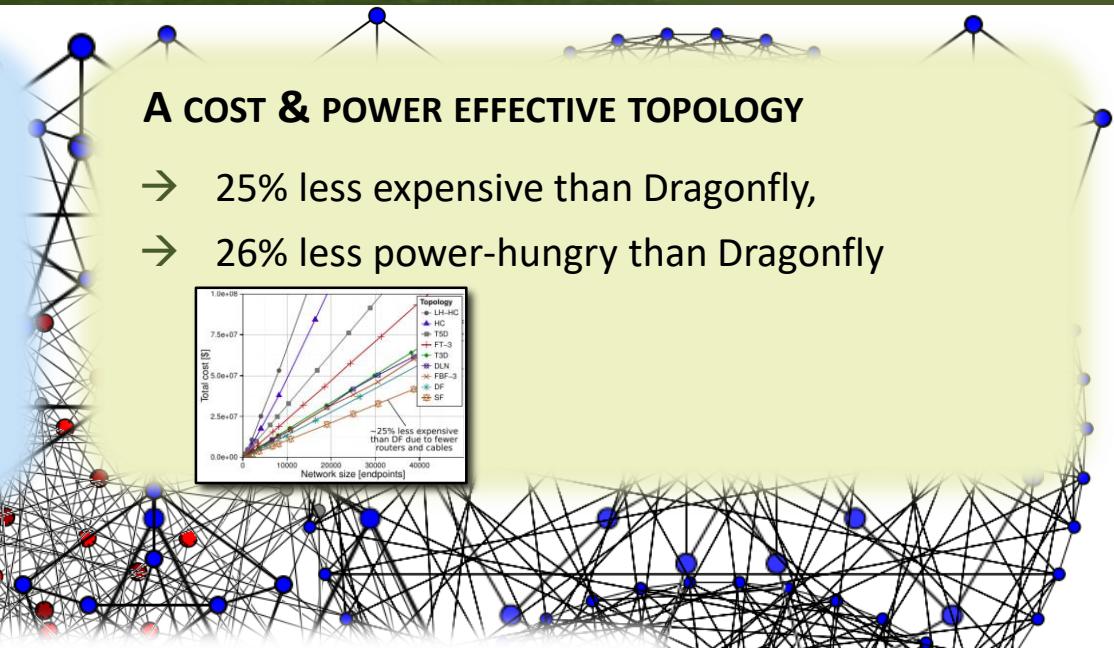
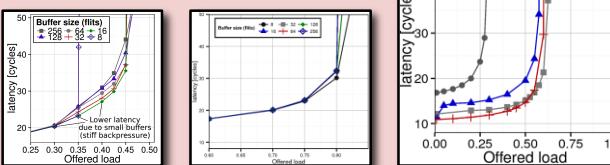


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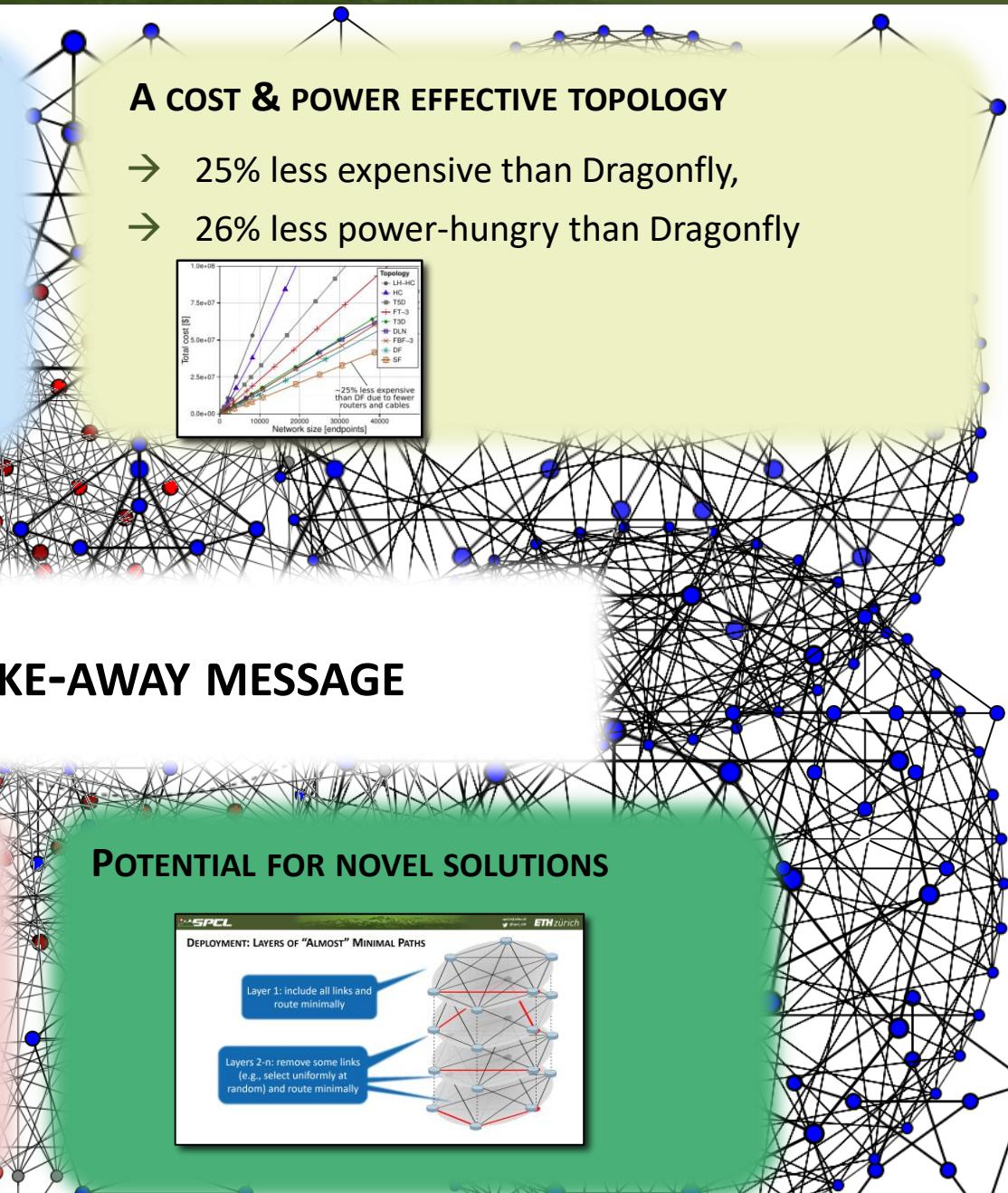
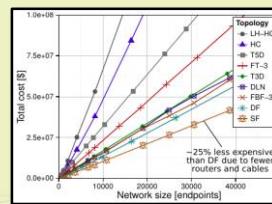
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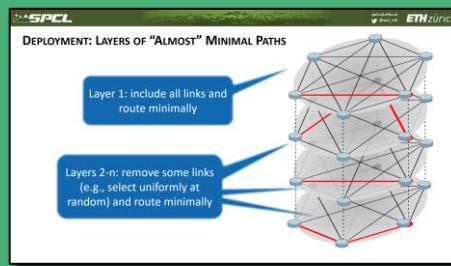


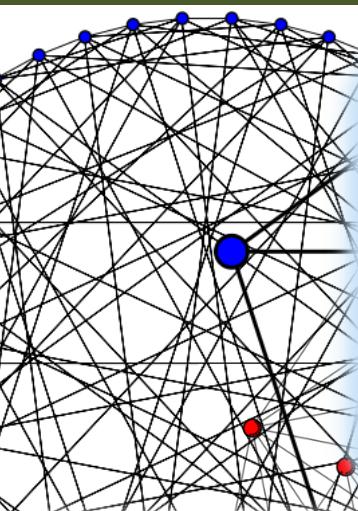
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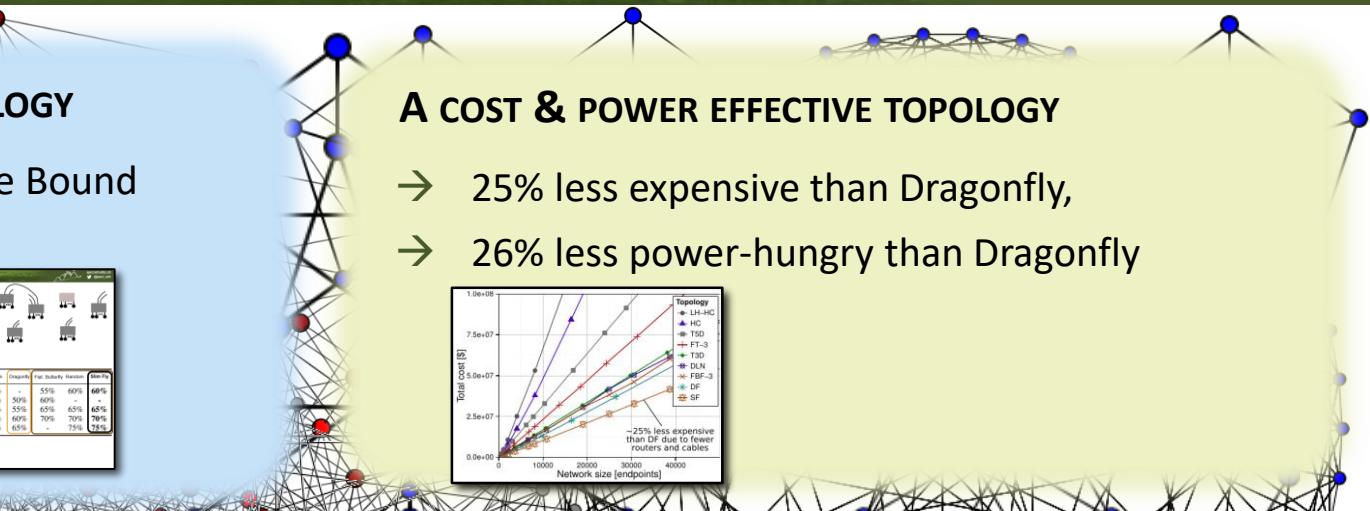
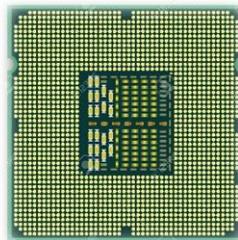
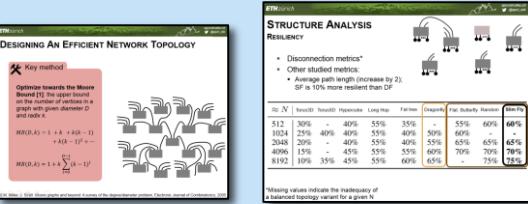
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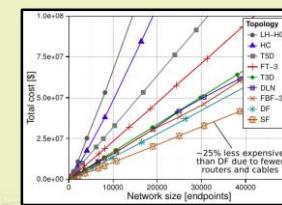
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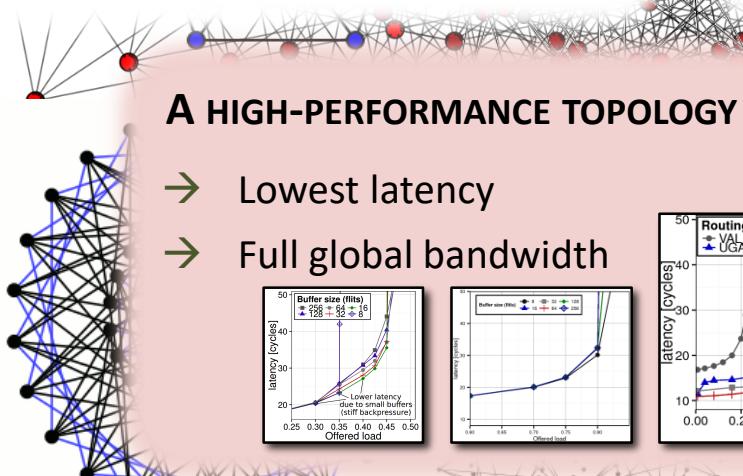


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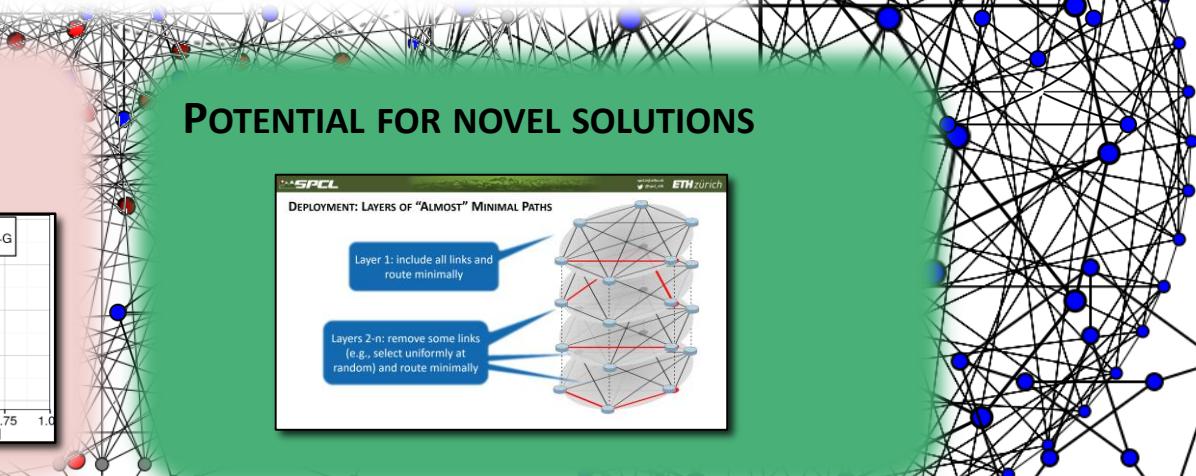
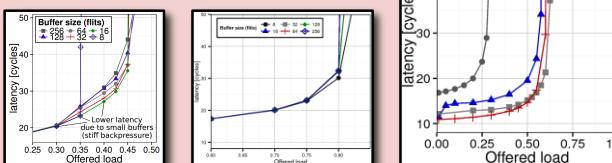


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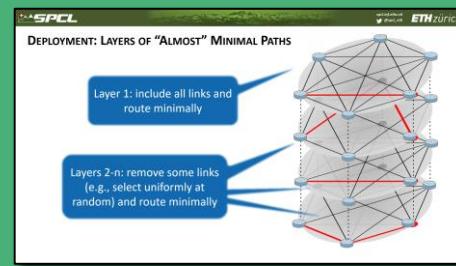


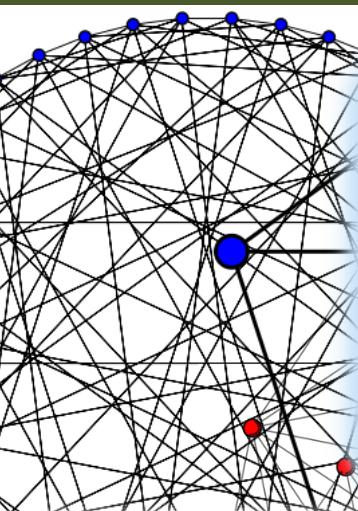
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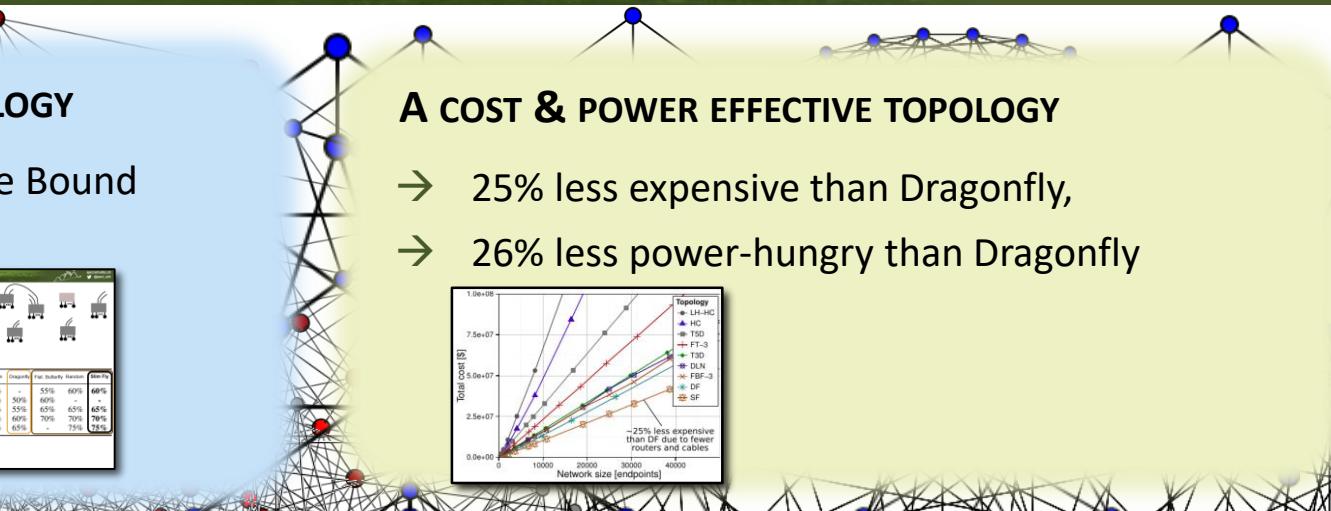
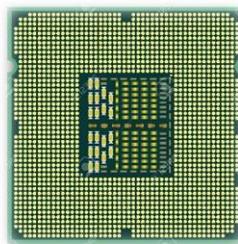
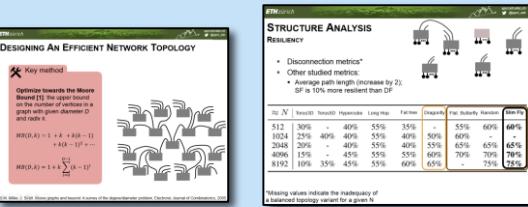
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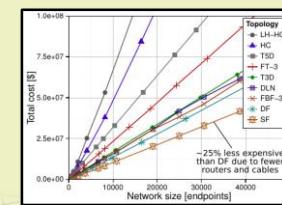
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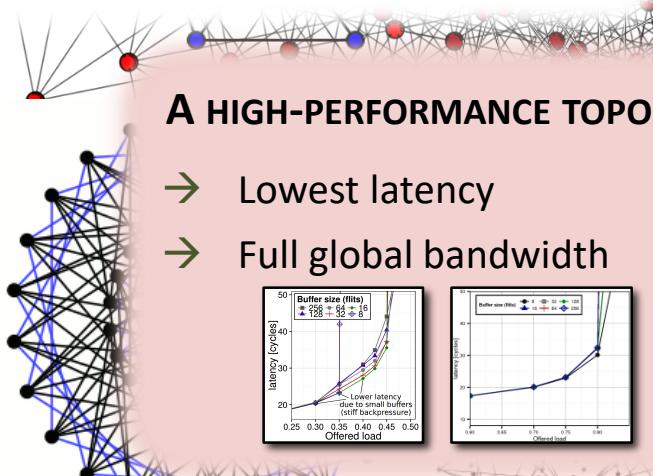


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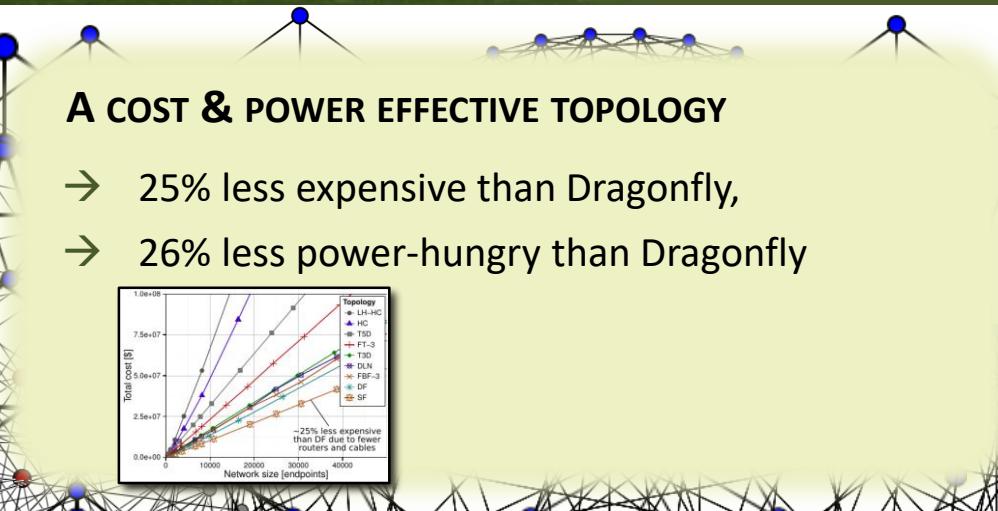
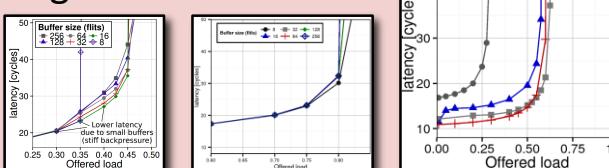


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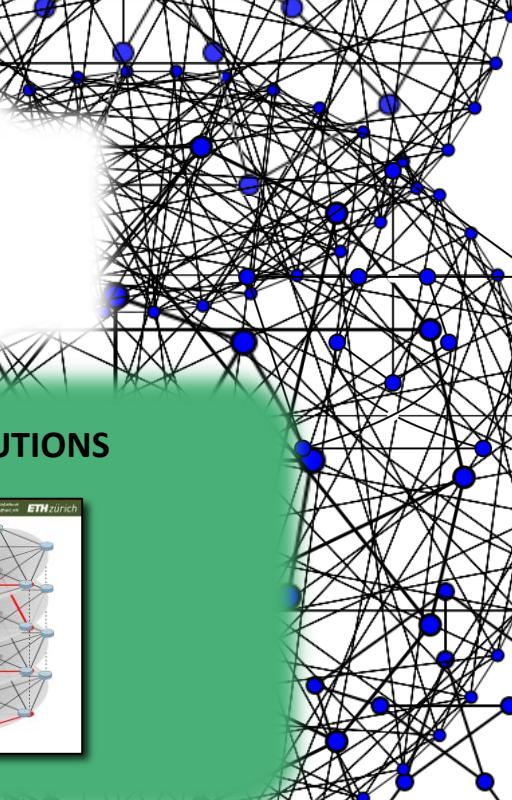
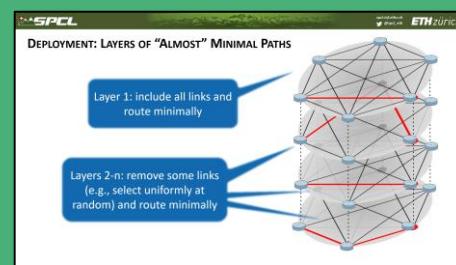


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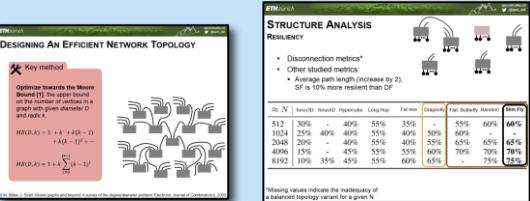


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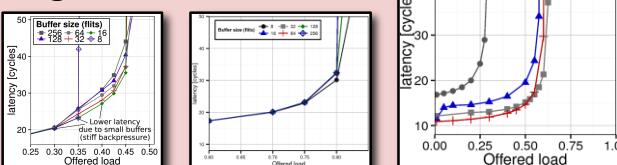


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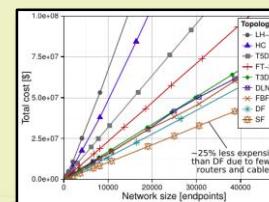
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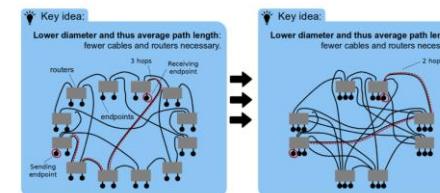
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**Scalable Parallel Computing Lab****Slim Fly: A Cost Effective Low-Diameter Network Topology**

The key idea in a single sentence  
"It's ALL about the diameter!": Optimize your topology for low diameter to not only reduce the latency (due to shorter paths) but also cost! and power consumption as packets will traverse and thus require fewer routers and cables.

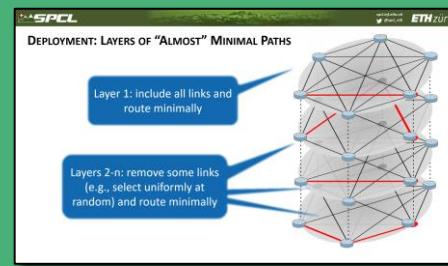
**Key motivation**

Interconnection networks play an important role in today's large-scale computing systems. An ideal topology should ensure: high bandwidth, low total system cost and energy consumption, low endpoint-to-endpoint latency, and resilience to link failures. We develop the Slim Fly topology that achieves all these goals by lowering the diameter.



The key idea and motivation: On the left, a topology has a diameter of 3; two communicating endpoints require 3 hops. On the right, the topology has been rearranged so that the diameter is 2. Thus, packets require fewer routers and cables.

# Thank you for your attention

**POTENTIAL FOR NOVEL SOLUTIONS**

# Lowering Diameter Enables Cost-Effective and High-Performance Networks

**MACIEJ BESTA, ERIK HENRIKSSON, TORSTEN HOEFLER**



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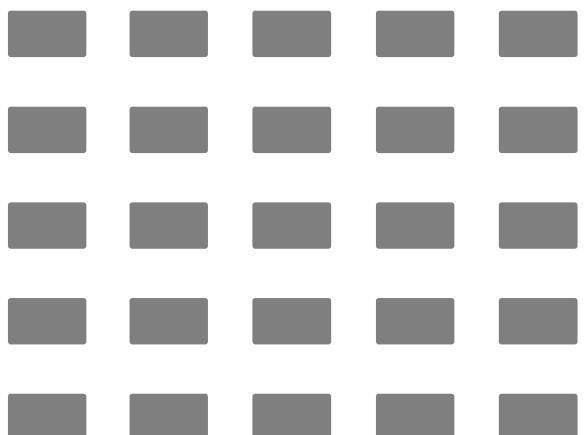
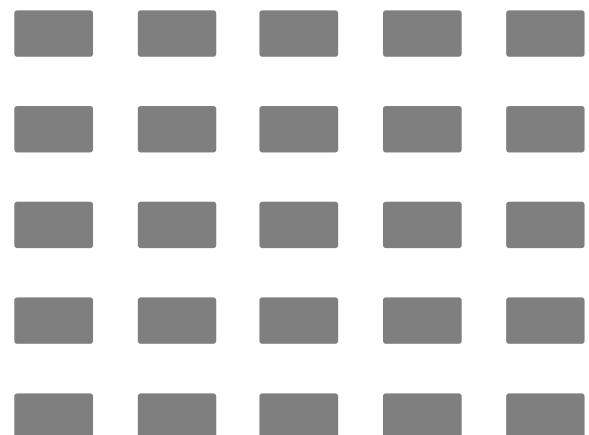
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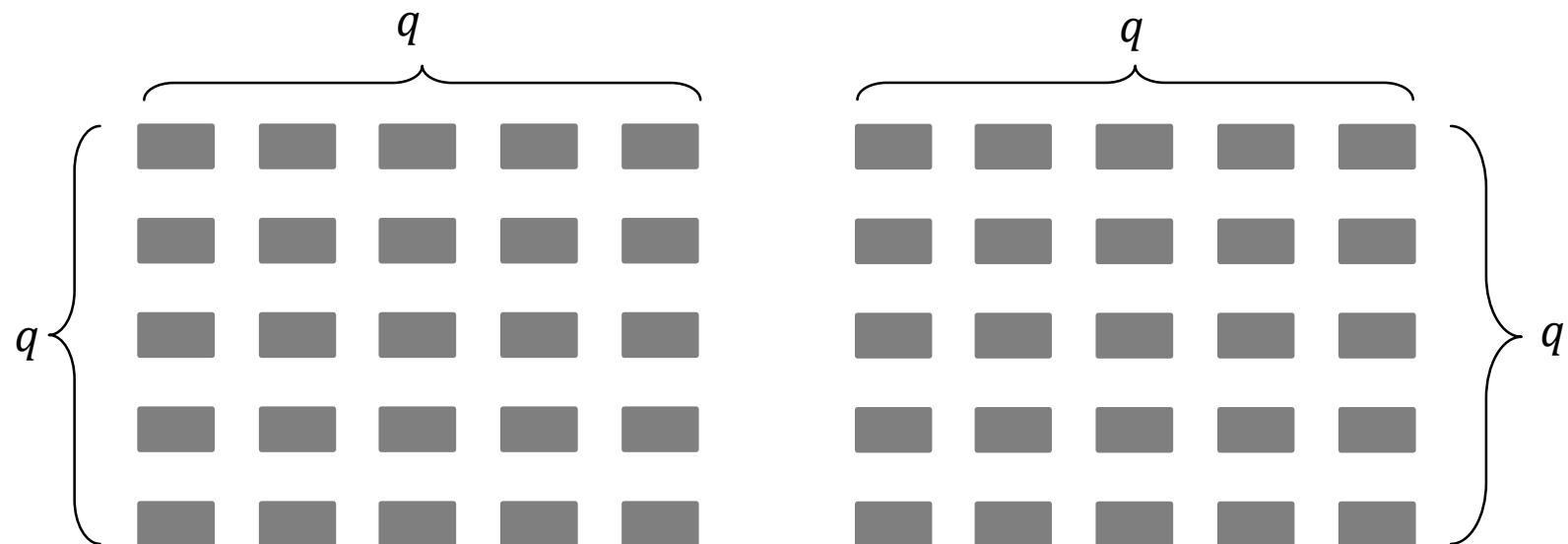


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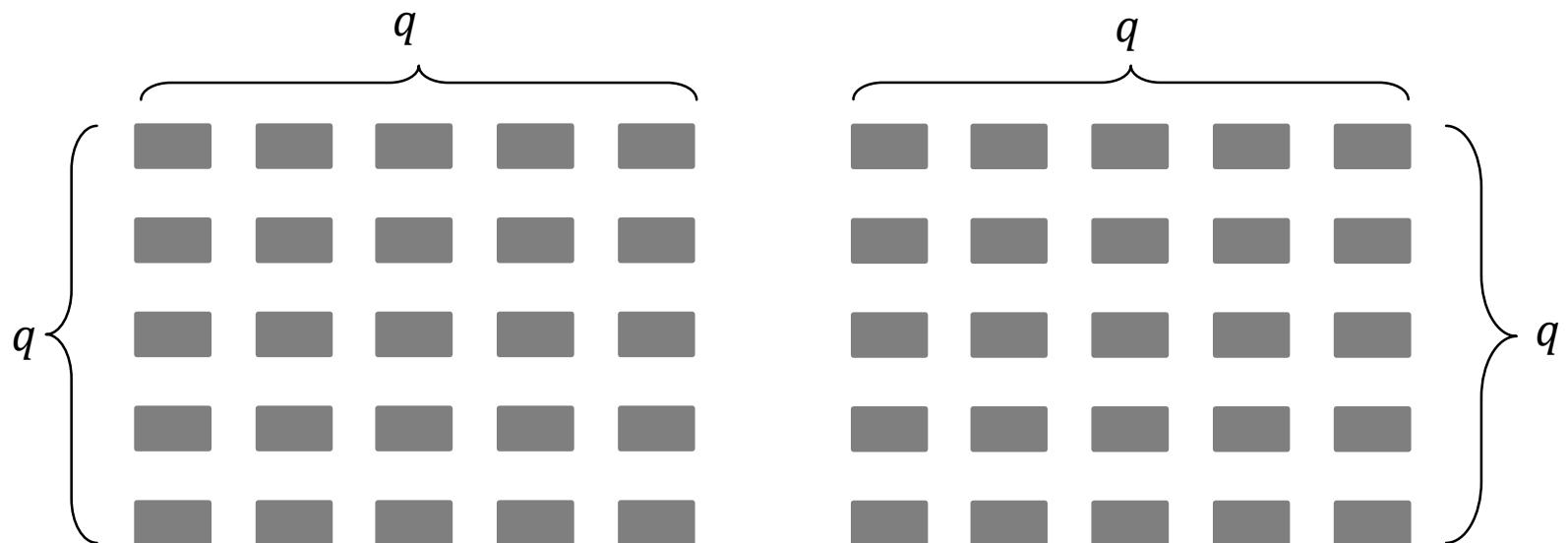
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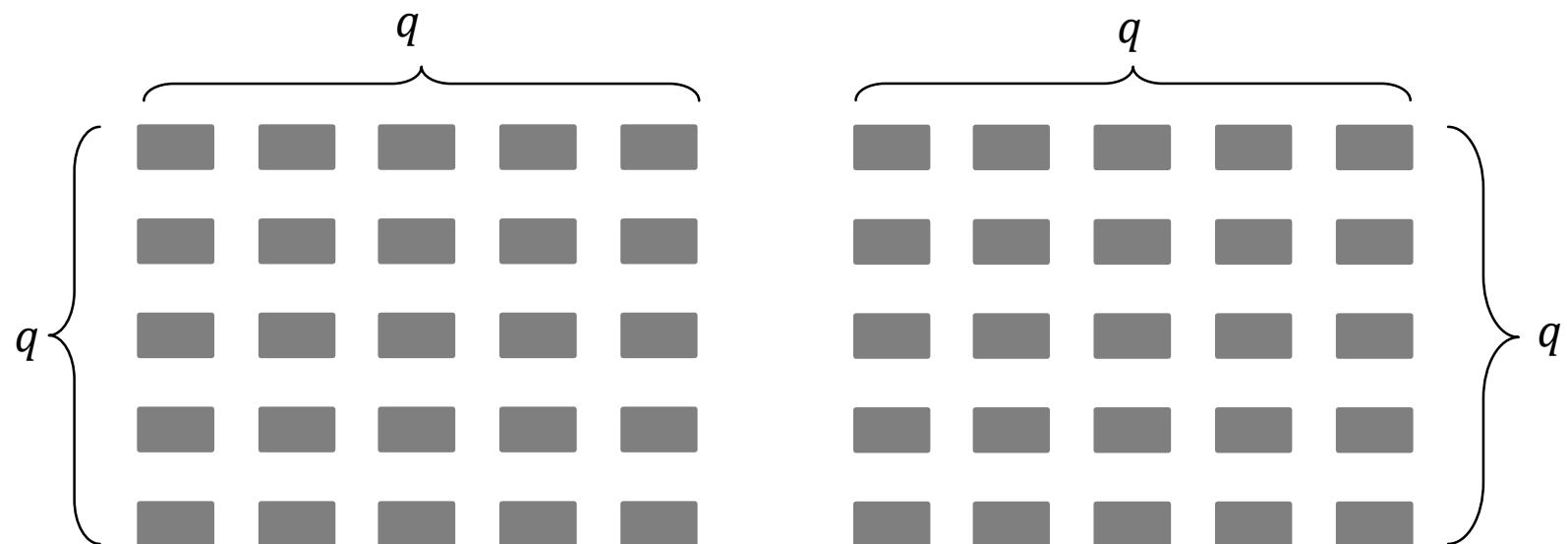
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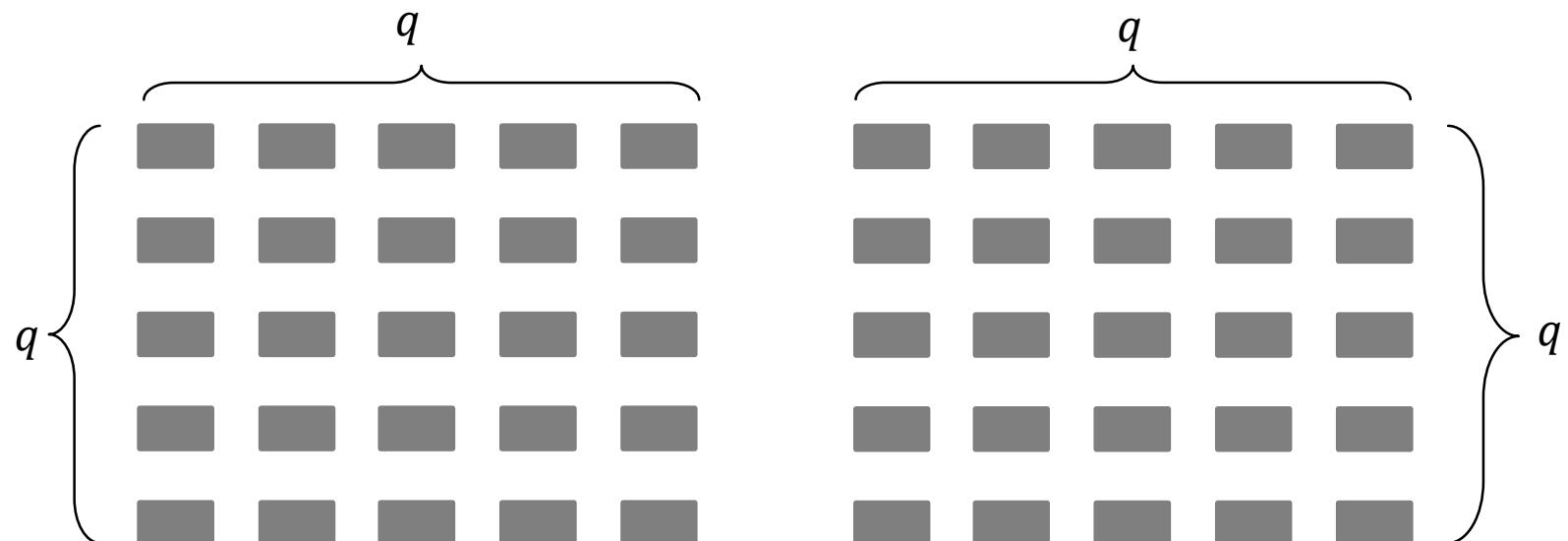
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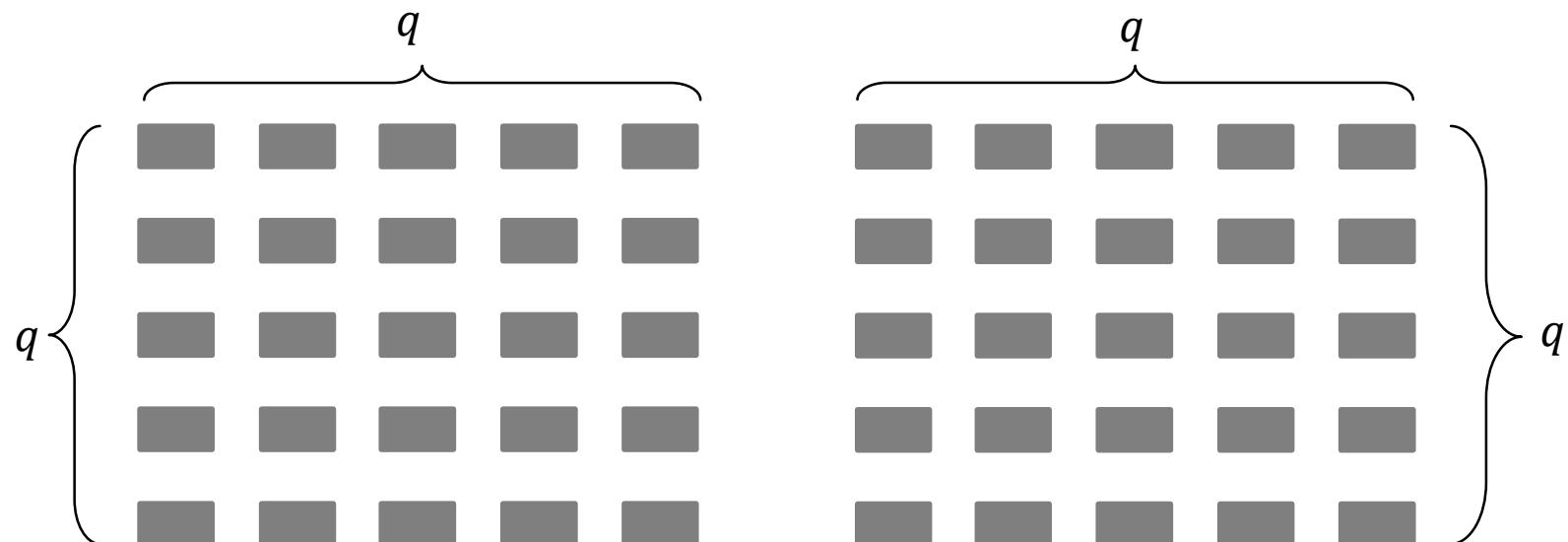
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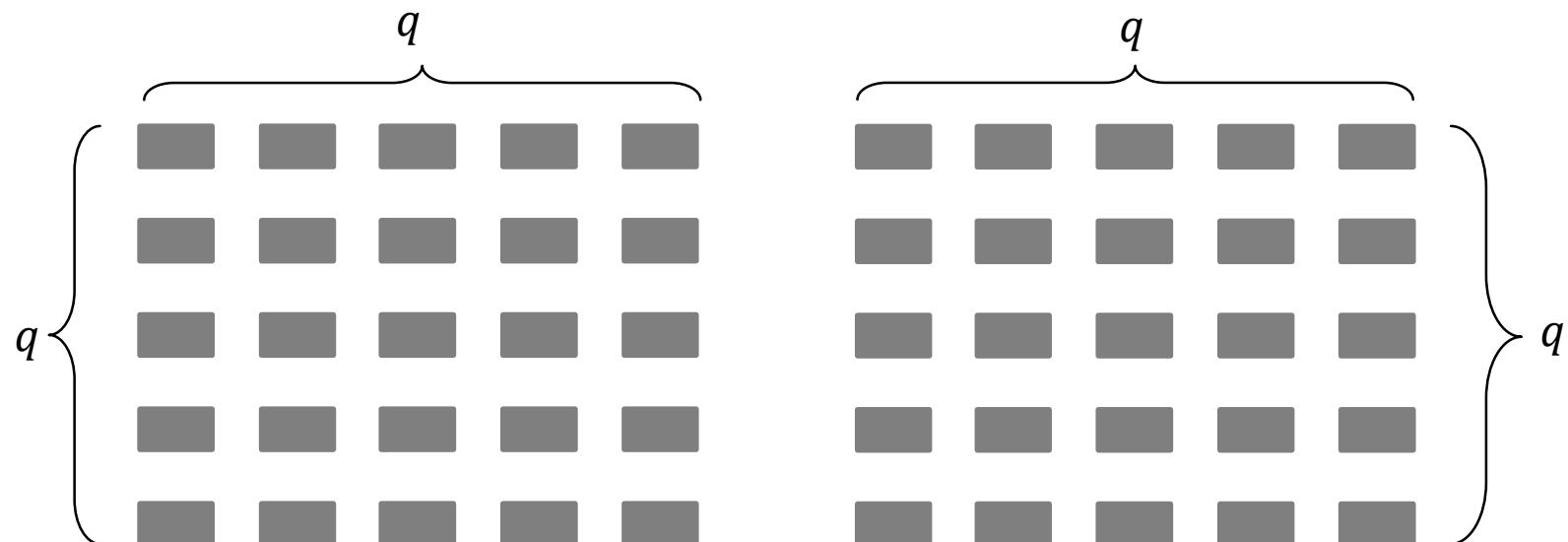
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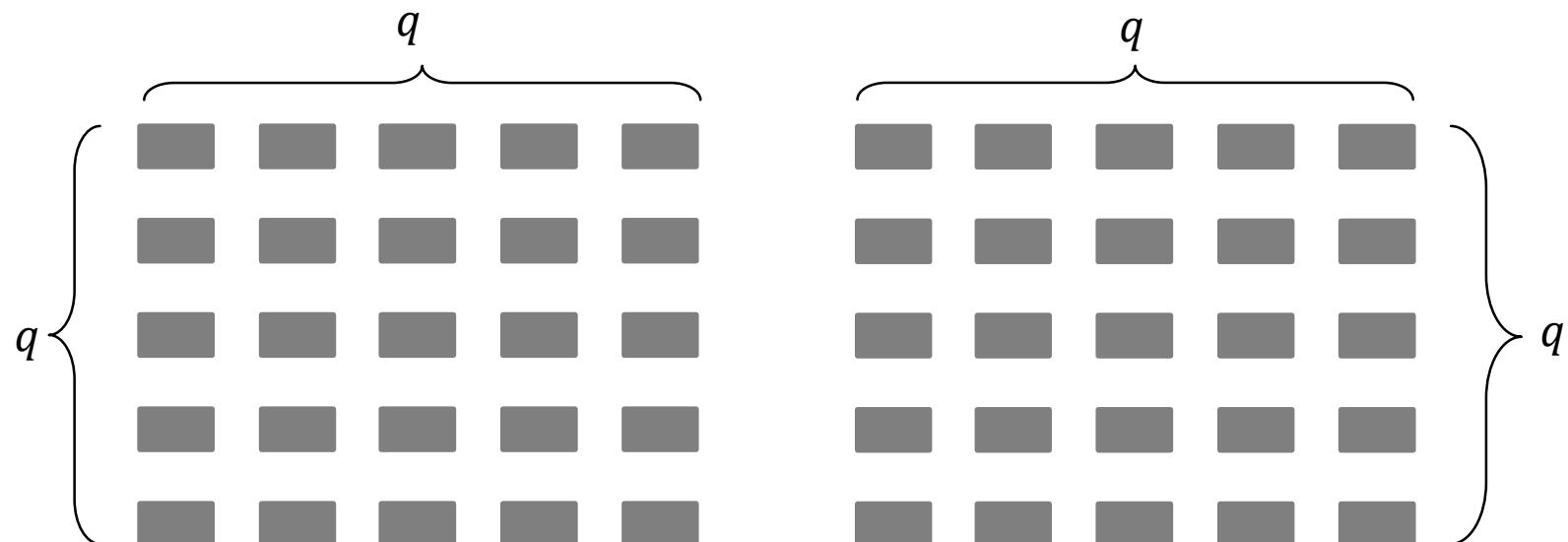
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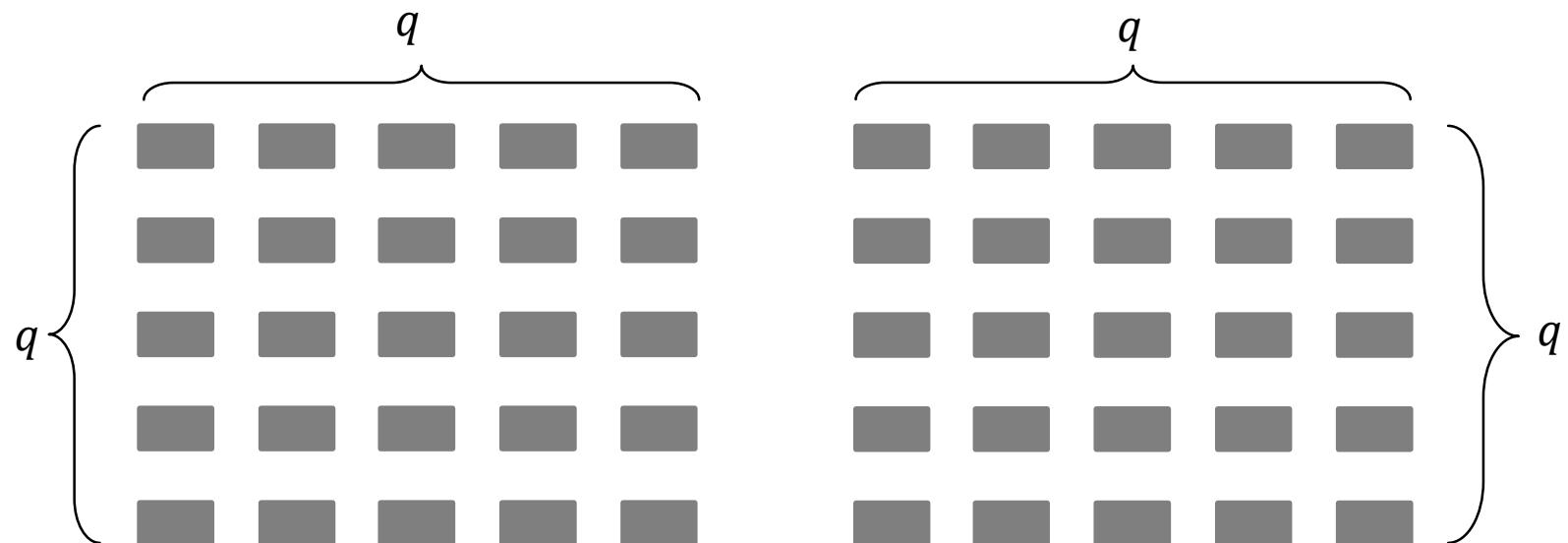
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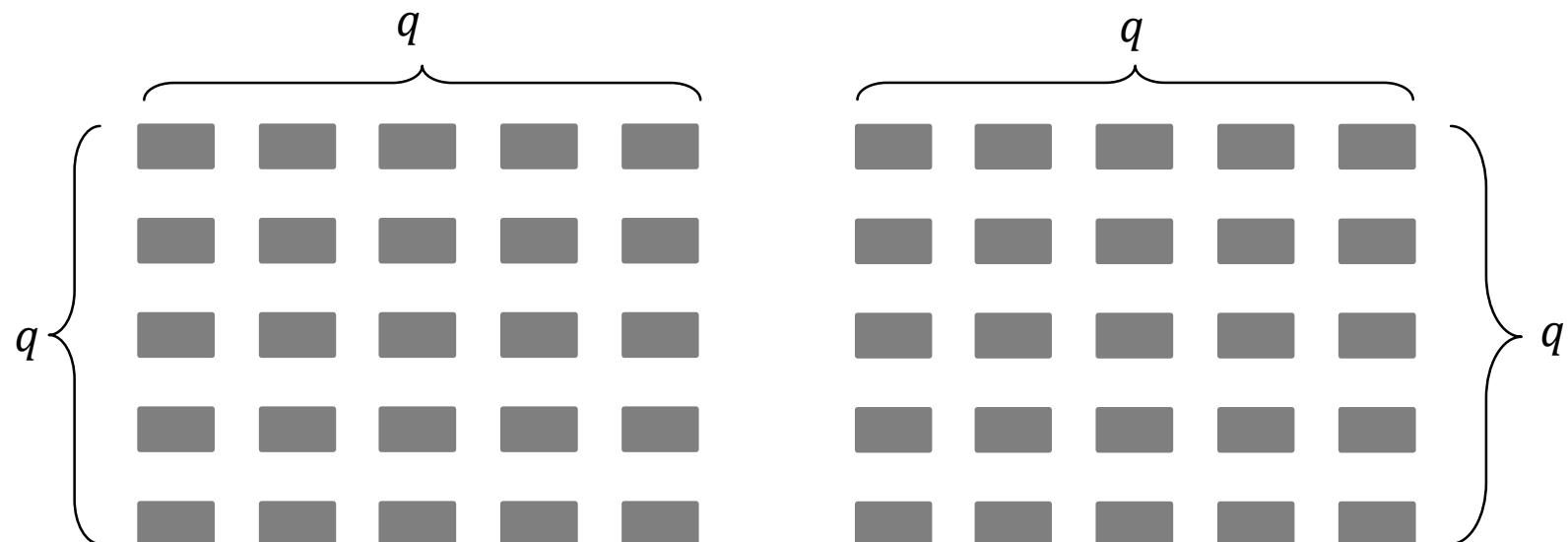
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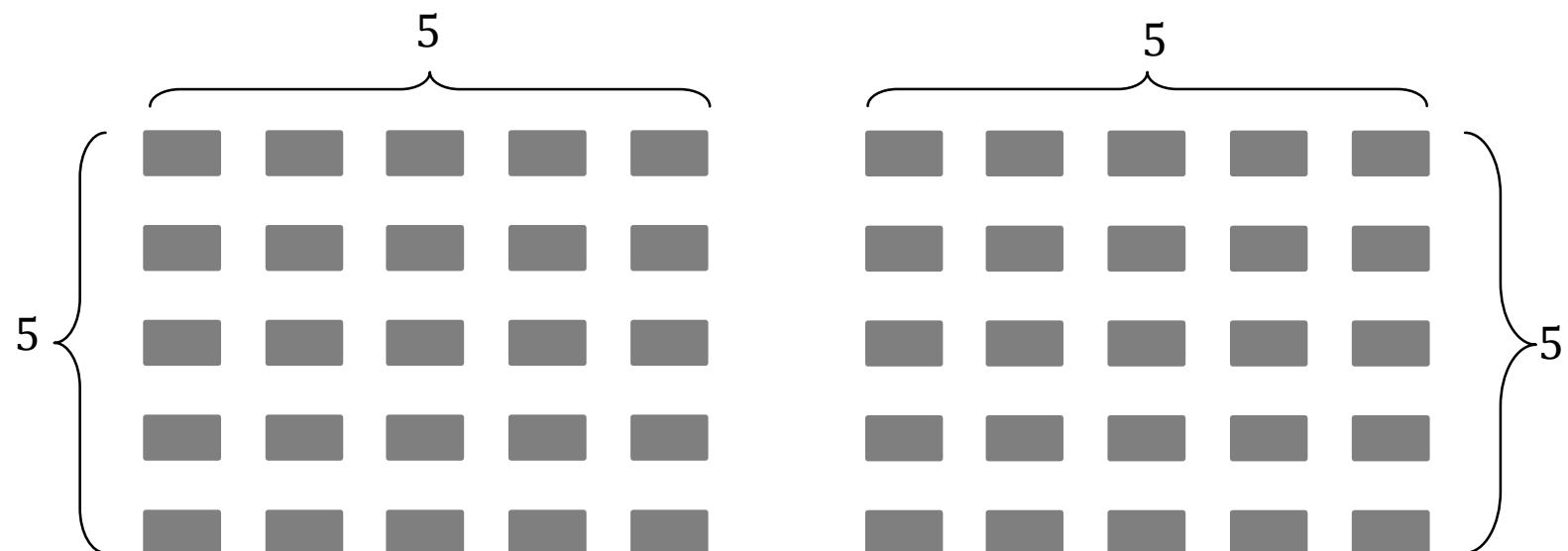
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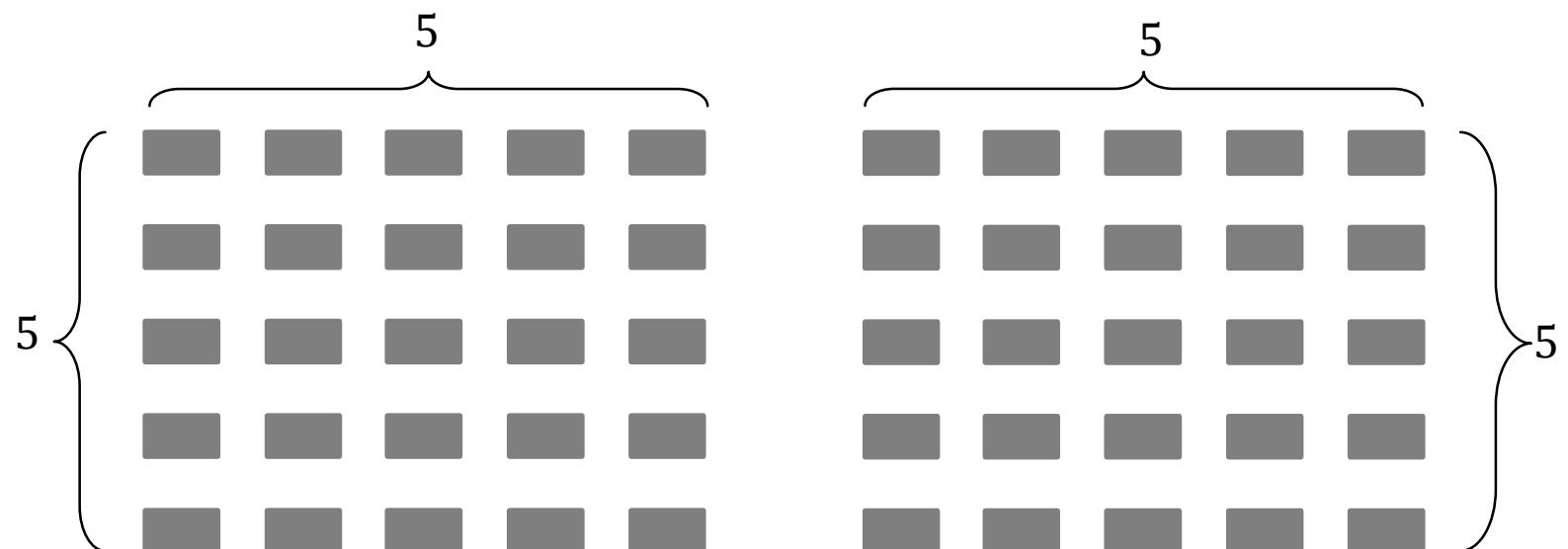
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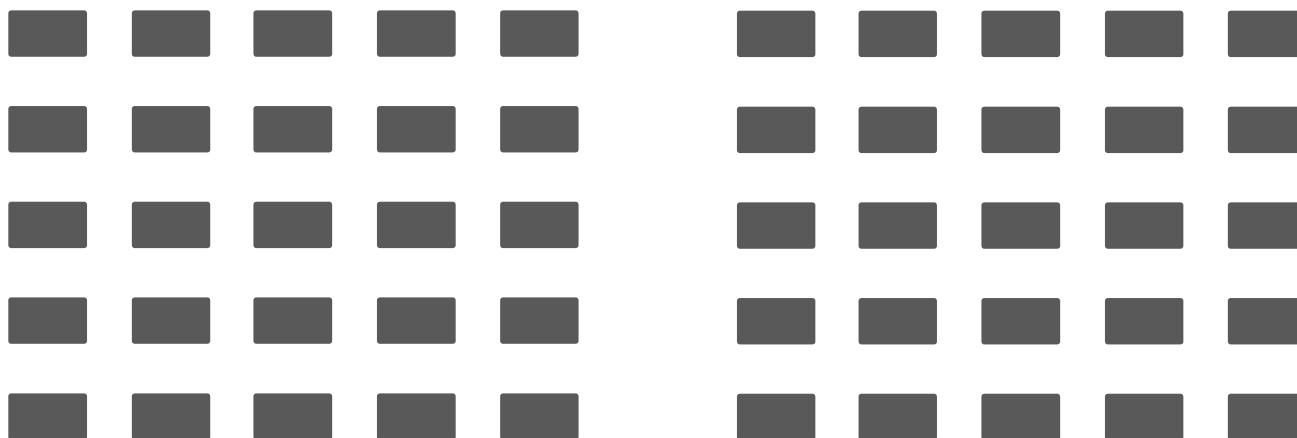
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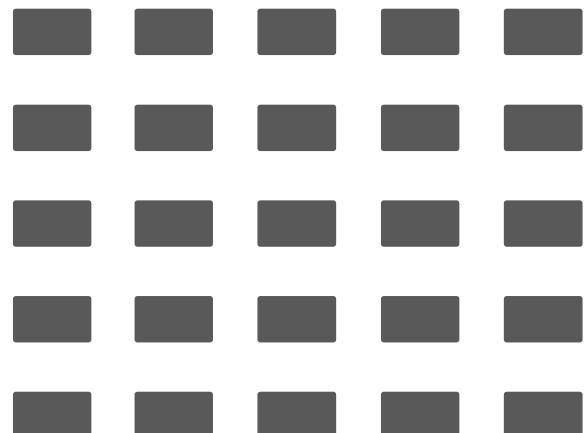
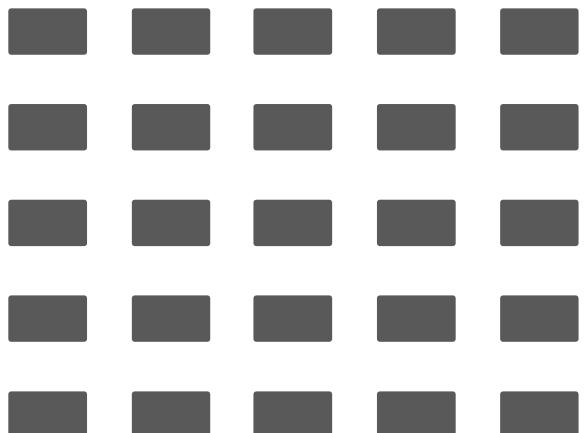


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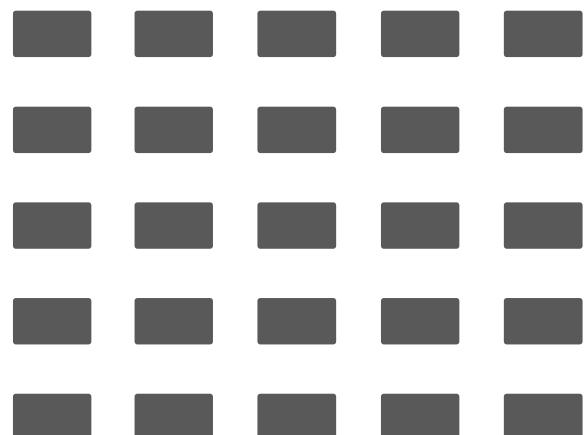
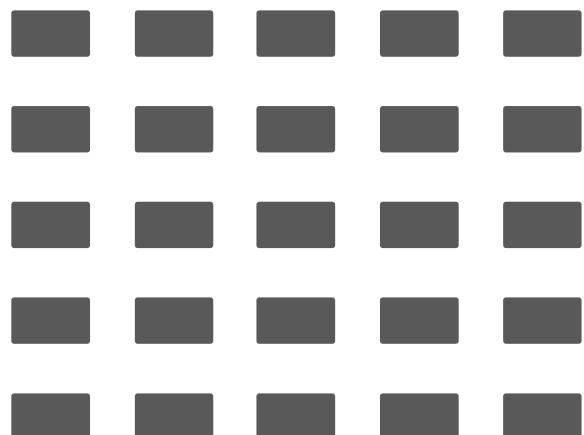


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## 3 Label the routers

Set of routers:

$$\{0,1\} \times \mathcal{F}_q \times \mathcal{F}_q$$



# DIAMETER-2 SLIM FLY

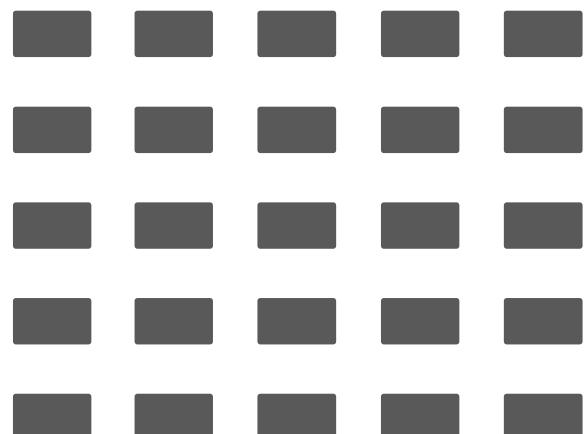
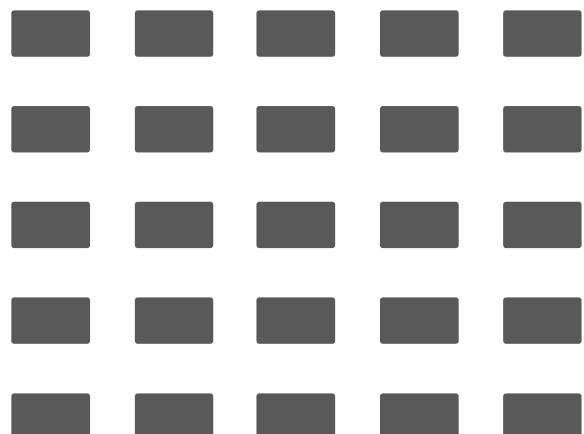
3 *Label the routers*

Set of routers:

$$\{0,1\} \times \mathcal{F}_q \times \mathcal{F}_q$$

E *Example: q = 5*

...



# DIAMETER-2 SLIM FLY

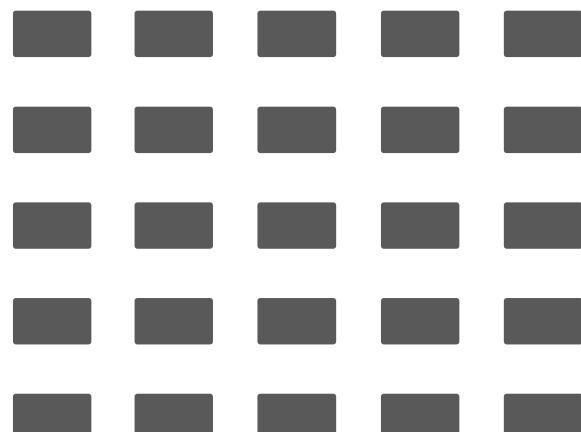
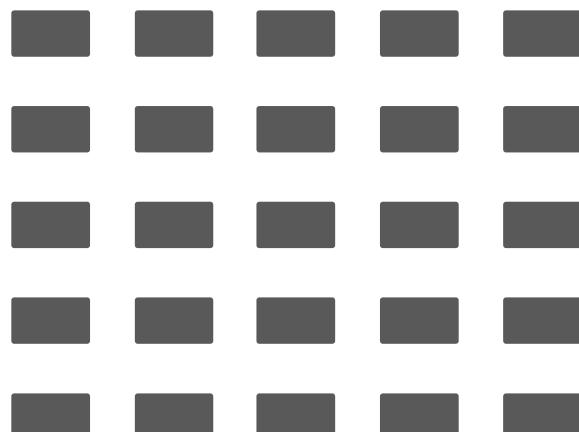
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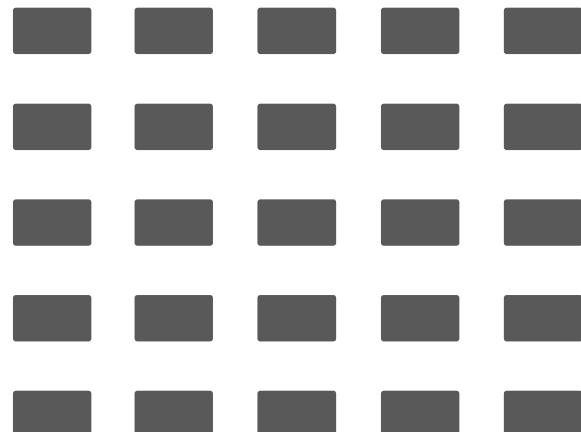
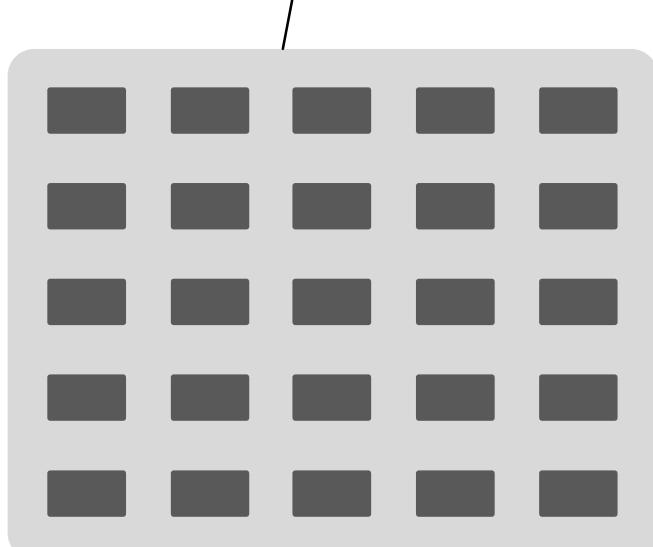
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E *Example: q = 5*

...

Routers (0,..)



# DIAMETER-2 SLIM FLY

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Set of routers:

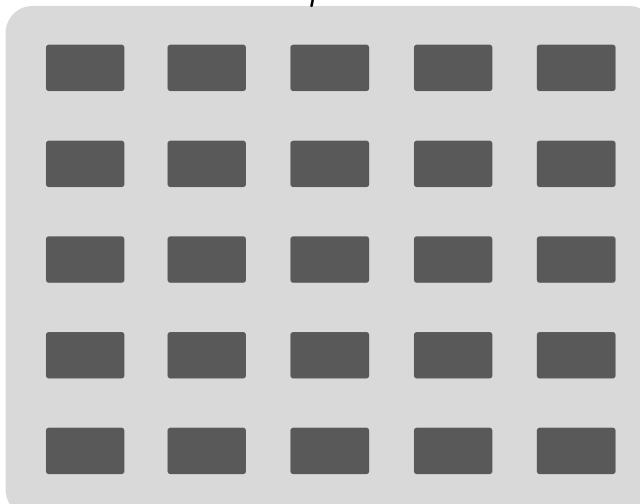
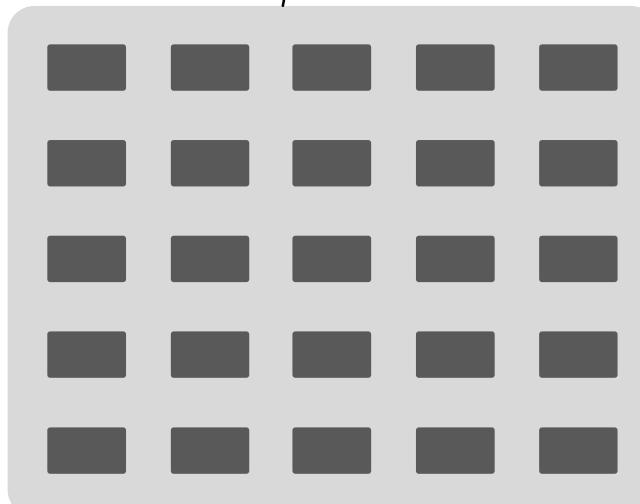
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E Example:  $q = 5$

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Routers (0,...)

Routers (1,...)



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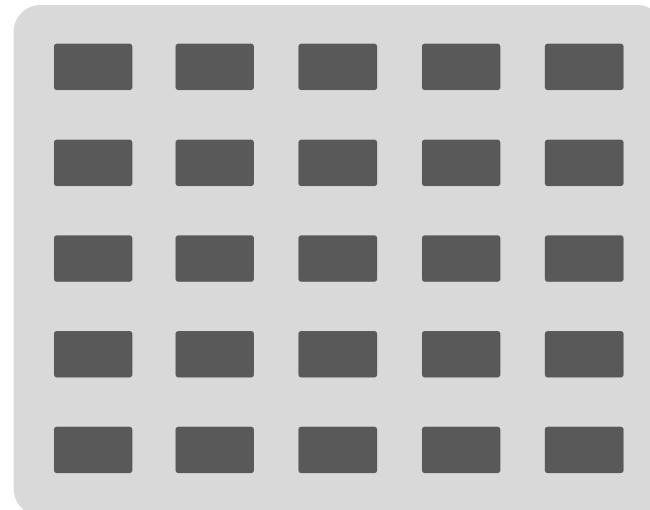
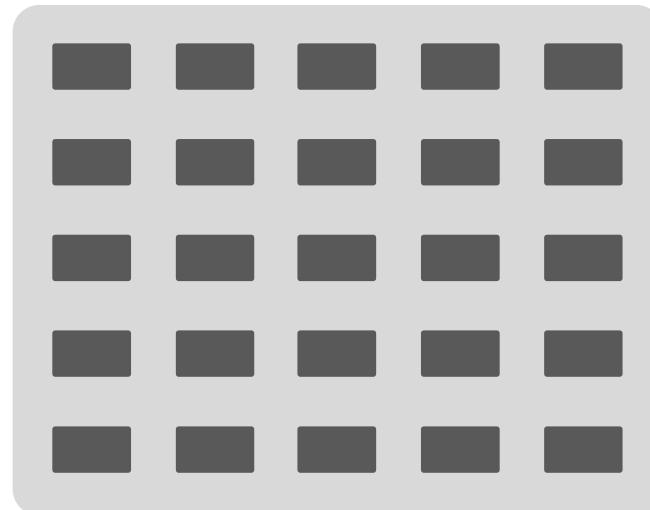
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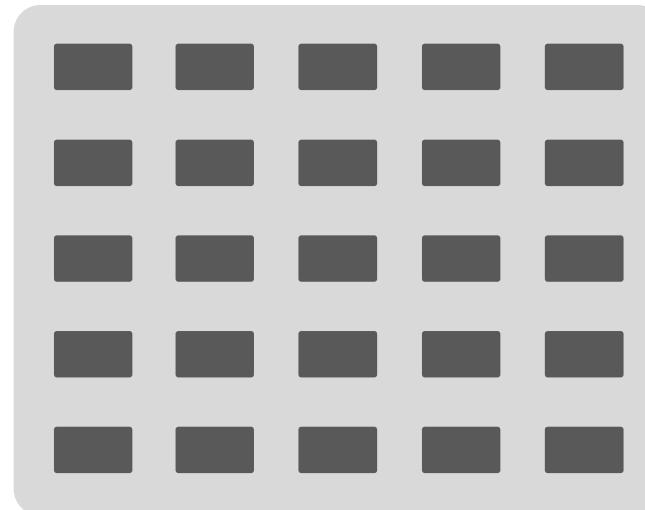
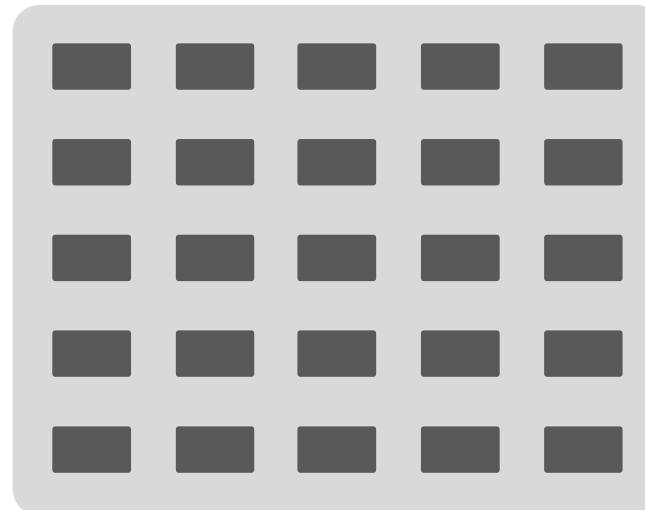
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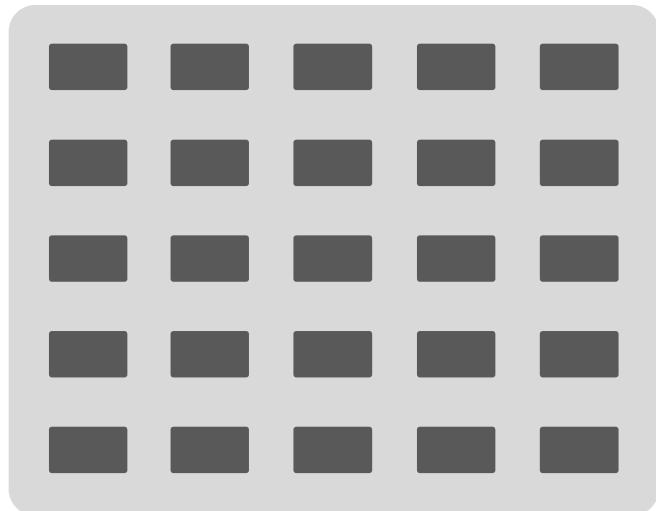
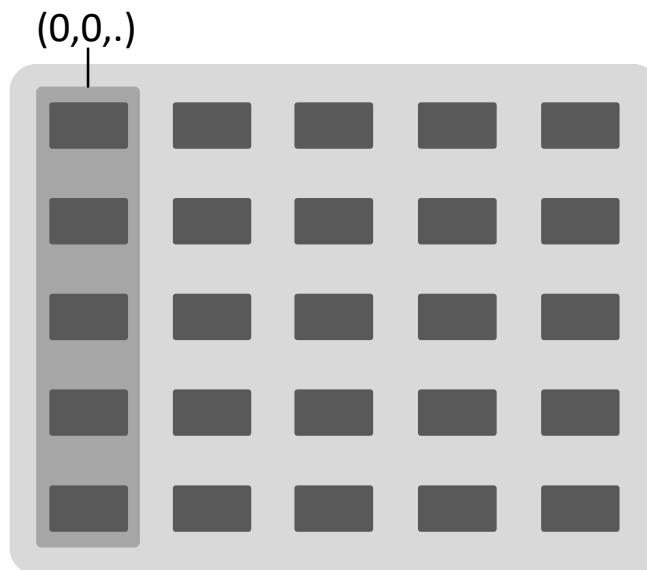
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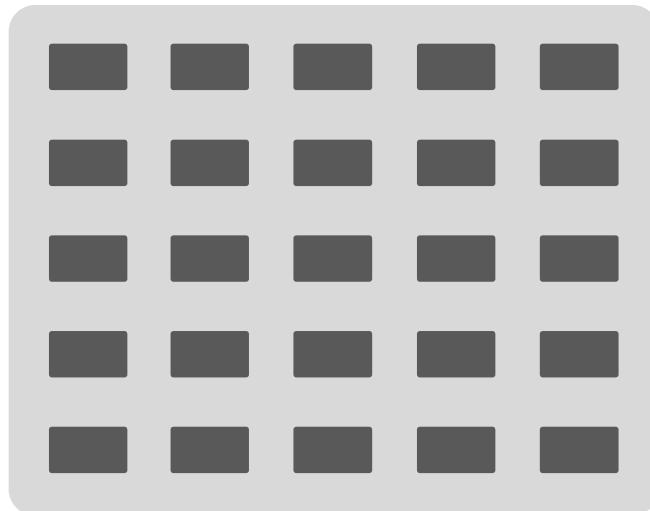
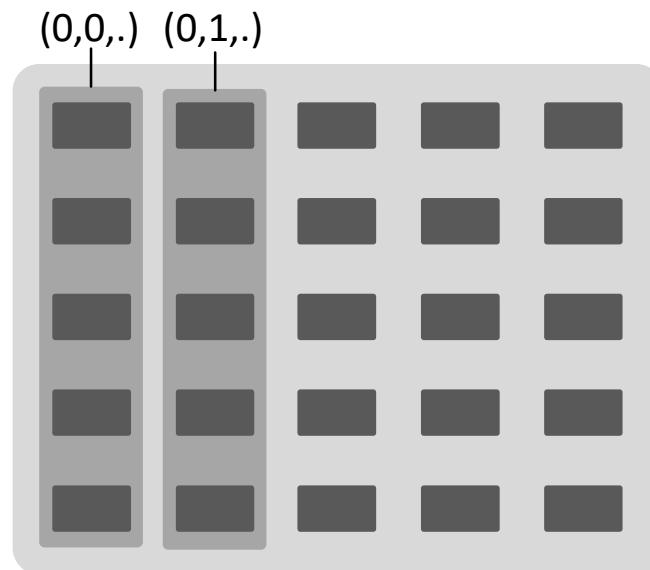
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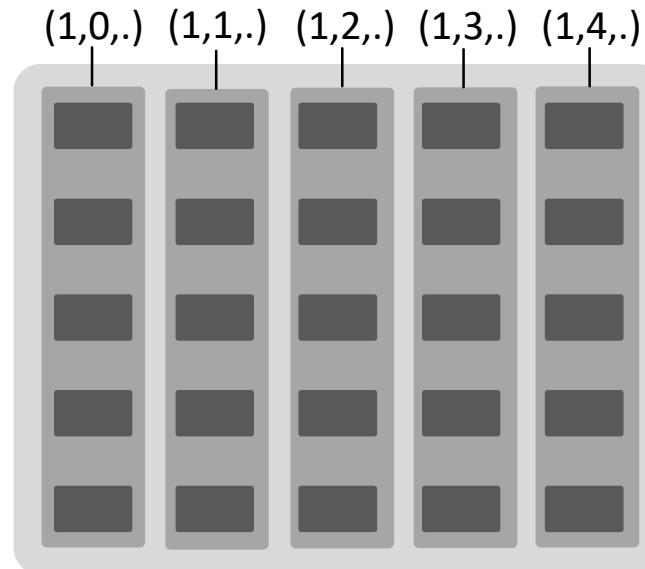
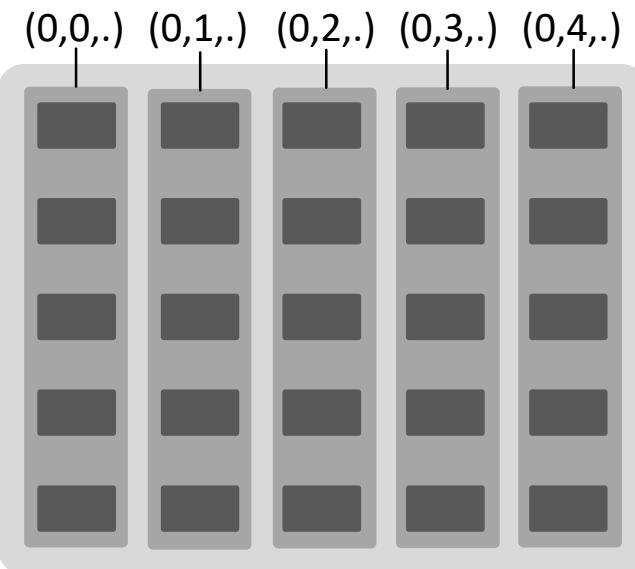
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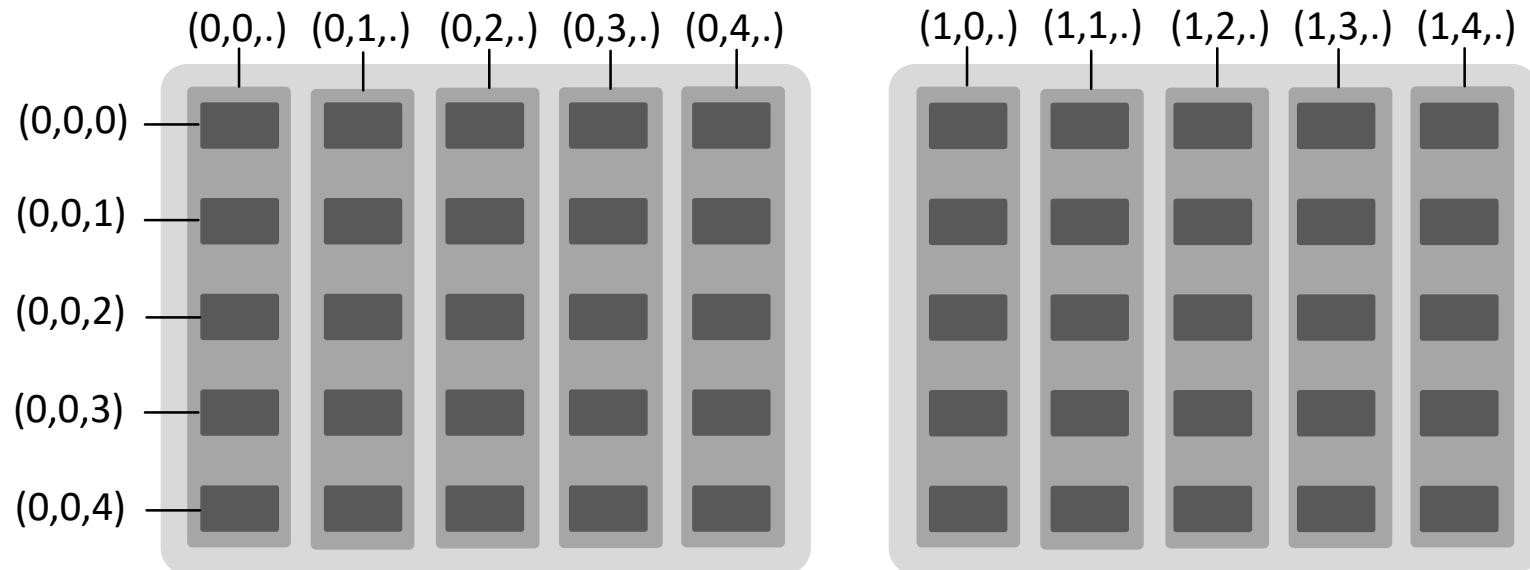
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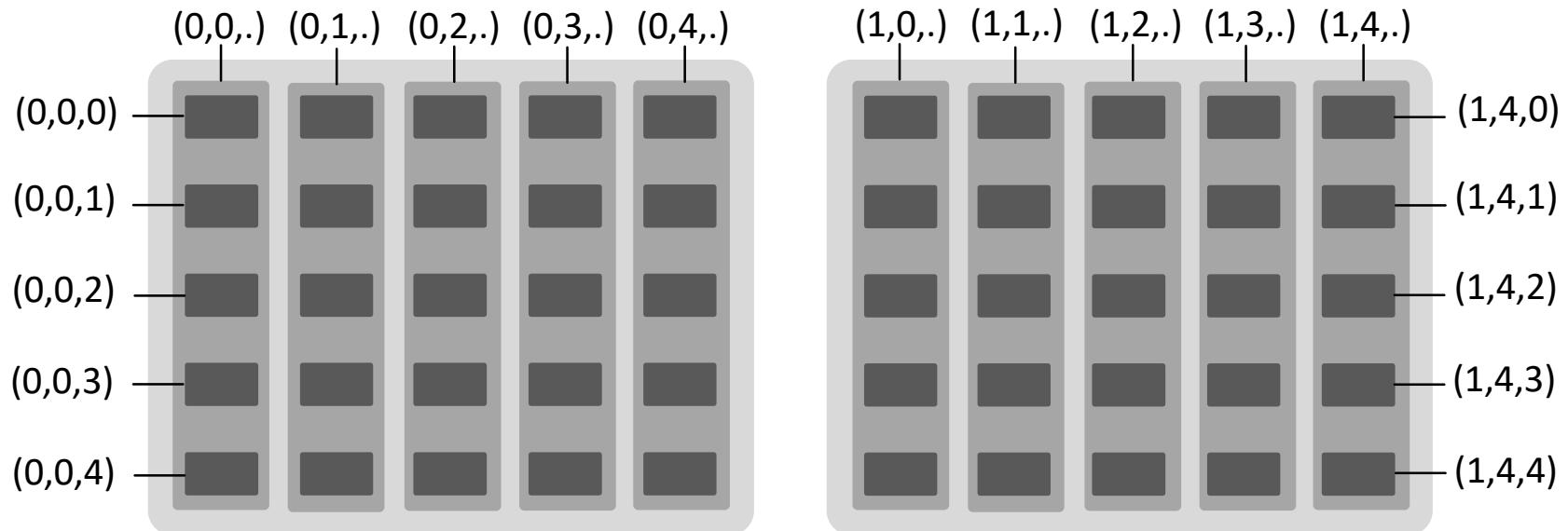
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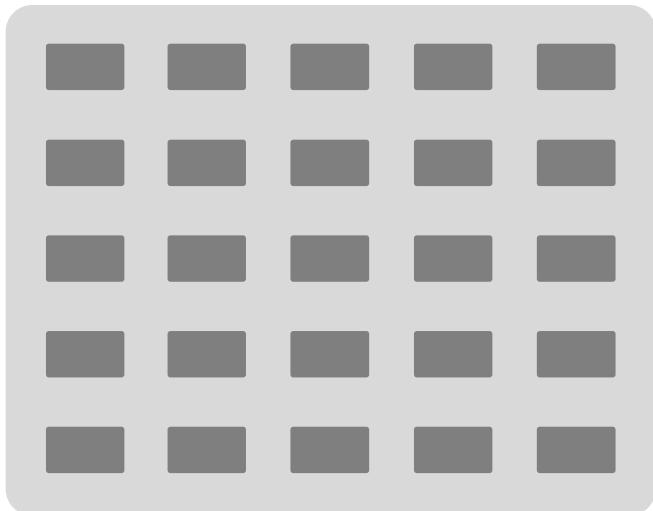
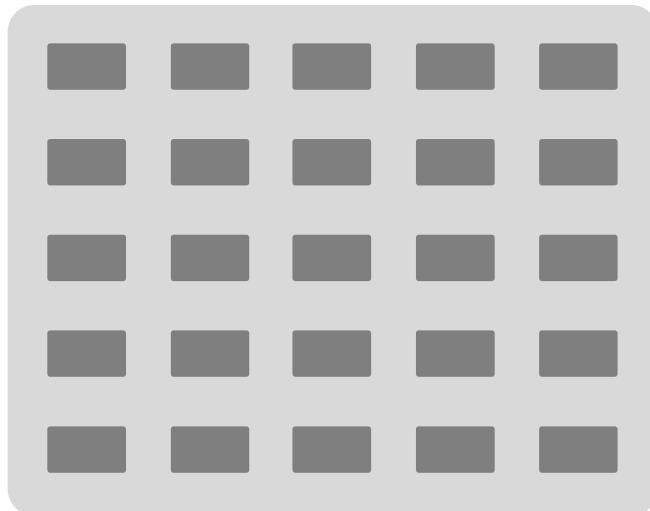
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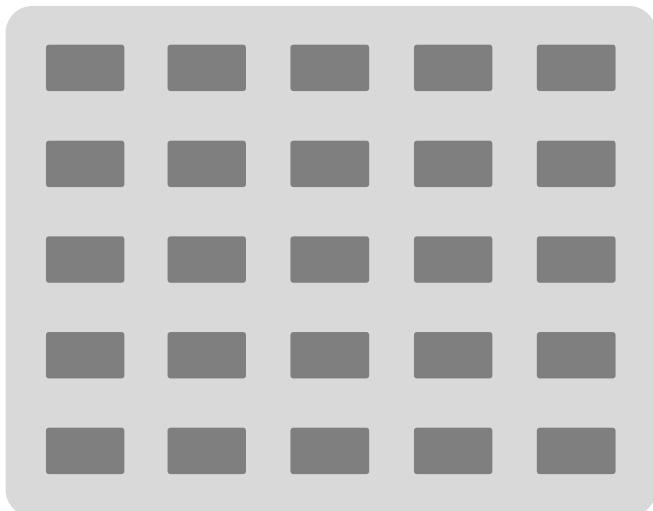
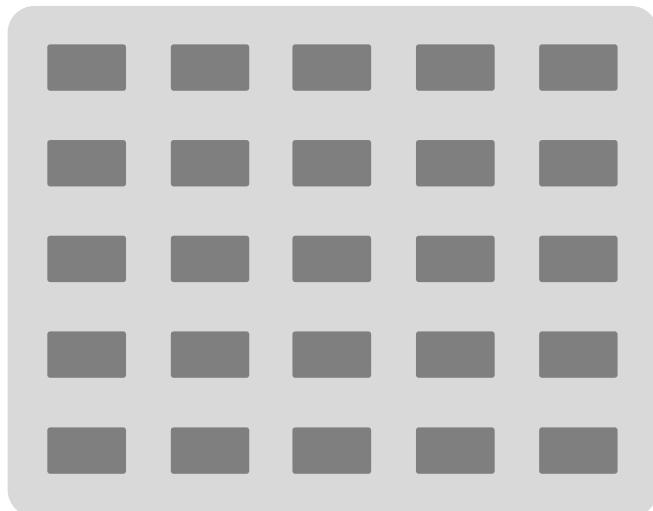
4 Find primitive element  $\xi$



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$\xi \in \mathcal{F}_q$  generates  $\mathcal{F}_q$ :

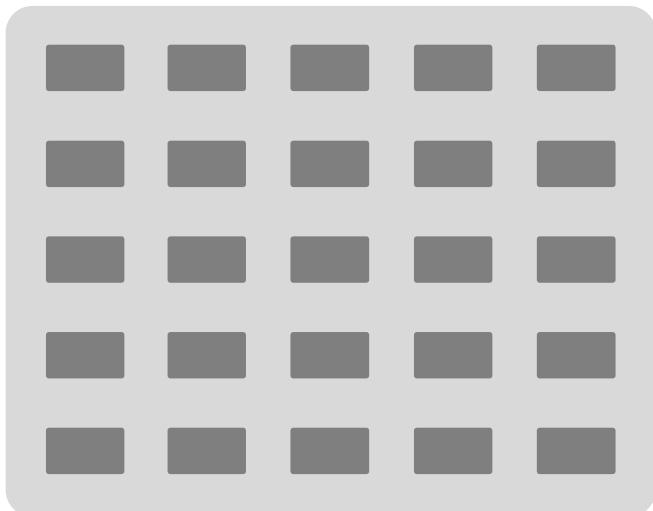
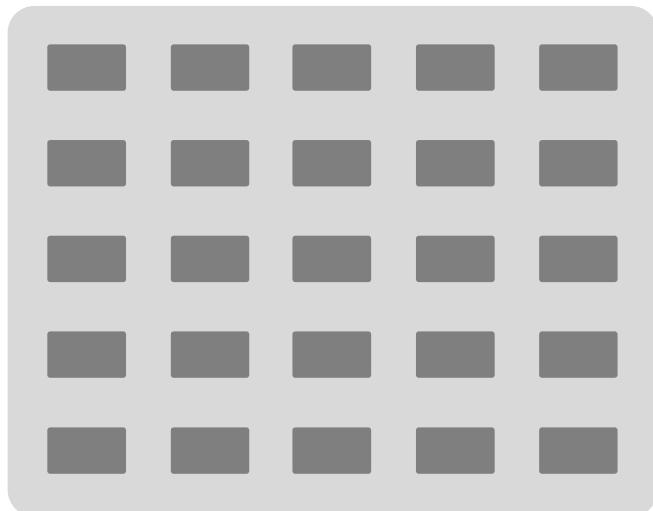


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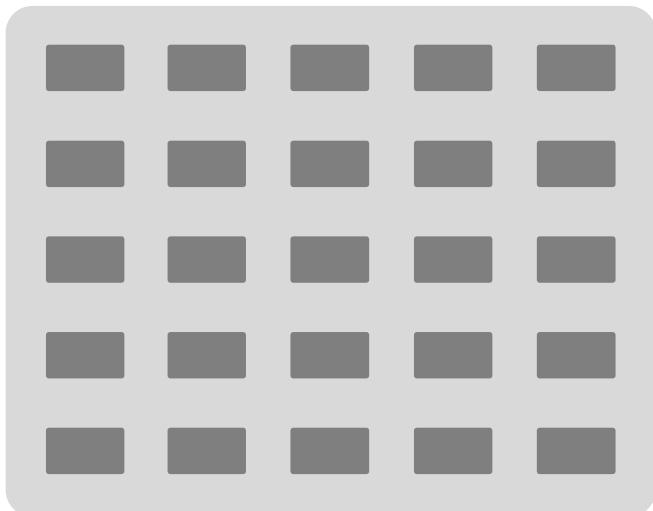
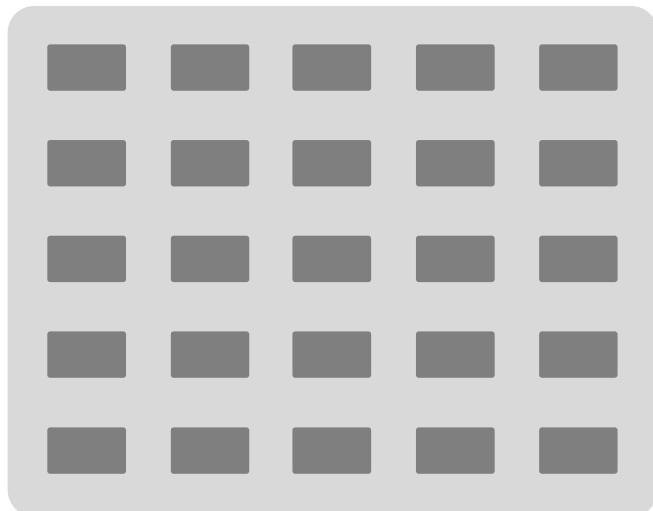
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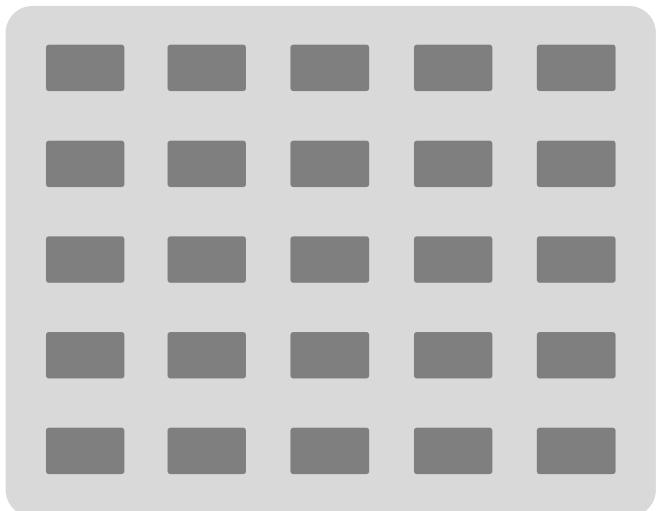
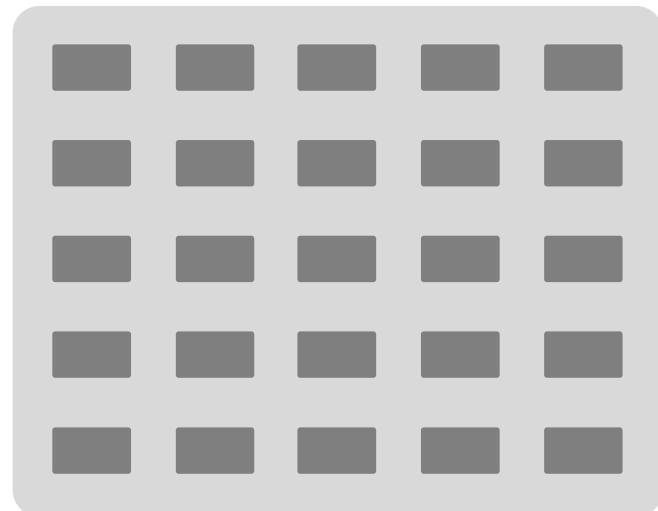
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E Example:  $q = 5$

$$\mathcal{F}_5 = \{0,1,2,3,4\}$$



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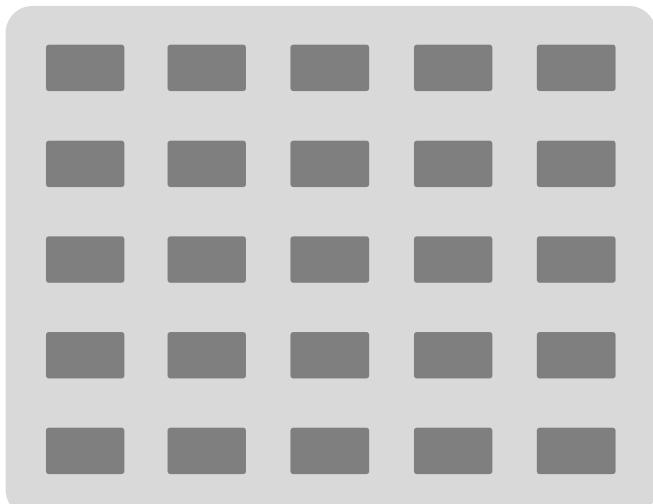
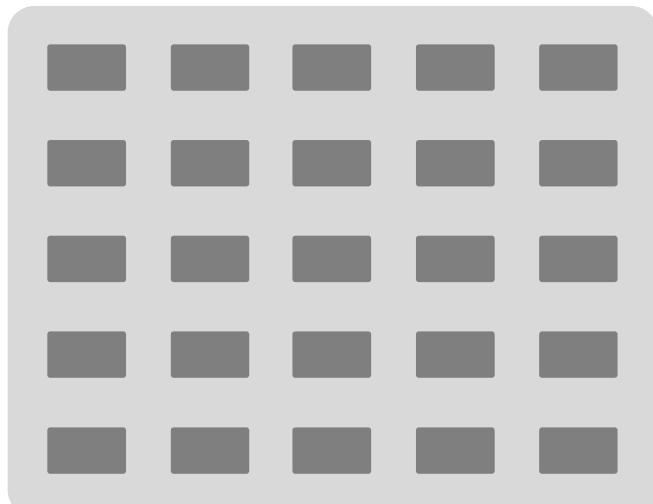
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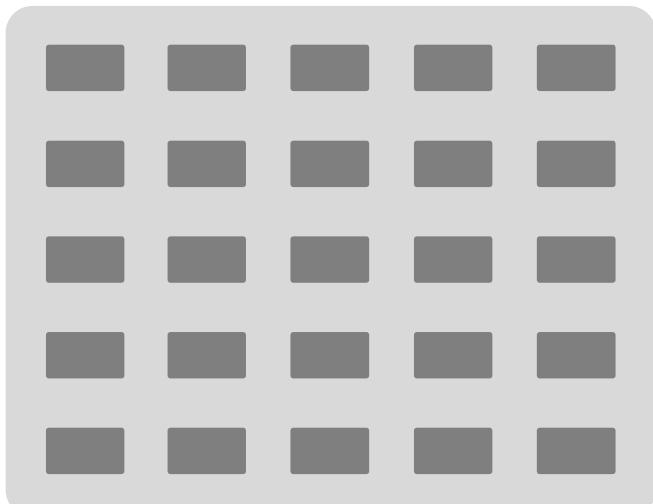
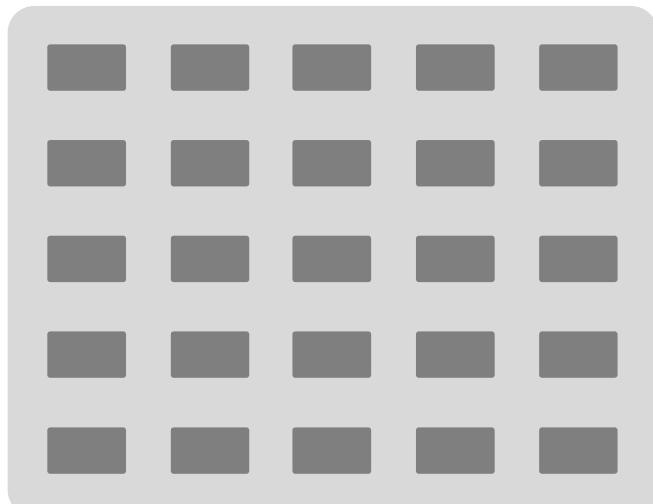
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$$1 = \xi^4 \bmod 5 = \\ 2^4 \bmod 5 = 16 \bmod 5$$



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$$X = \{1, \xi^2, \dots, \xi^{q-3}\}$$

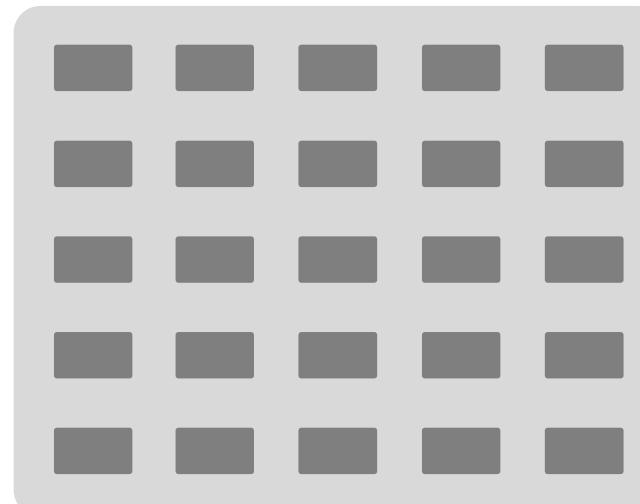
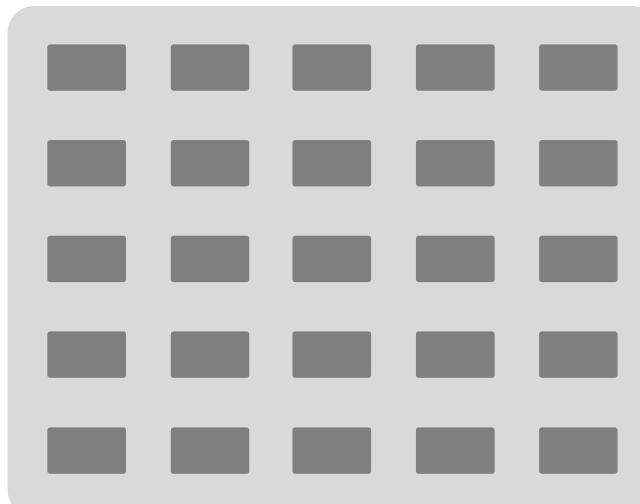
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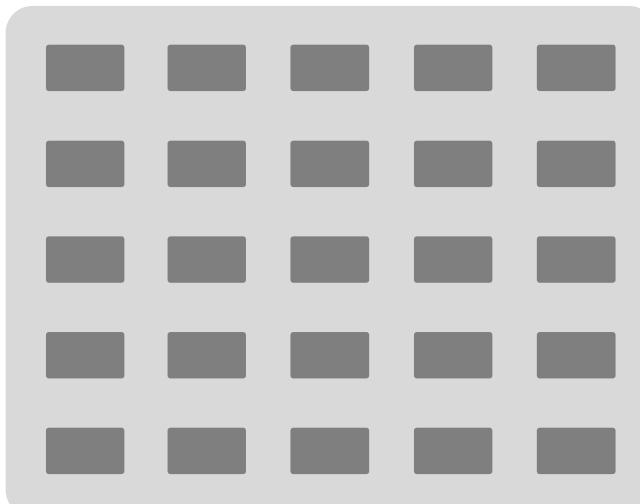
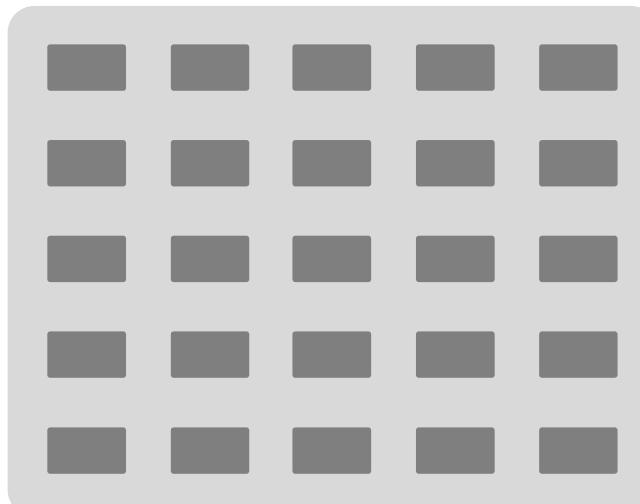
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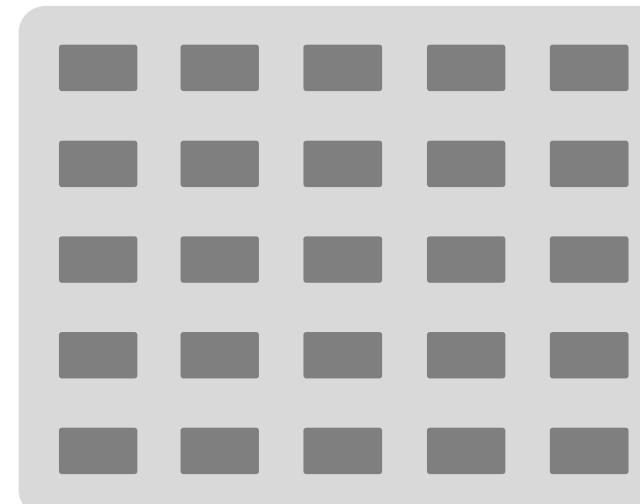
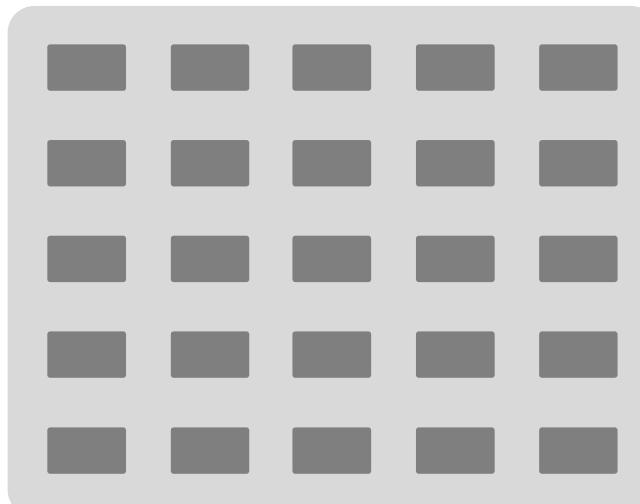
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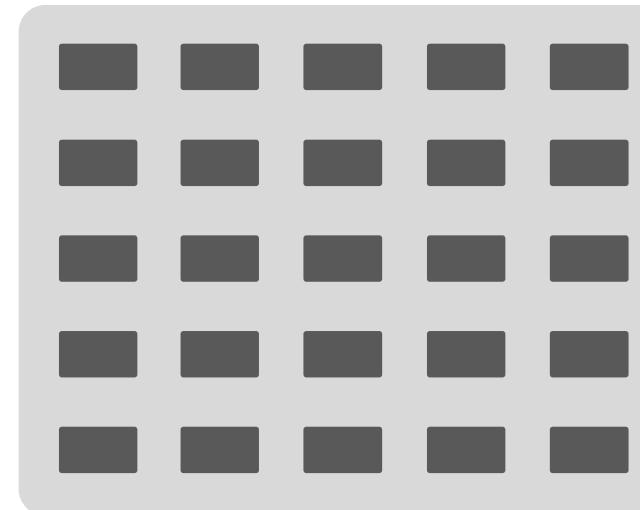
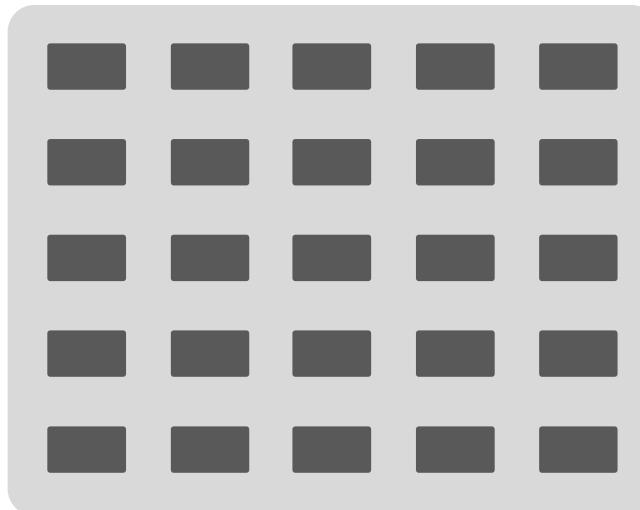
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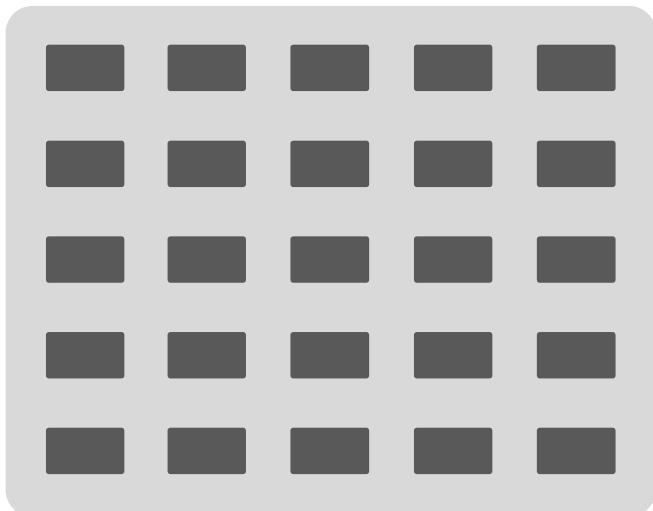
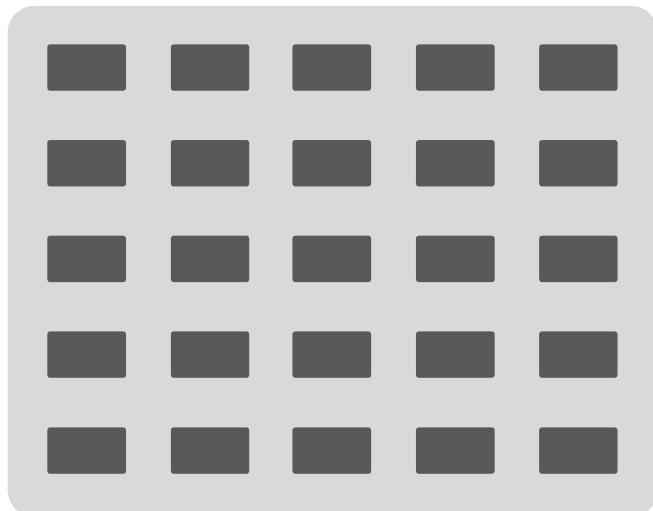


# DIAMETER-2 SLIM FLY



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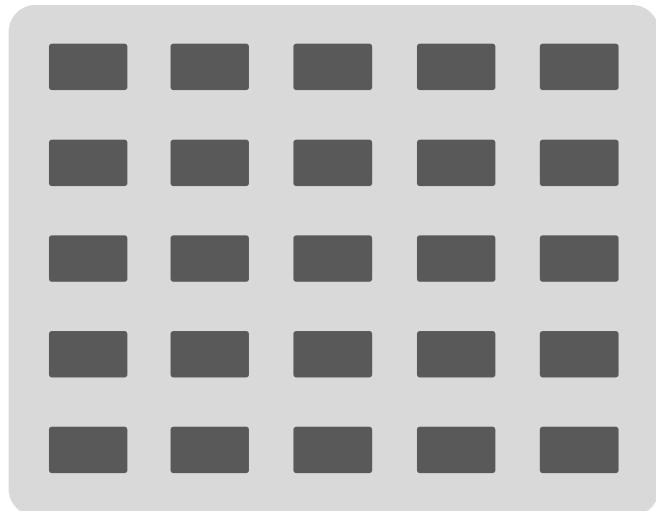
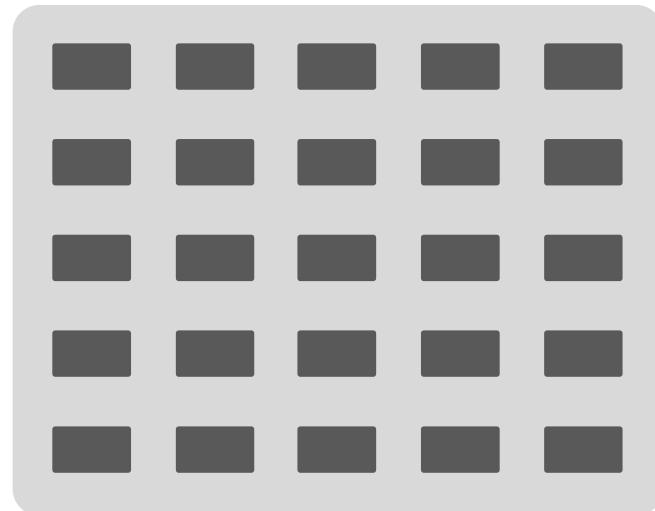
6 *Intra-group connections*



# DIAMETER-2 SLIM FLY

## 6 *Intra-group connections*

Two routers in one group are connected iff their “vertical Manhattan distance” is an element from:

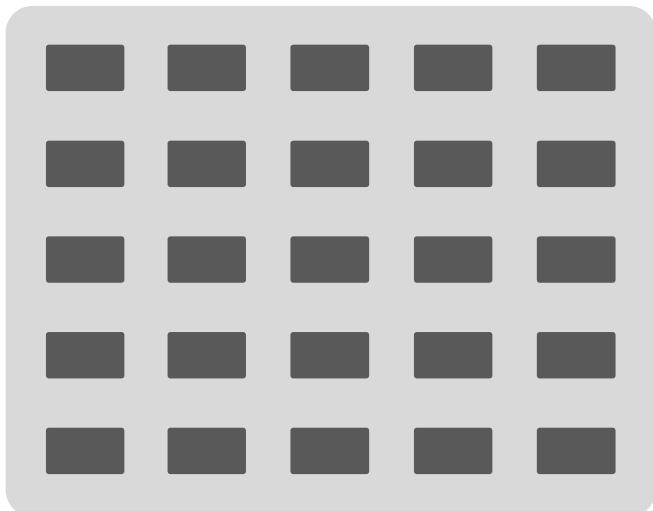
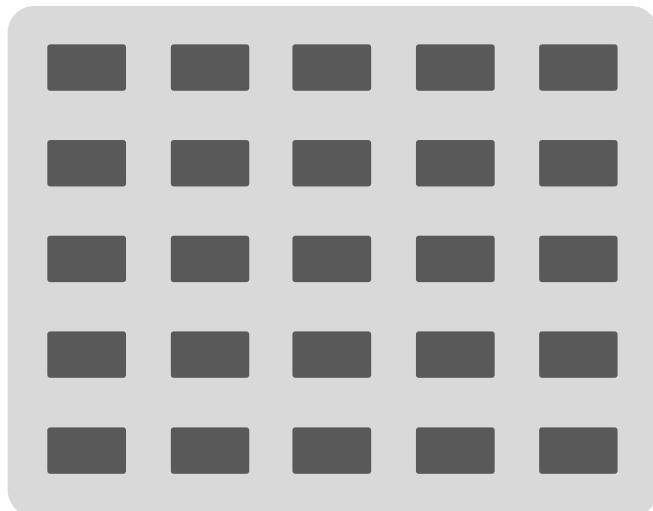


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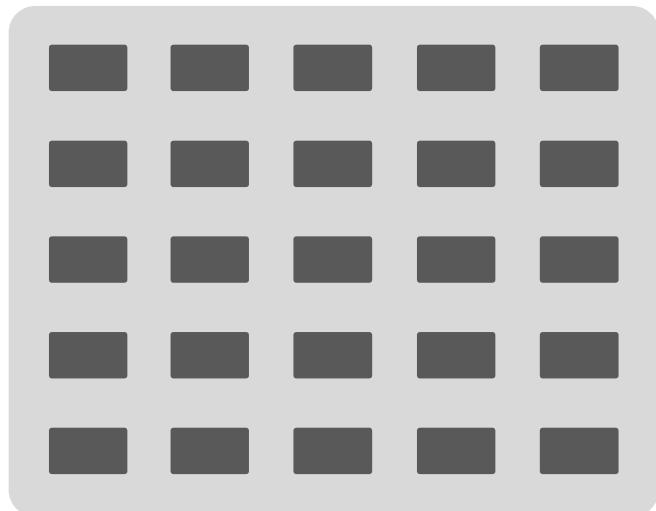
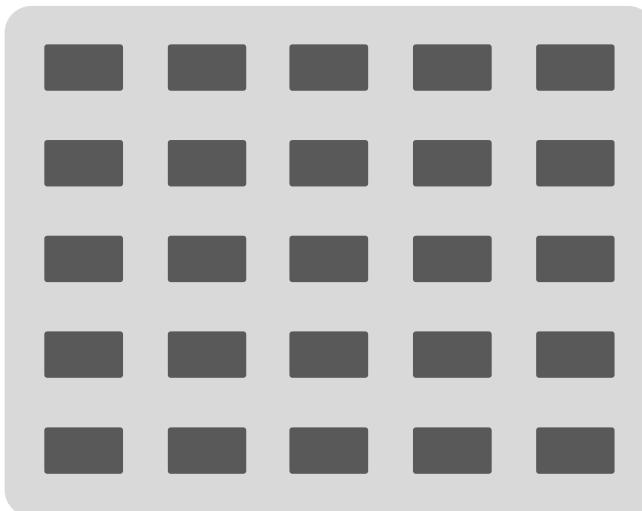
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## E Example: $q = 5$



# DIAMETER-2 SLIM FLY

## 6 *Intra-group connections*

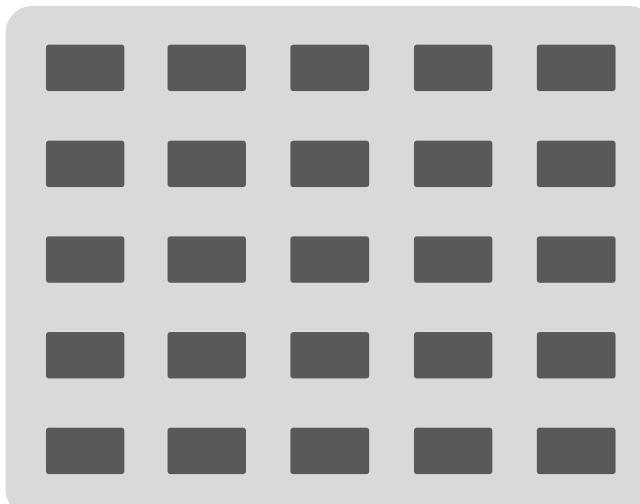
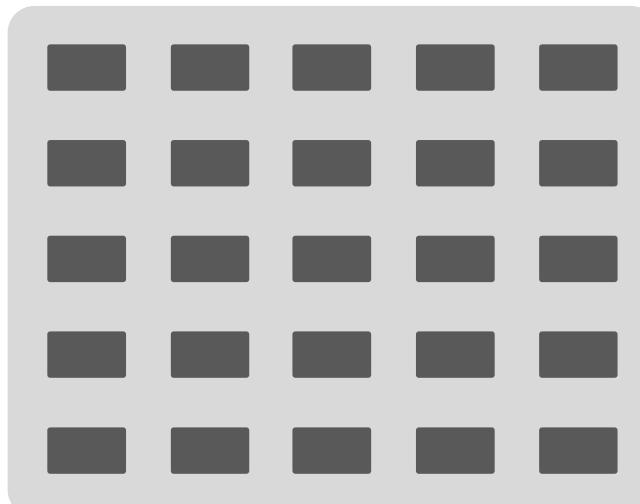
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Take Routers  $(0,0,.)$



# DIAMETER-2 SLIM FLY

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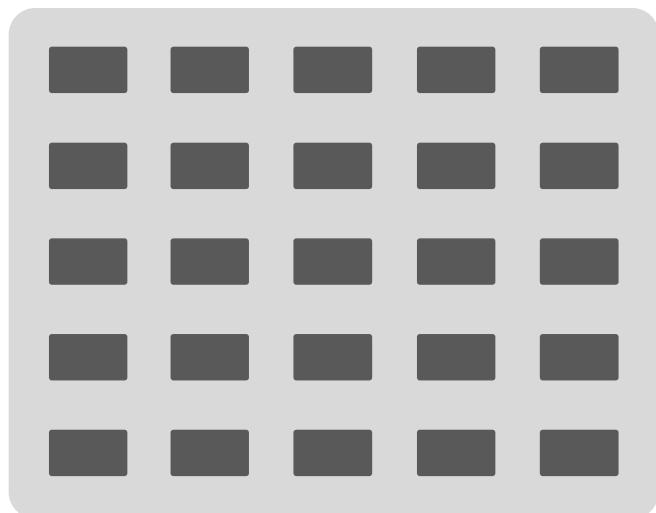
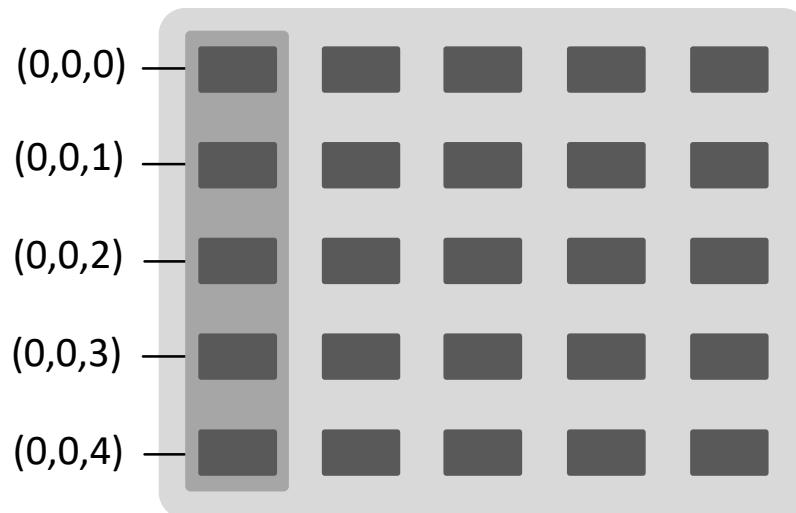
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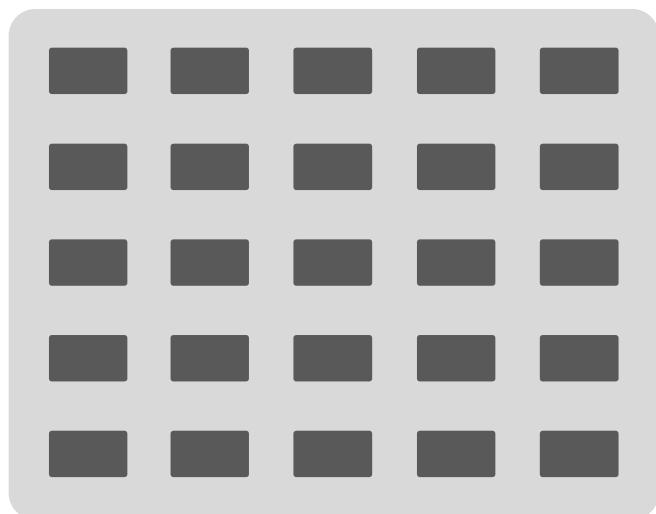
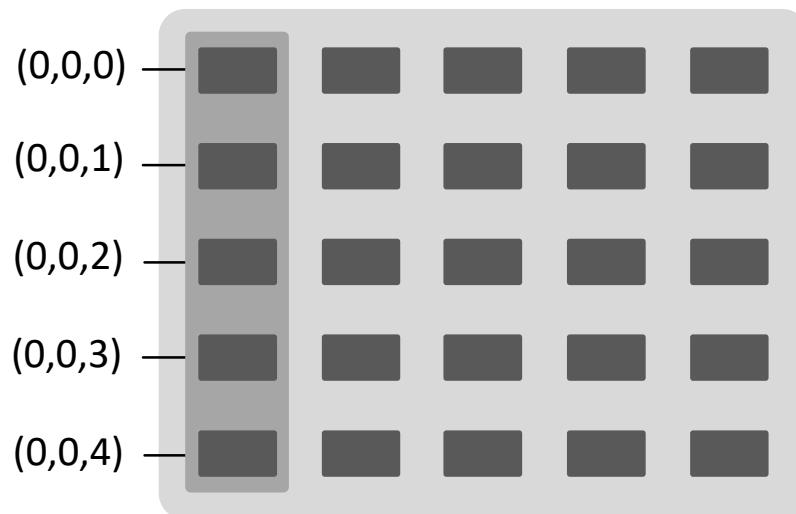
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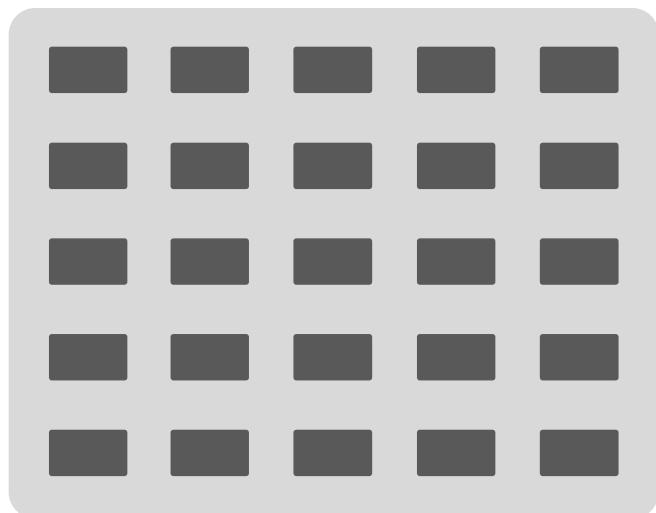
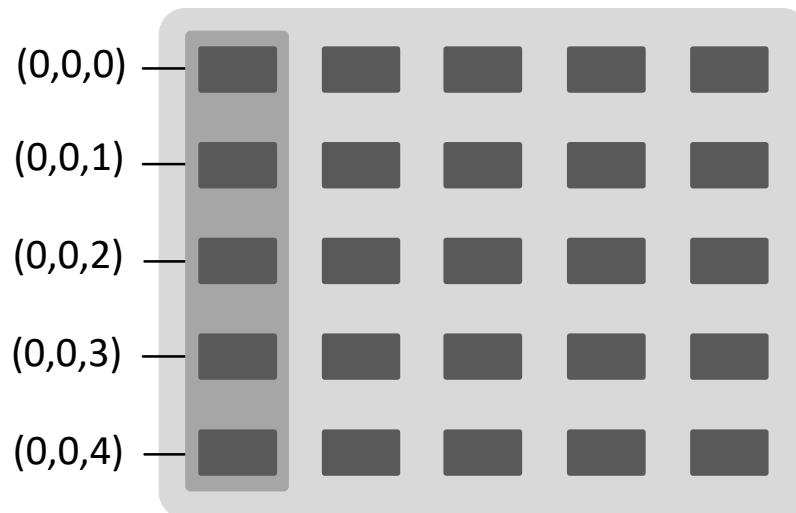
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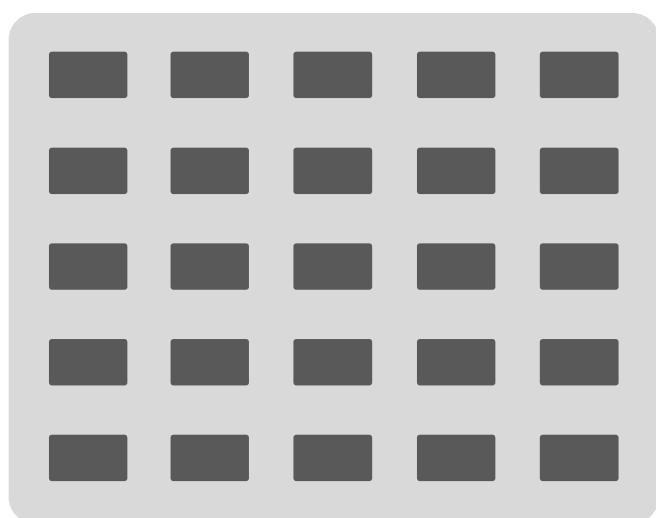
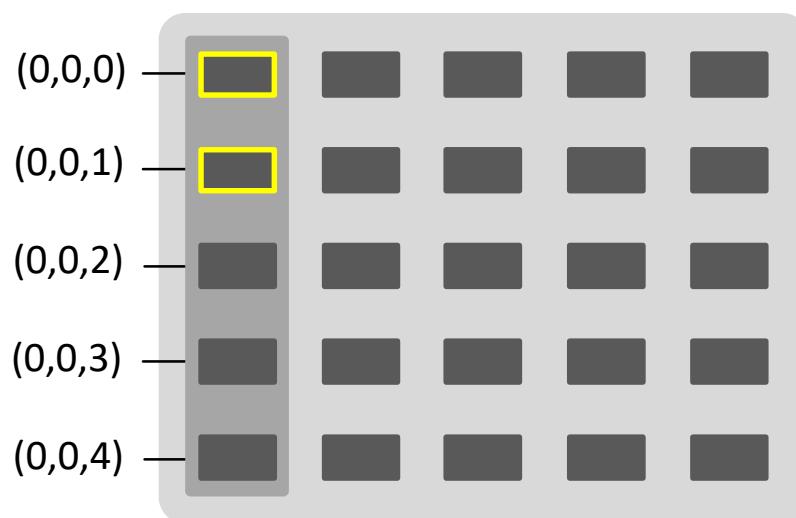
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# DIAMETER-2 SLIM FLY

## 6 Intra-group connections

Two routers in one group are connected iff their “vertical Manhattan distance” is an element from:

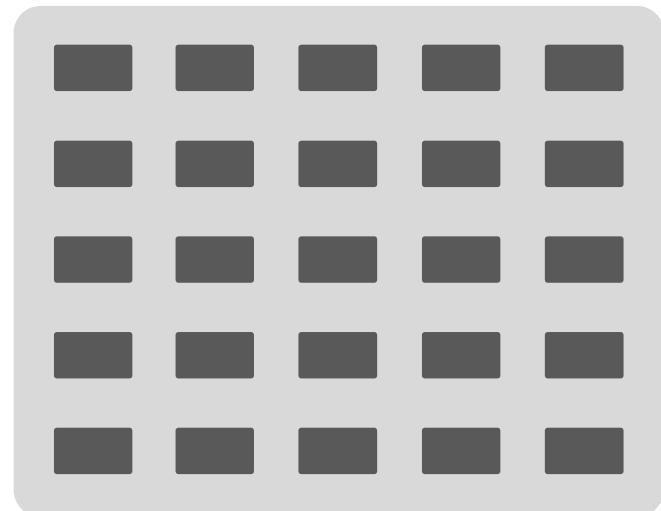
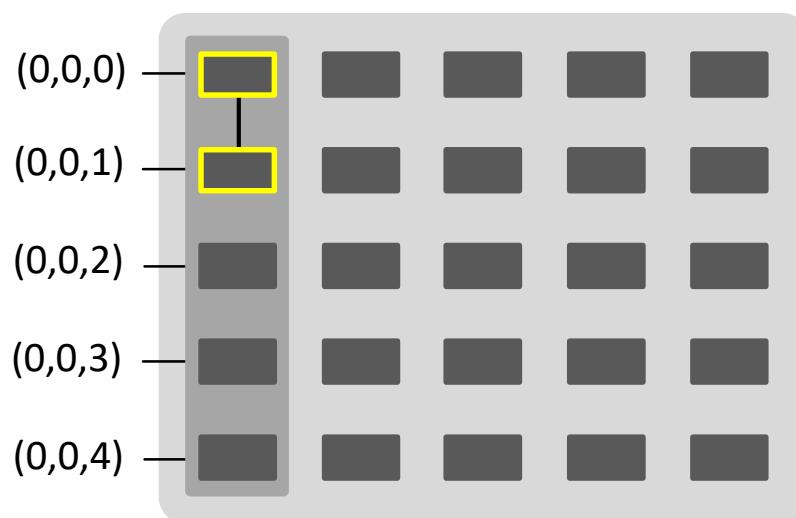
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## E Example: $q = 5$

Take Routers  $(0,0,.)$

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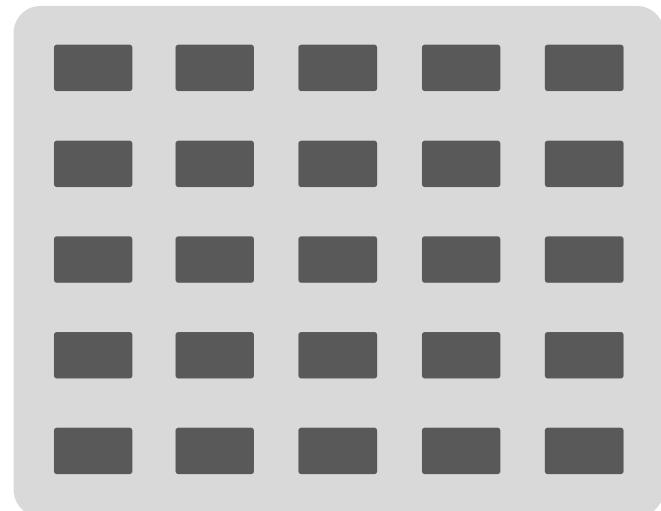
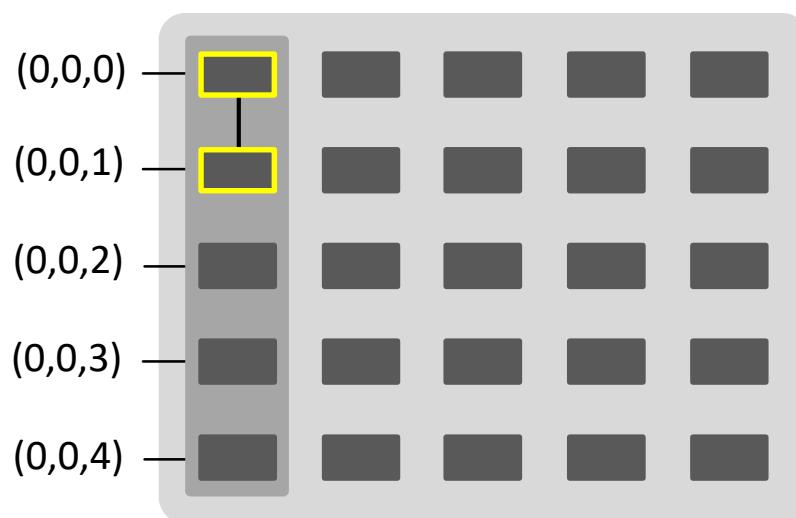
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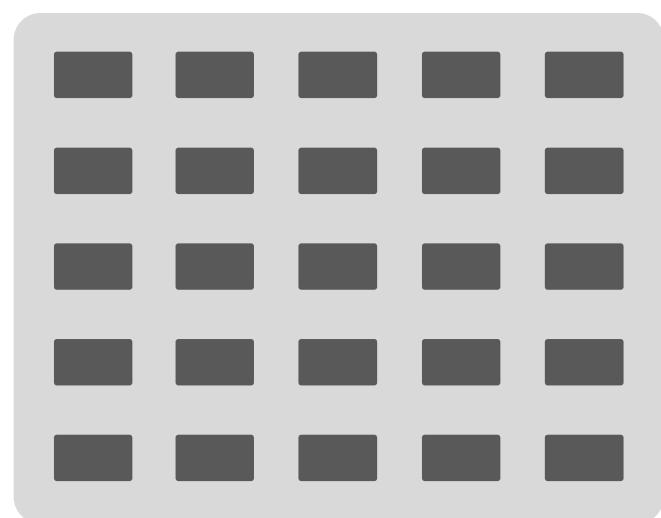
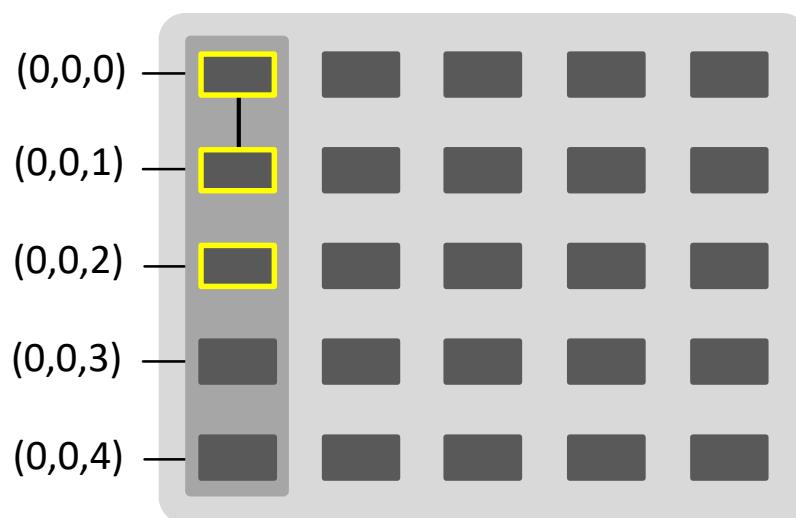
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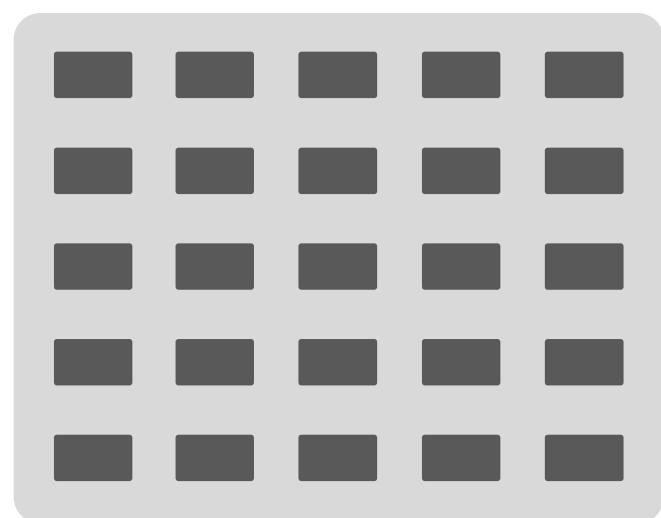
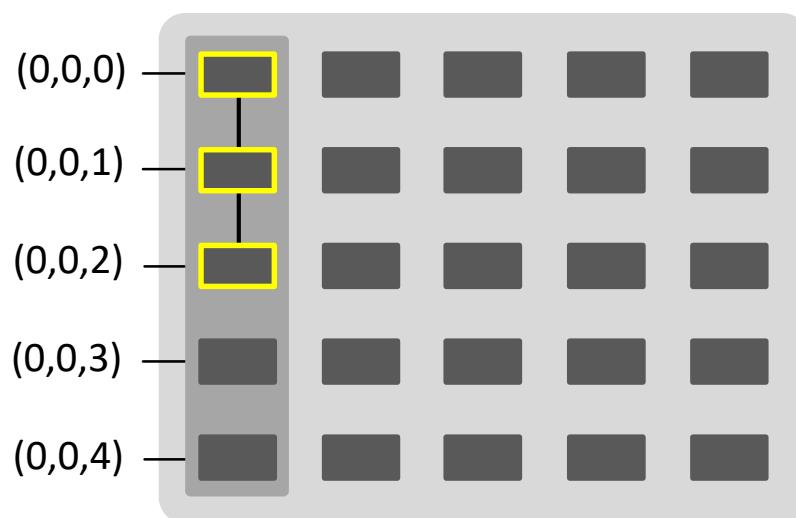
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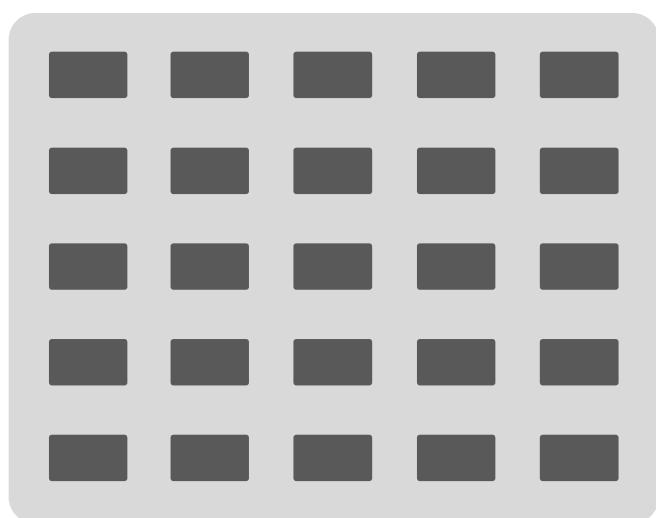
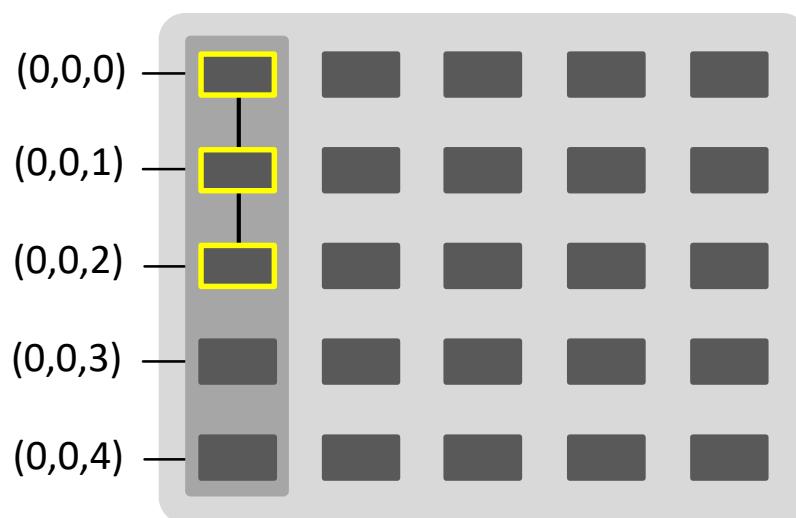
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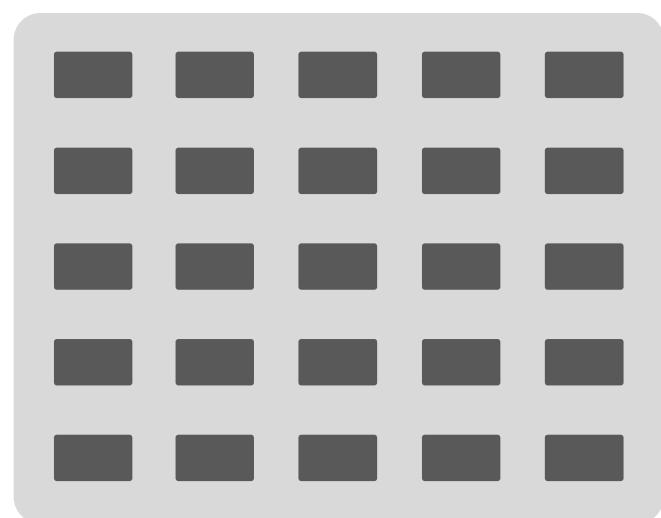
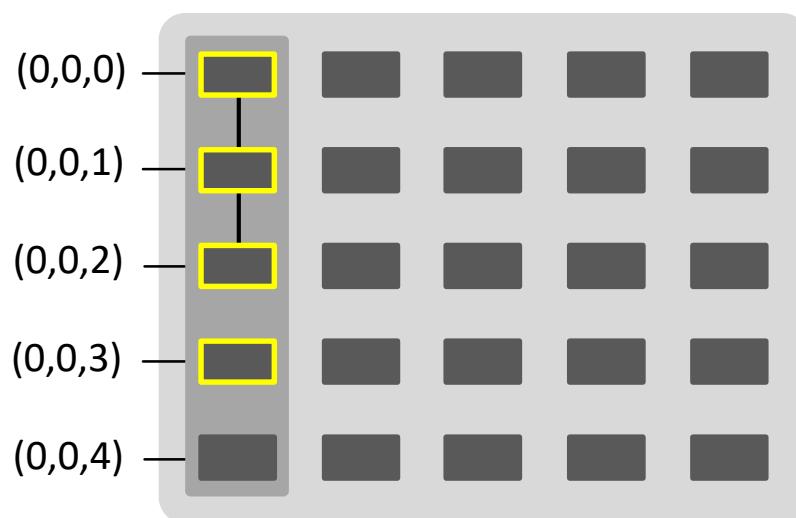
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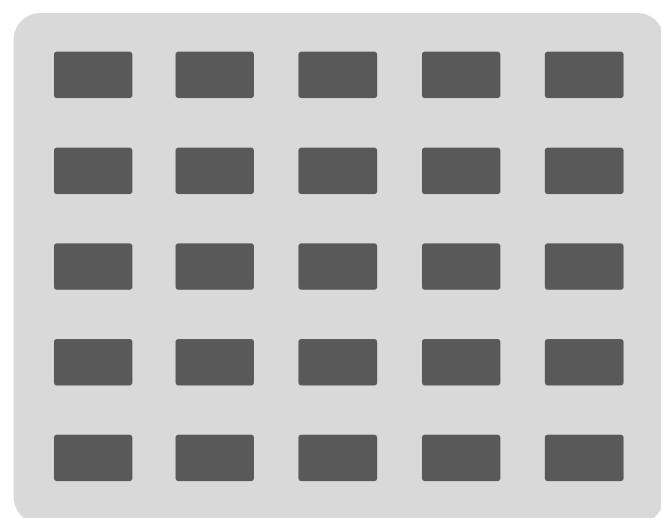
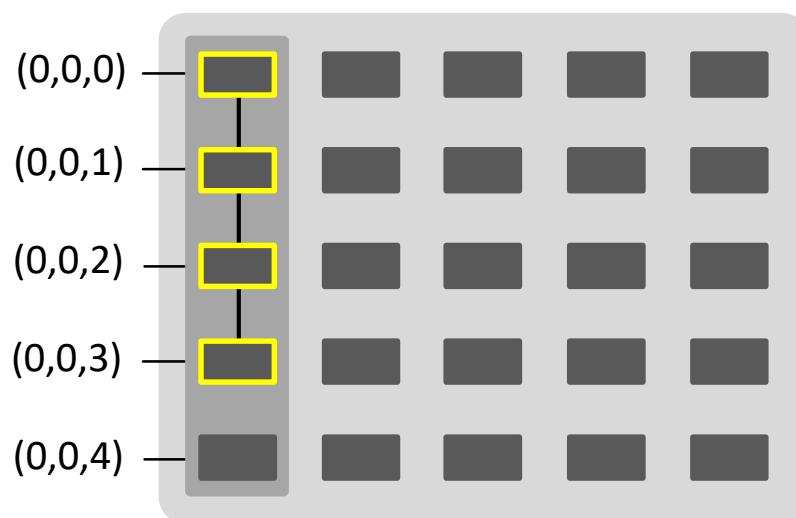
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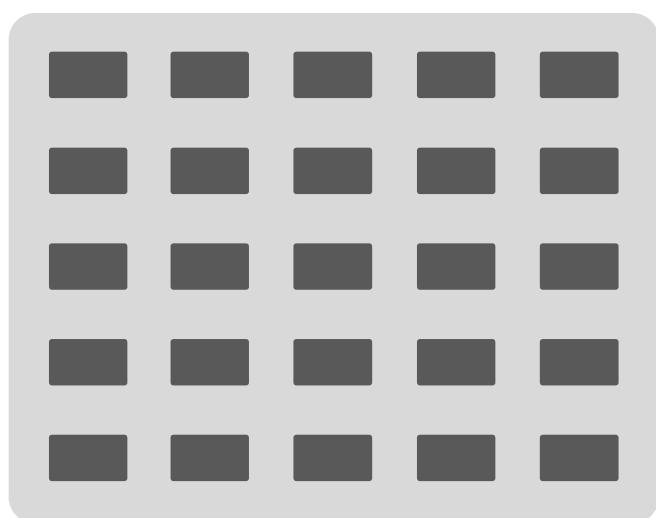
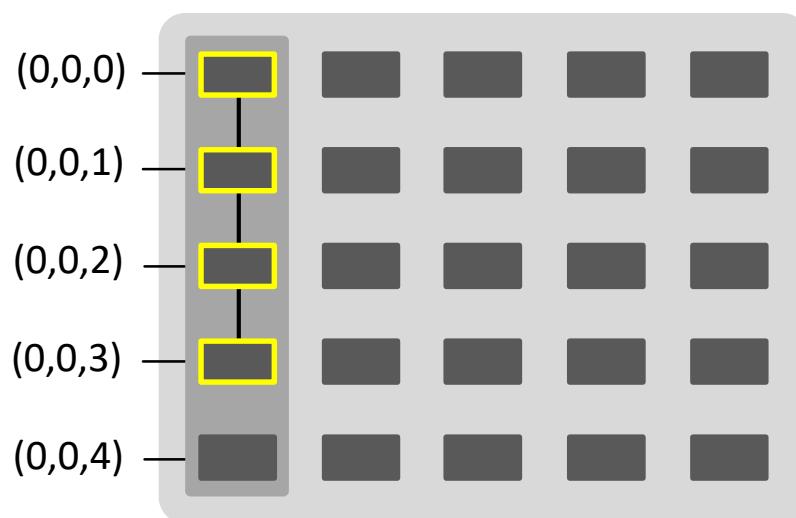
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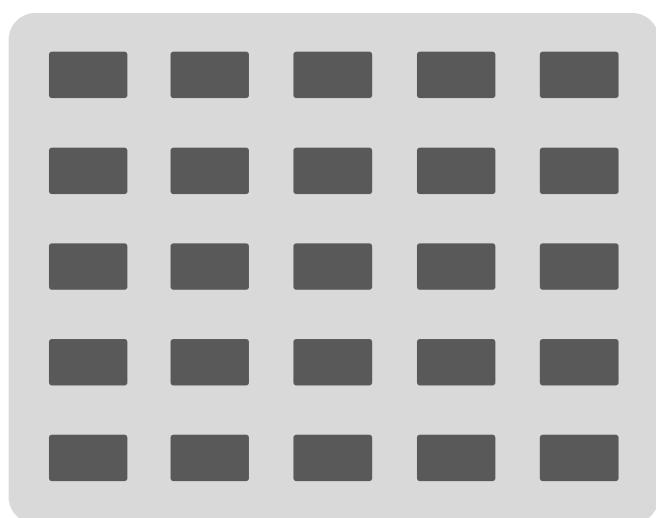
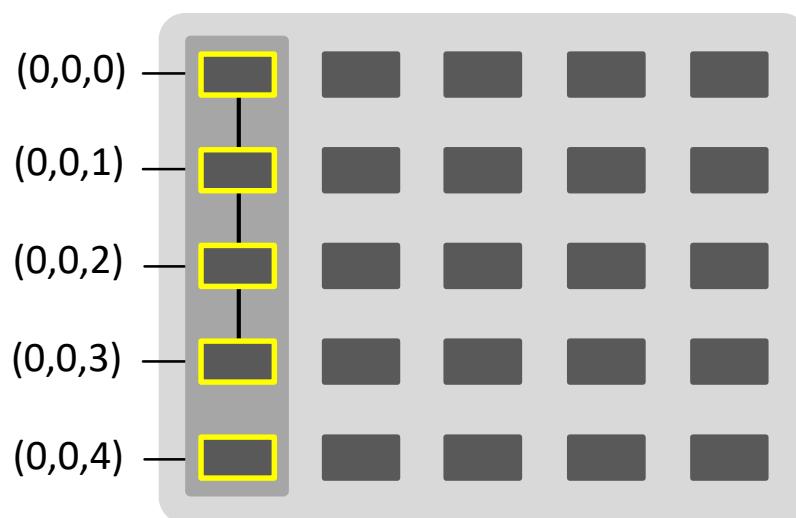
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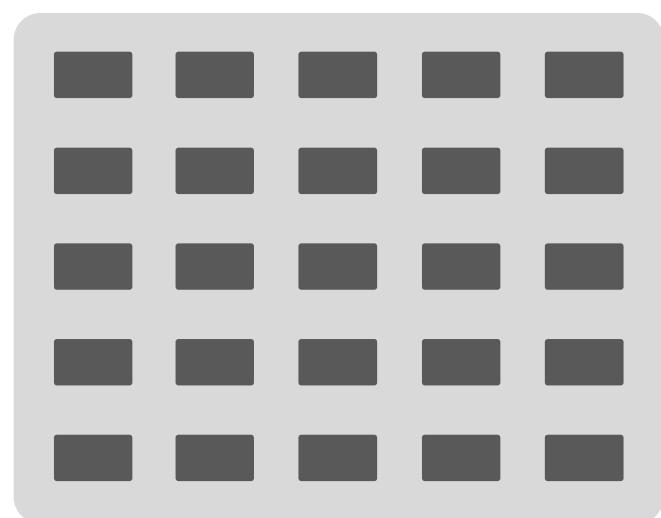
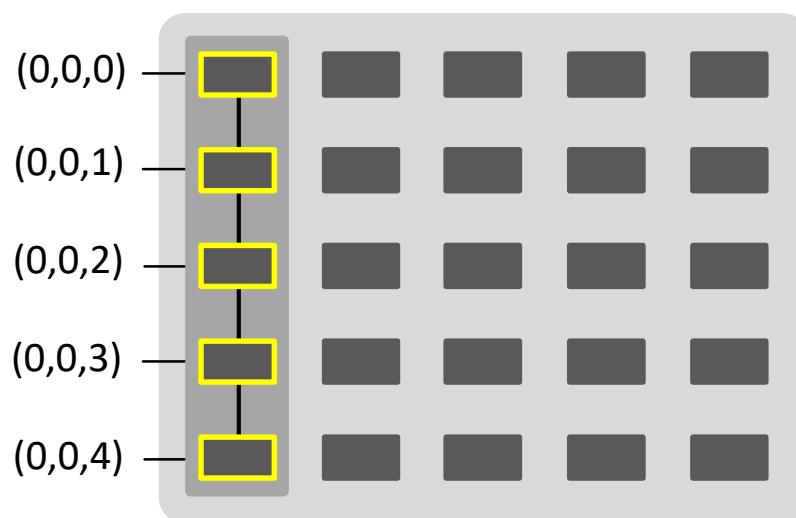
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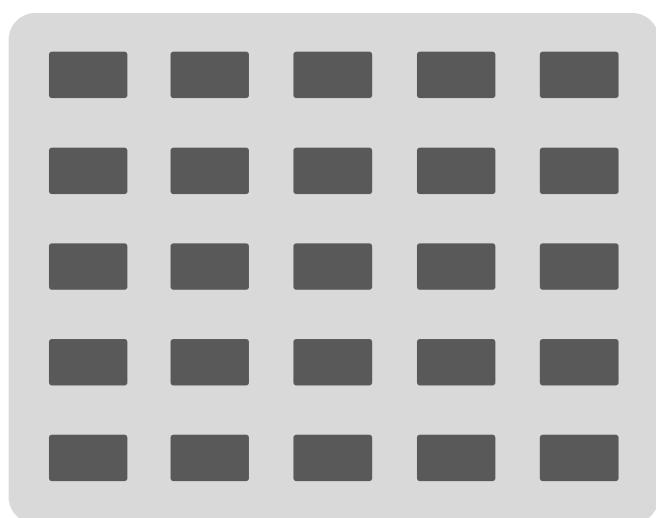
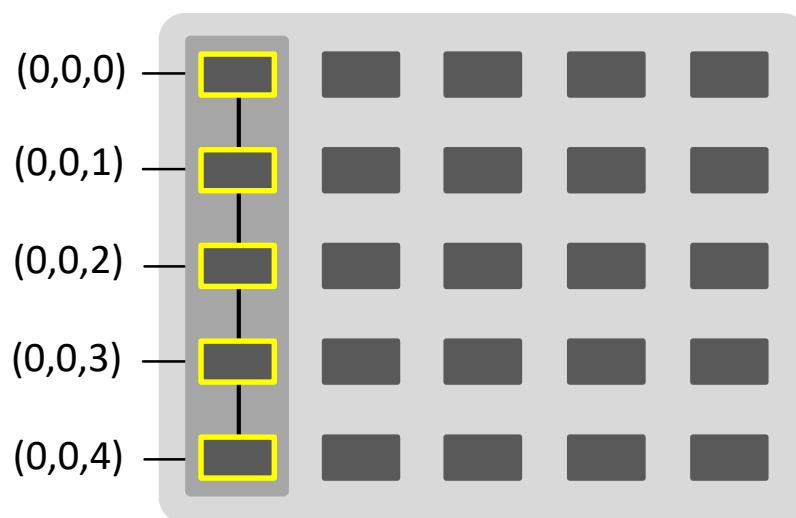
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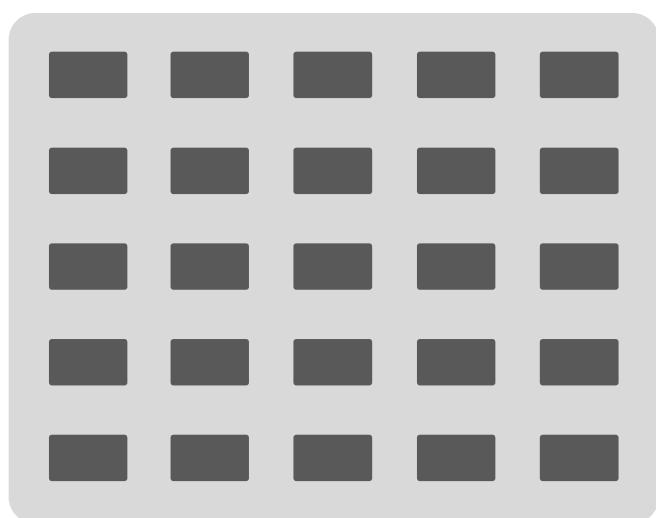
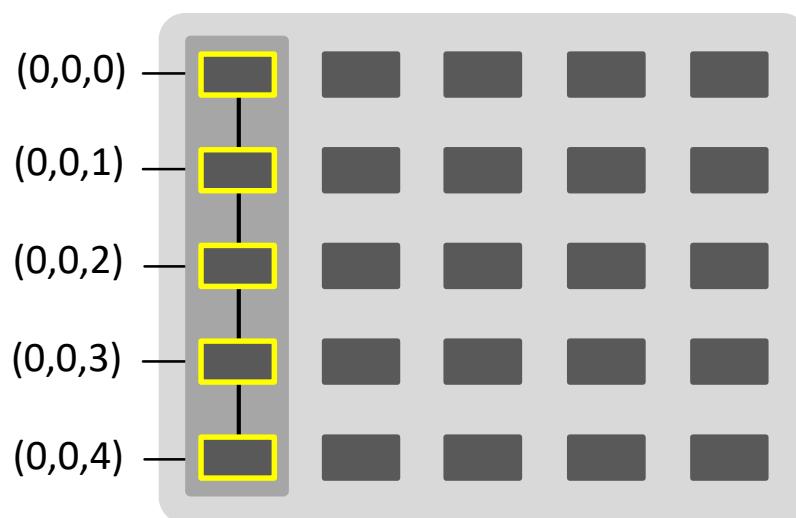
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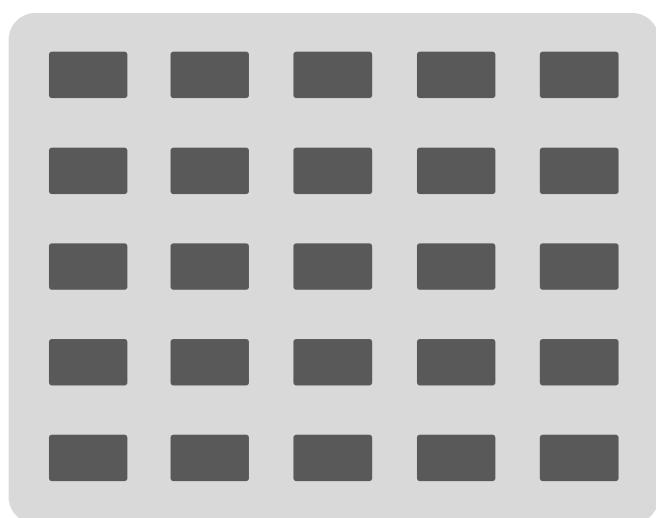
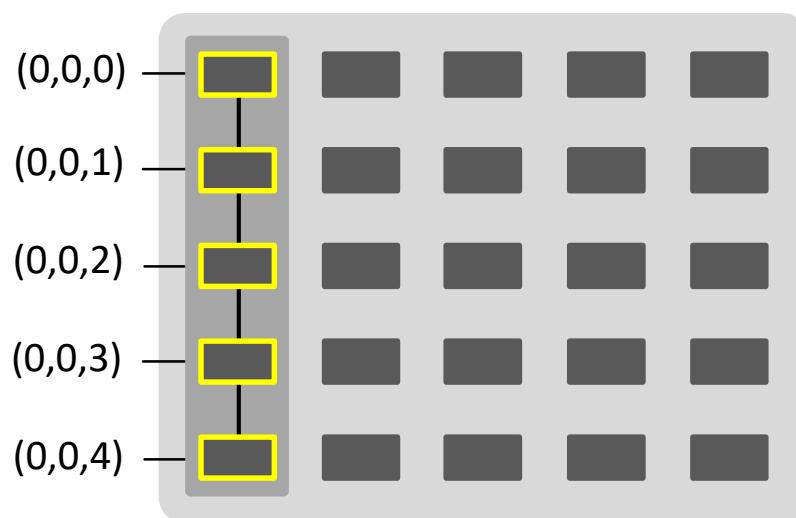
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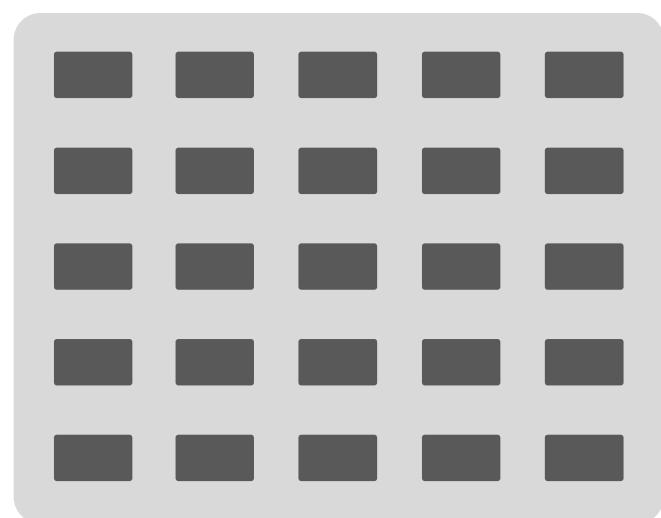
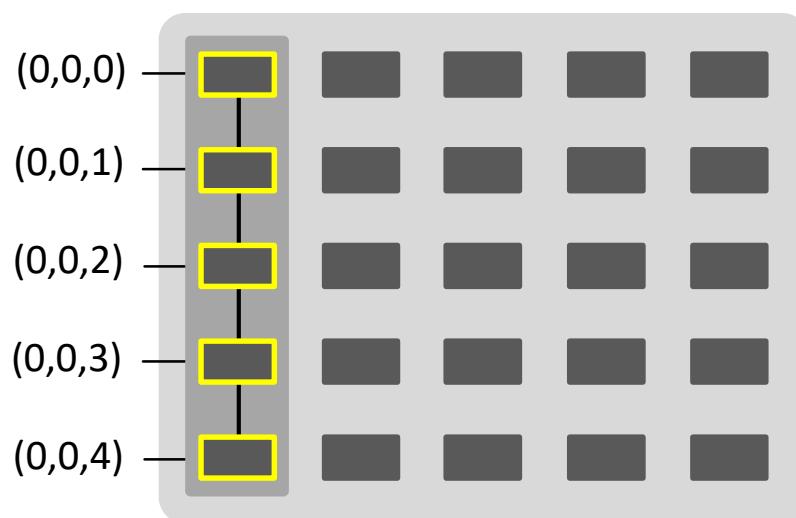
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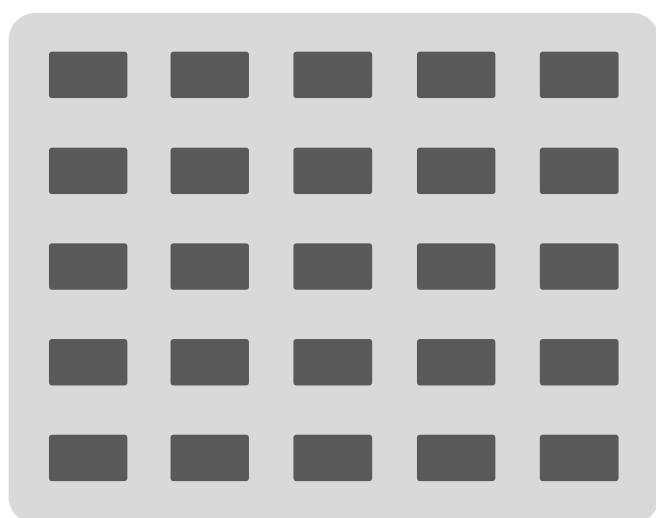
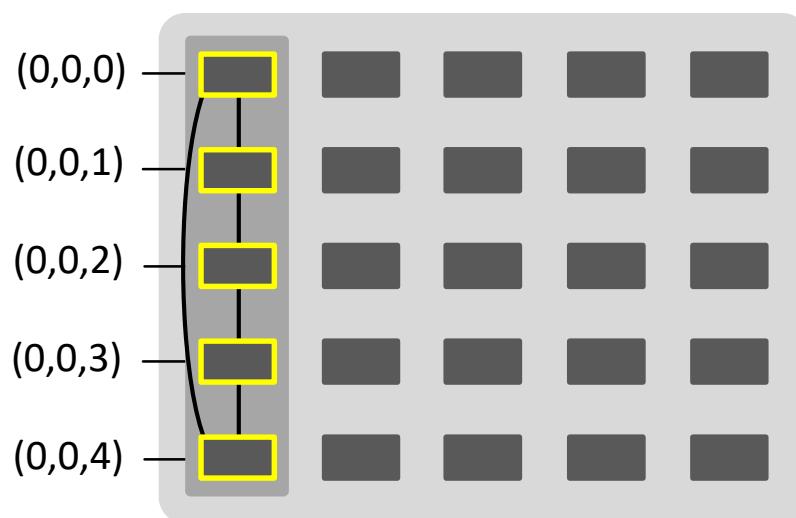
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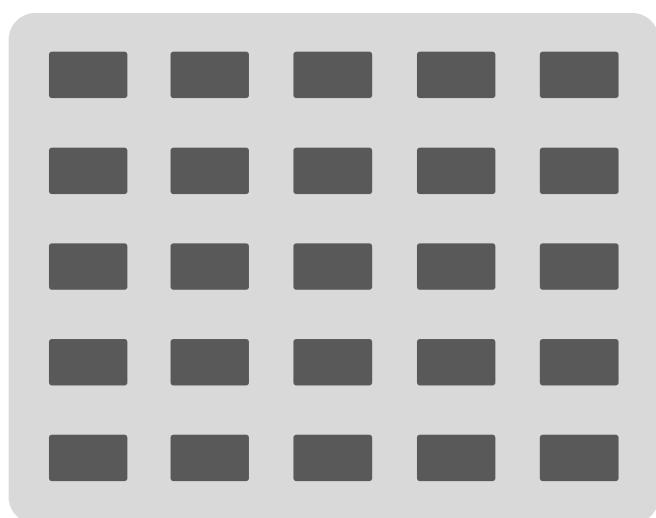
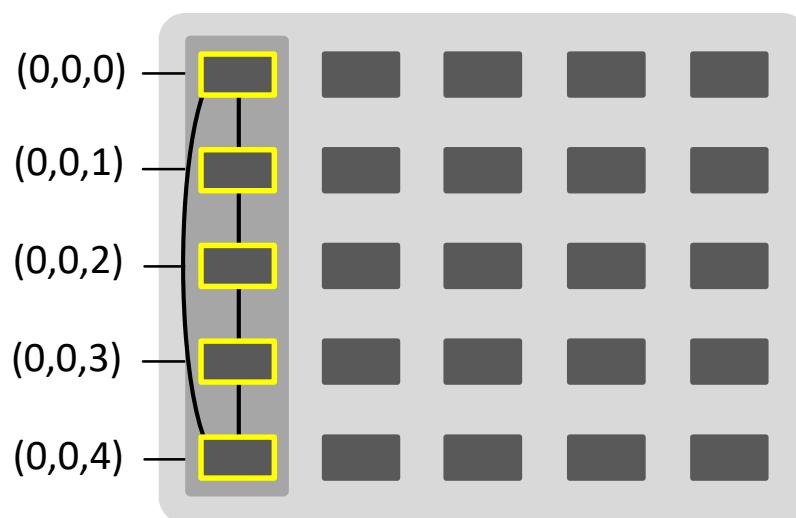
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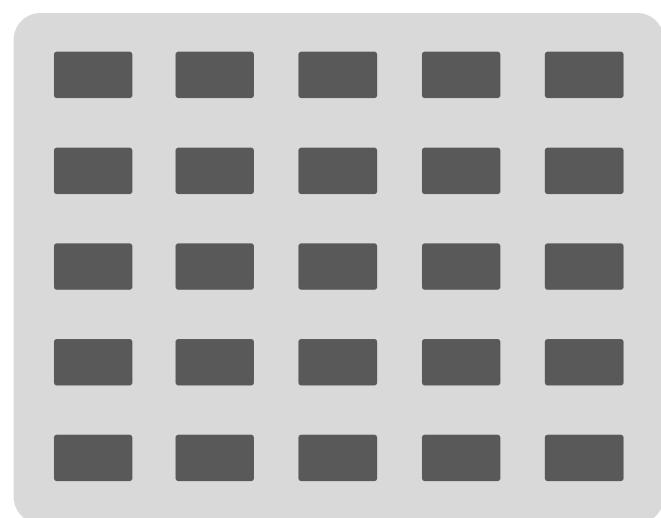
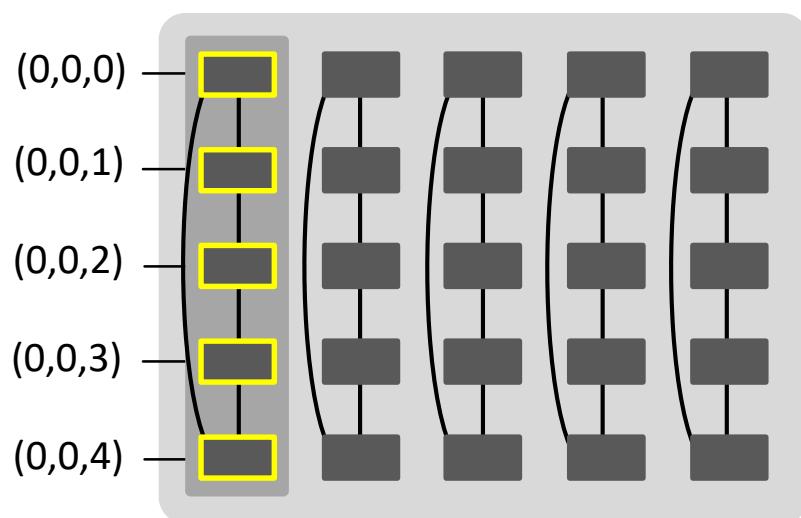
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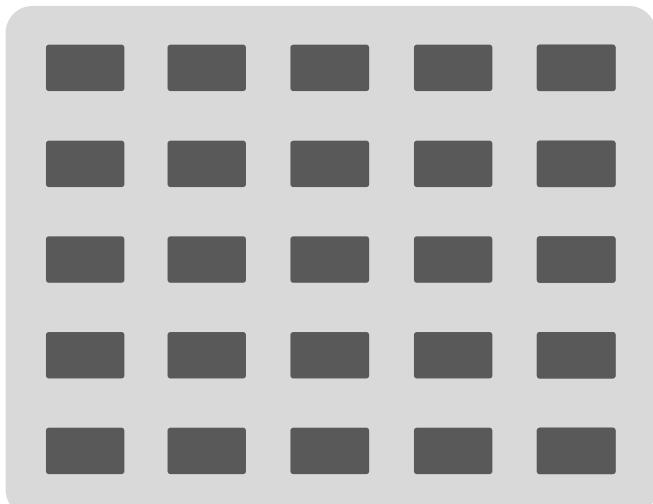
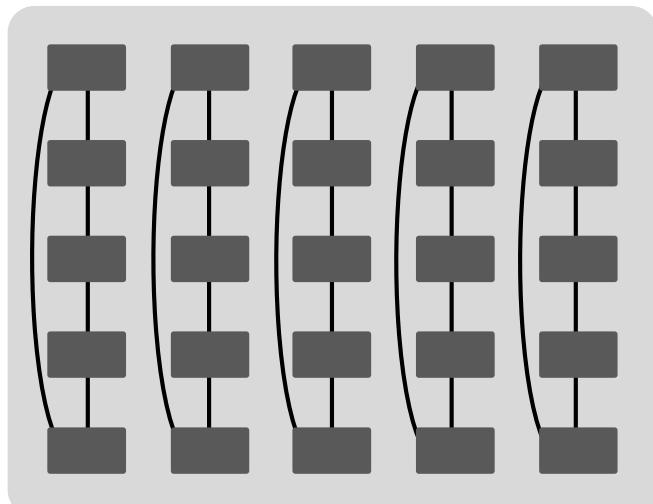
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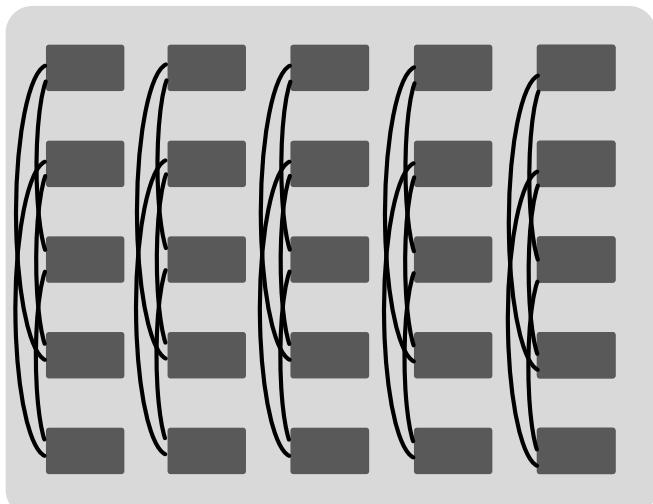
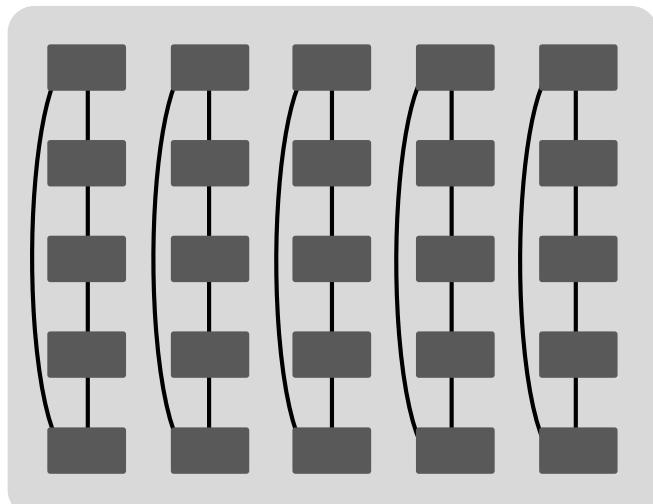
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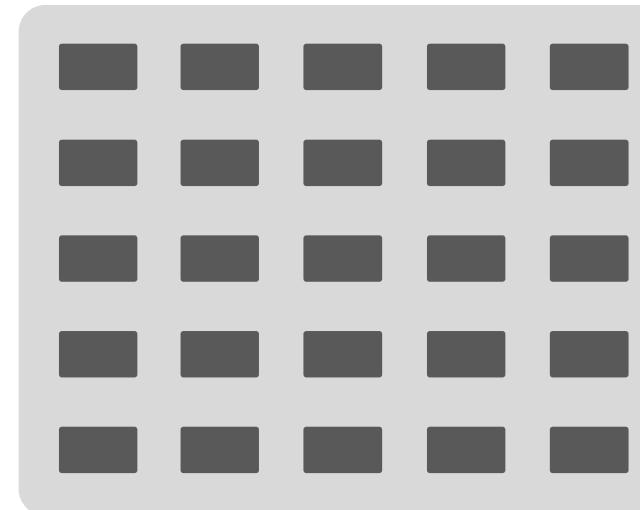
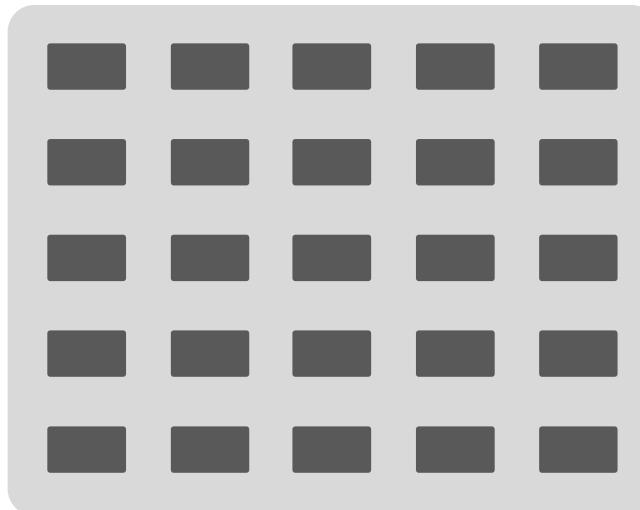
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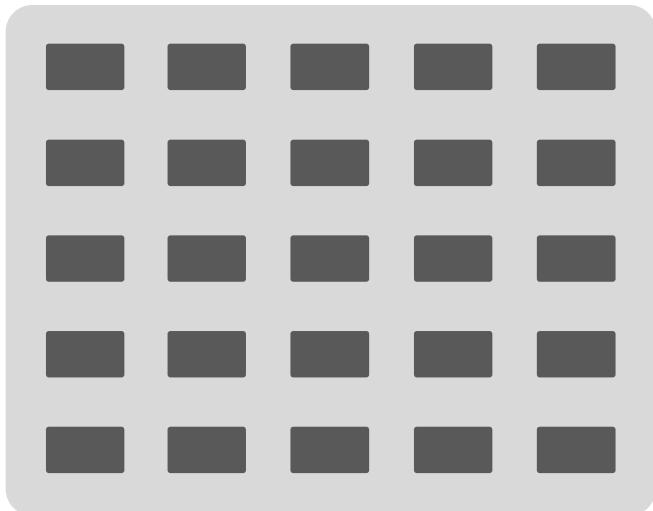
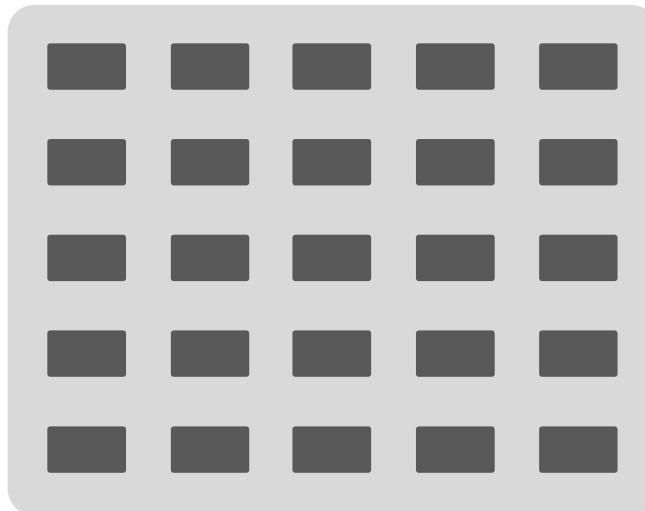
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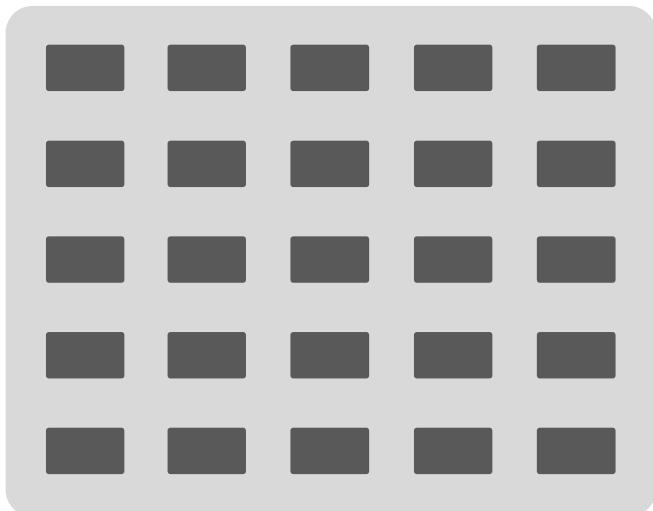
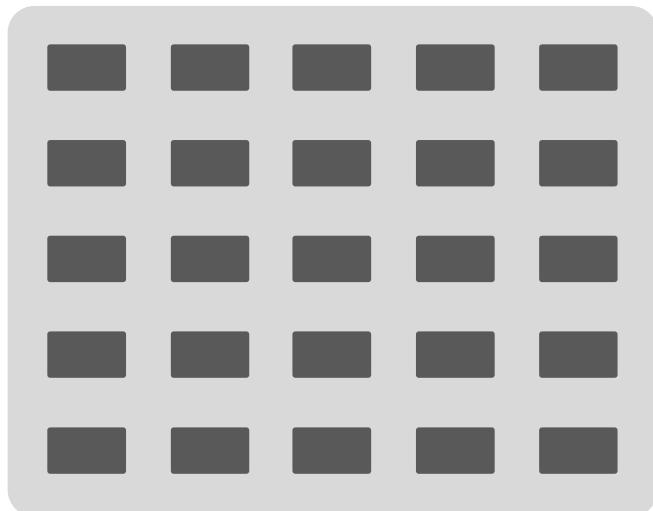
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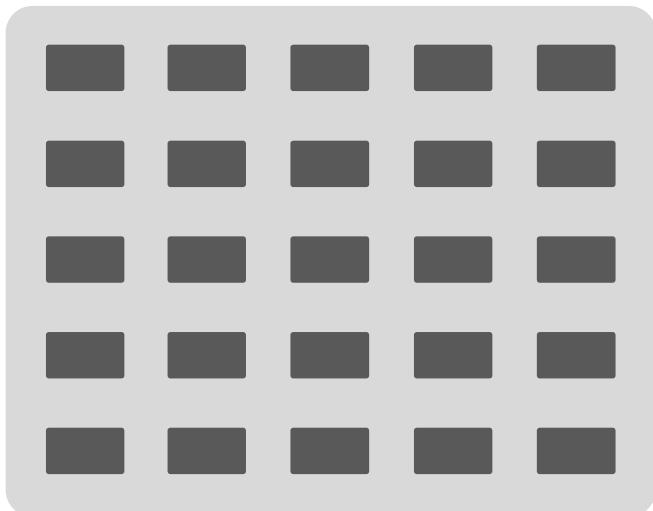
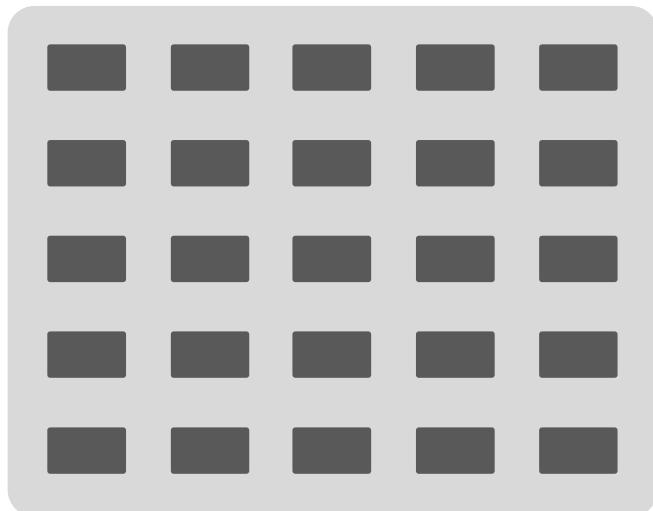
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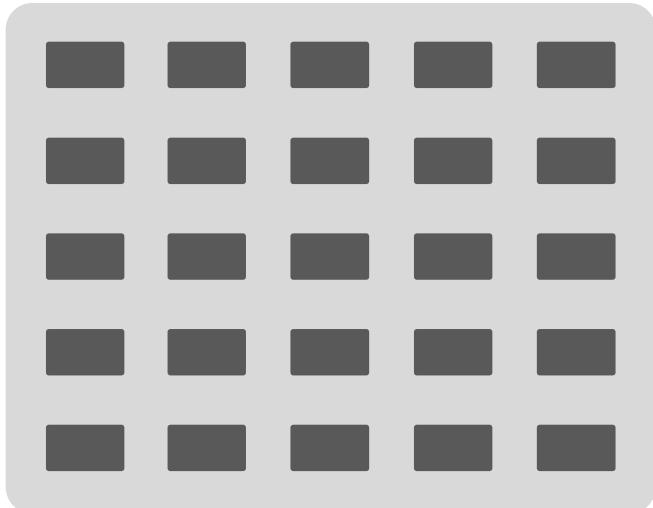
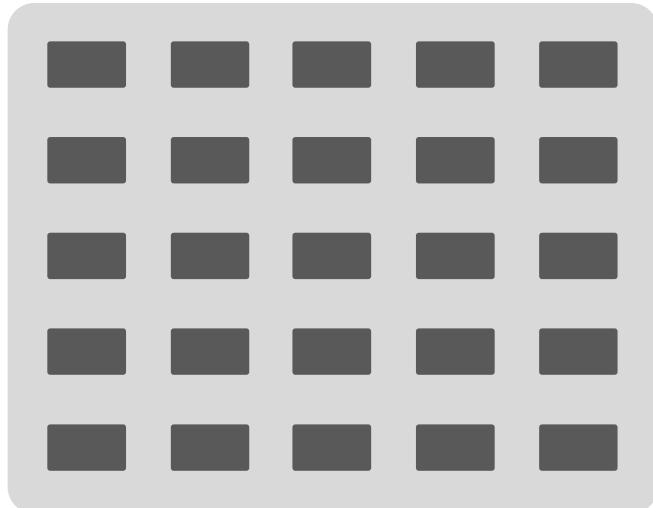


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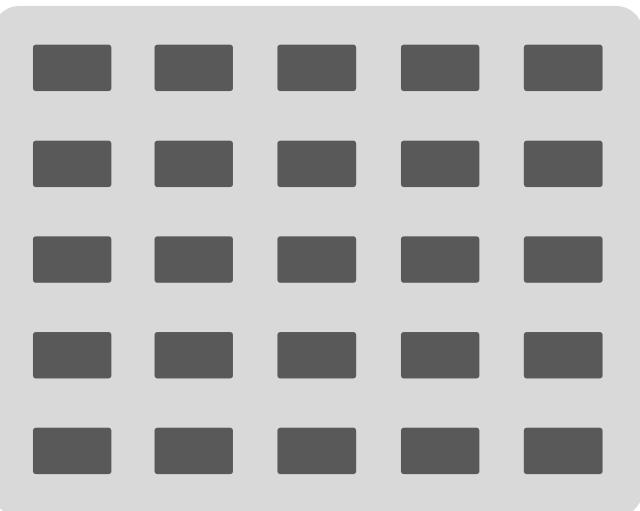
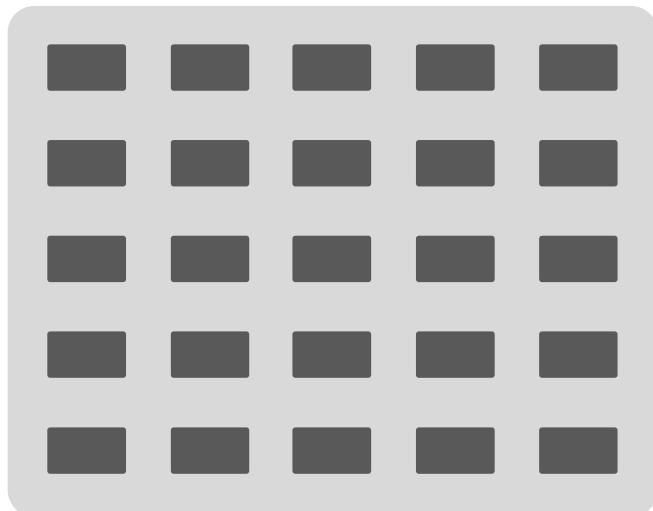
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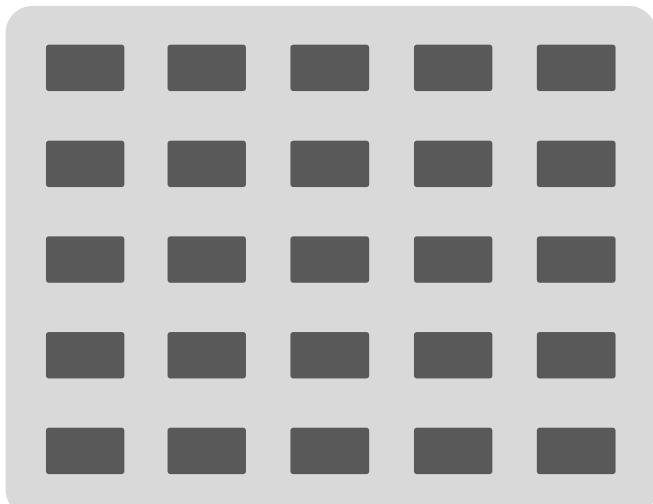
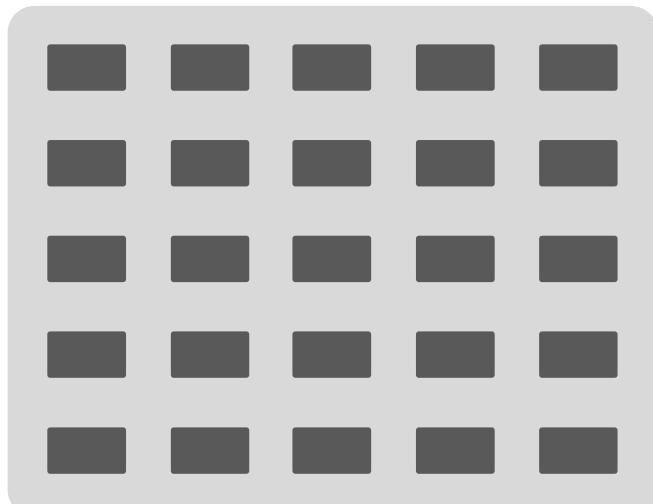
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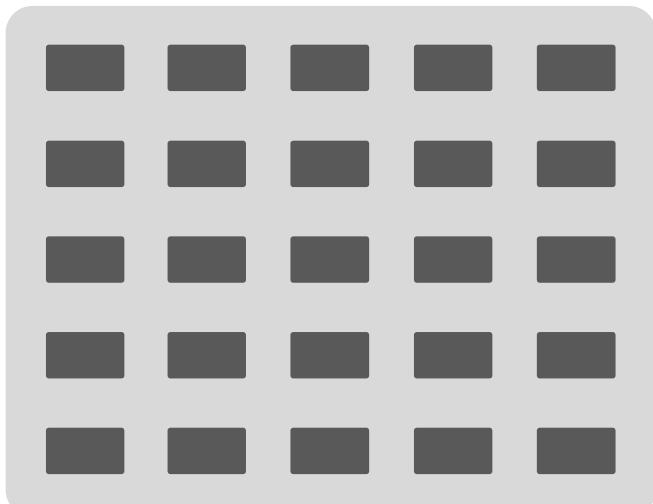
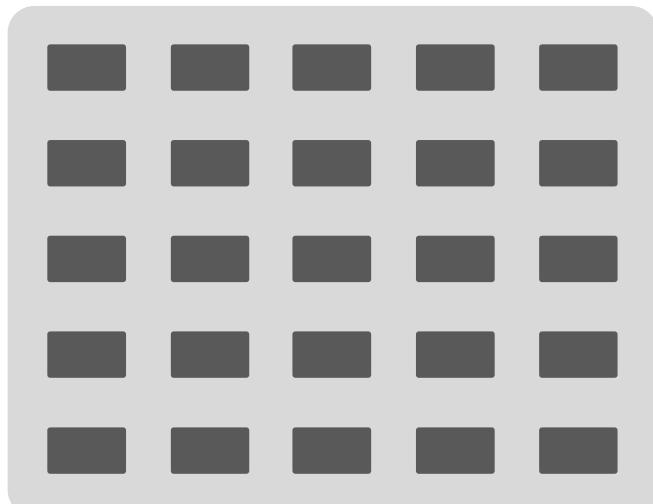
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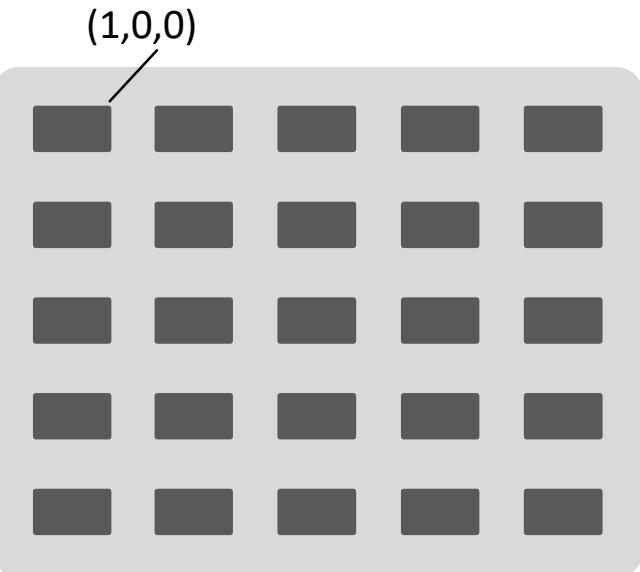
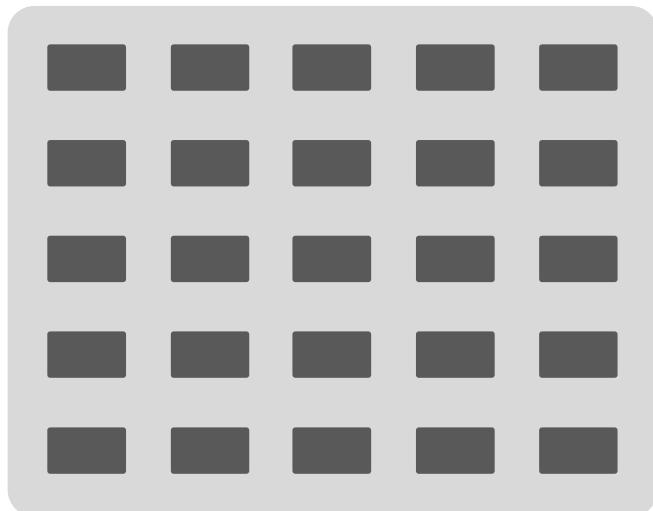
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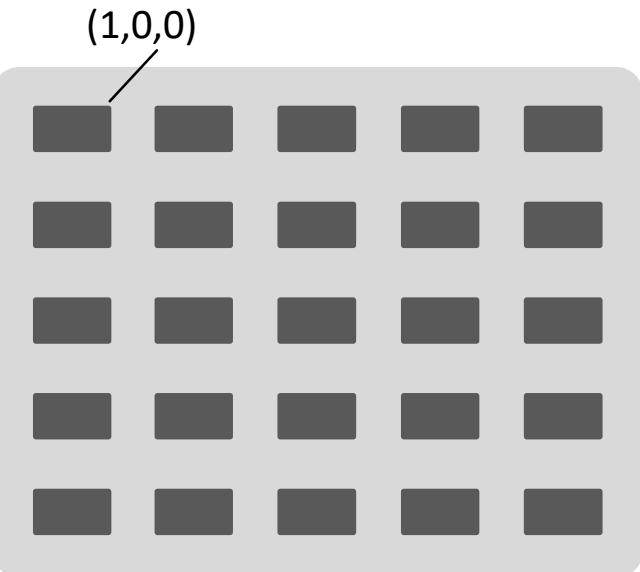
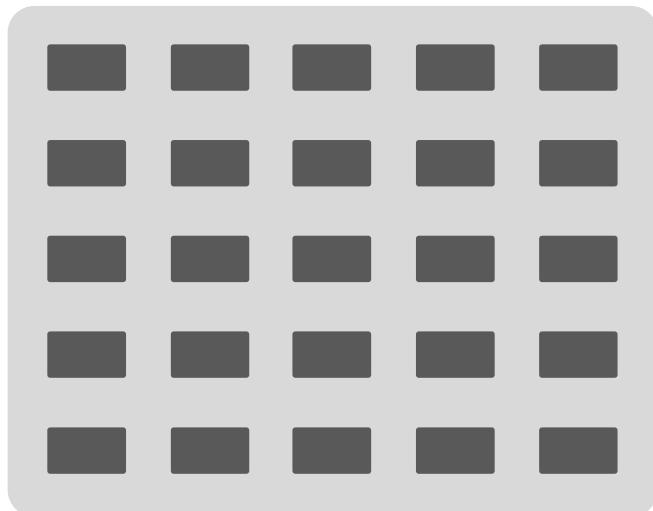
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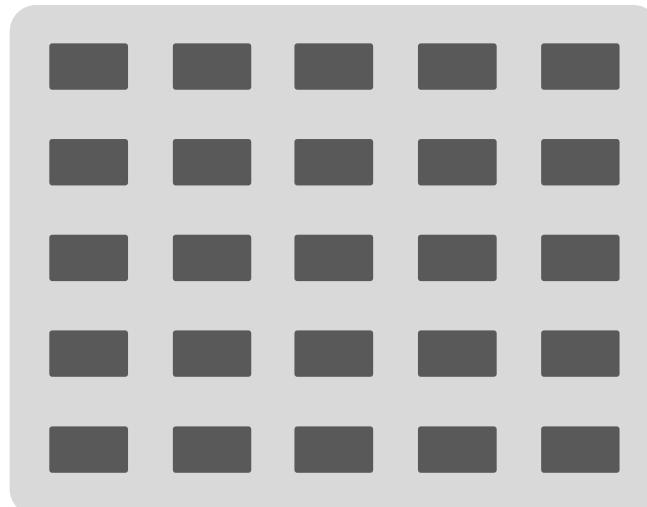


# DIAMETER-2 SLIM FLY

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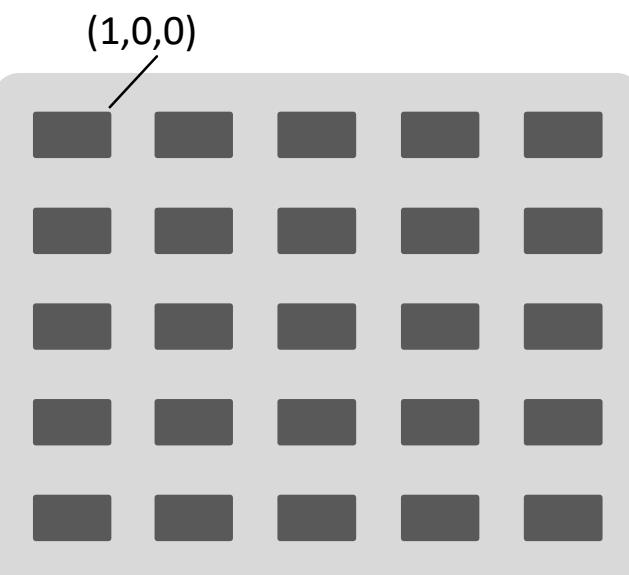


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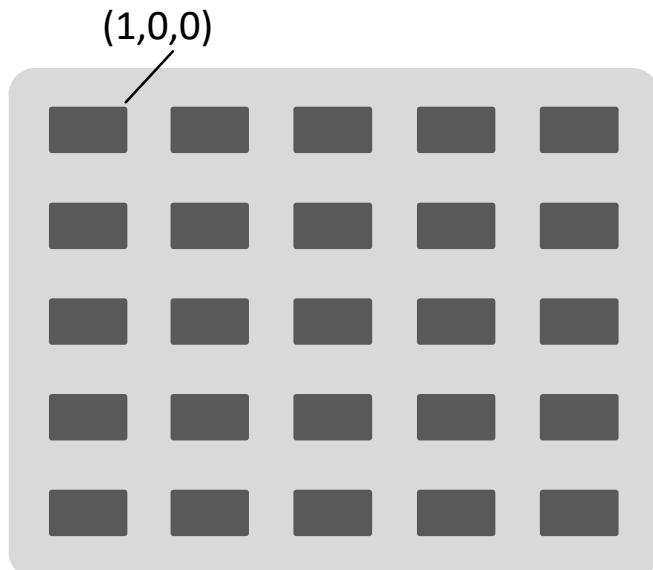
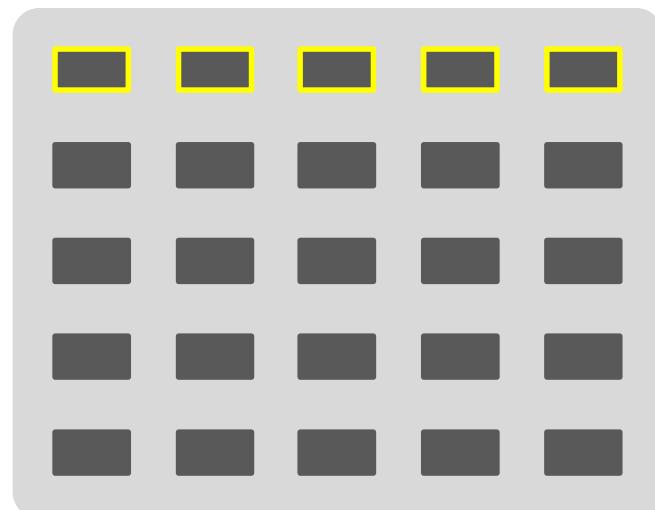
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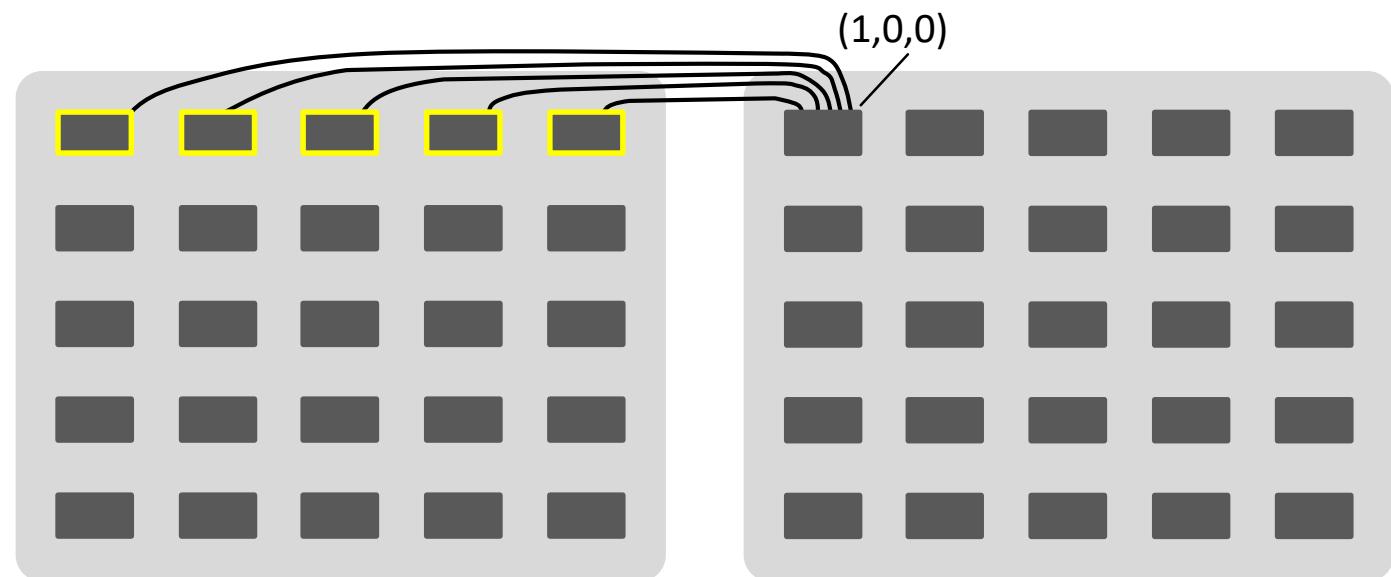
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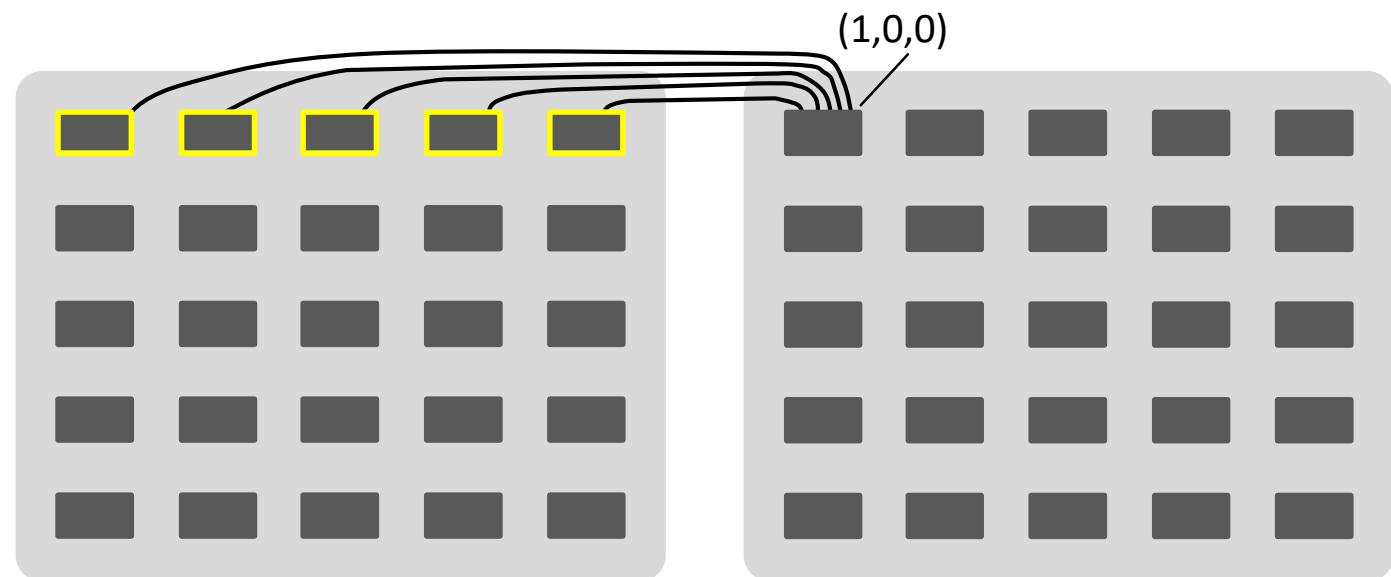
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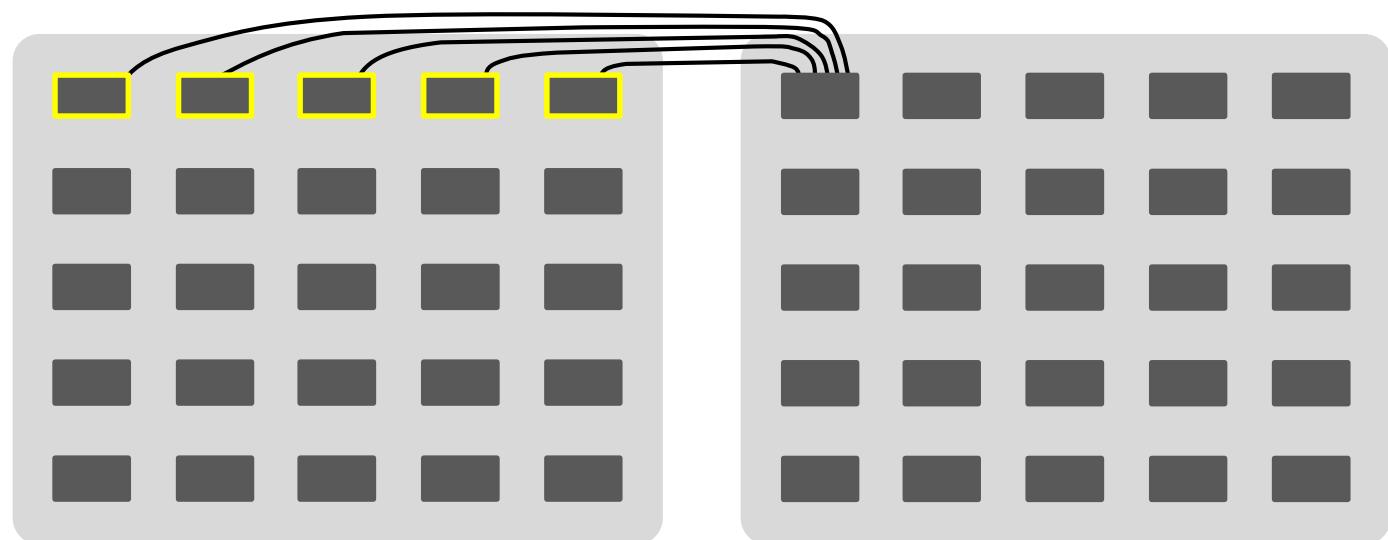
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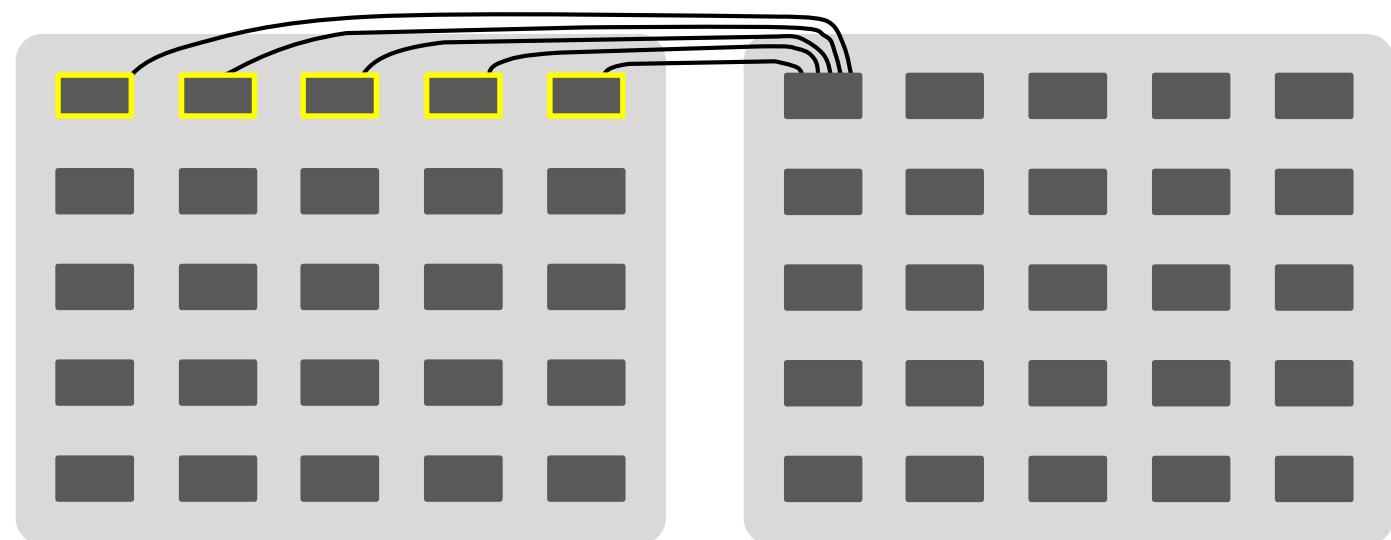
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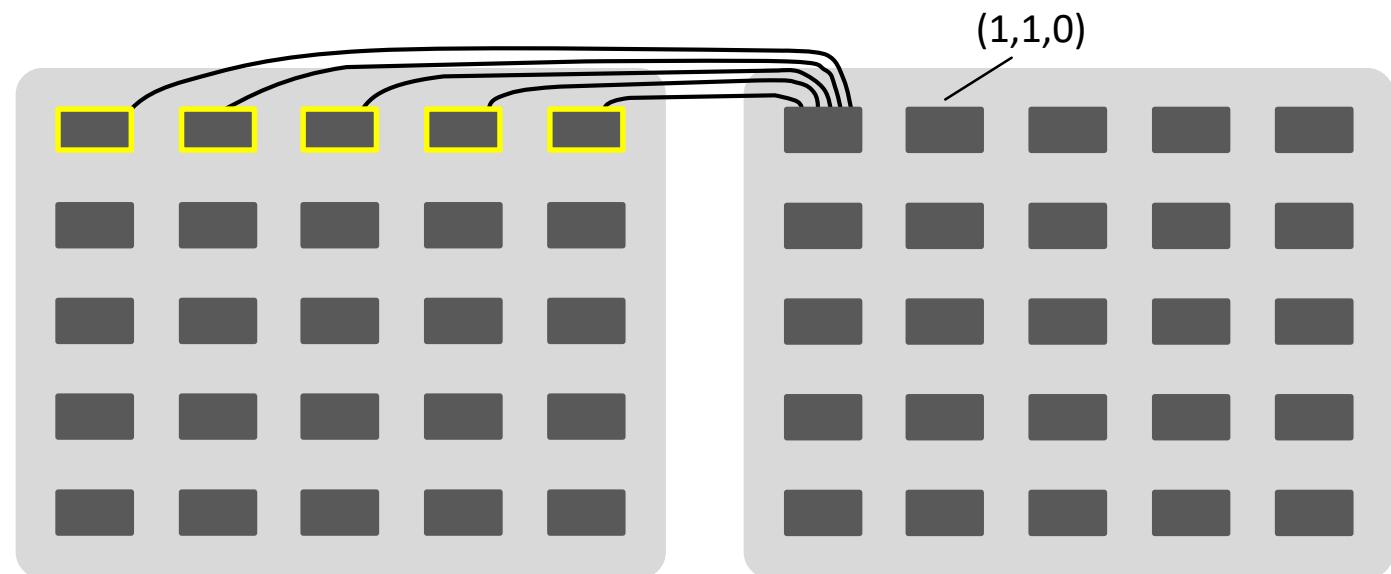
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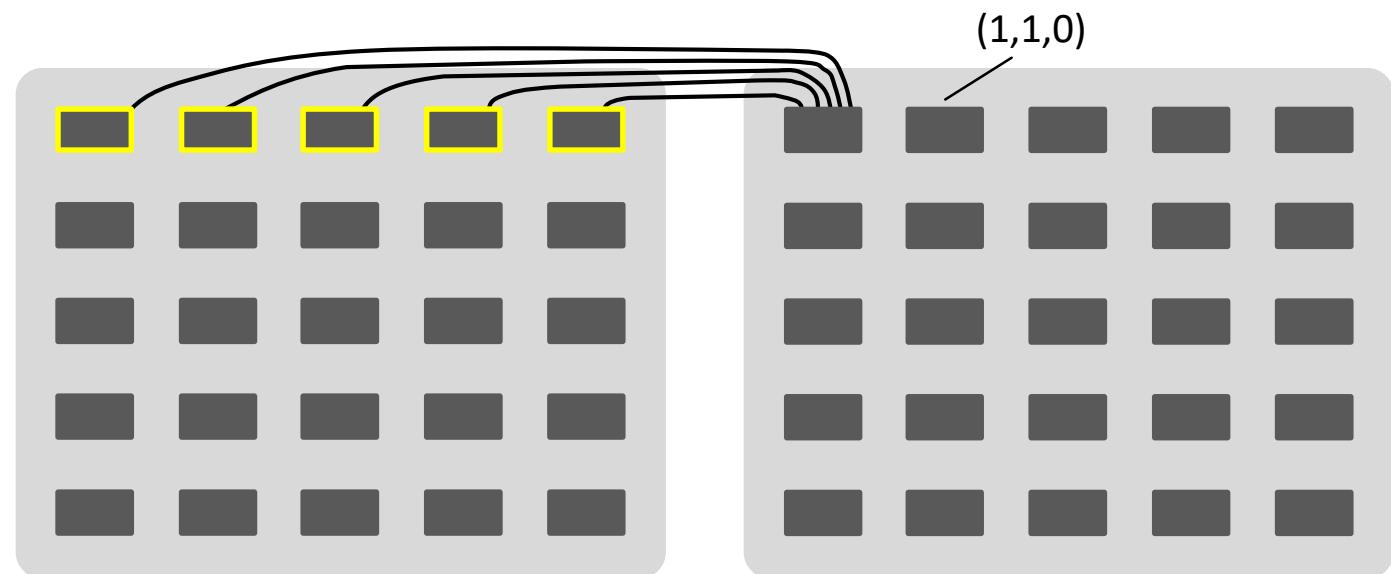
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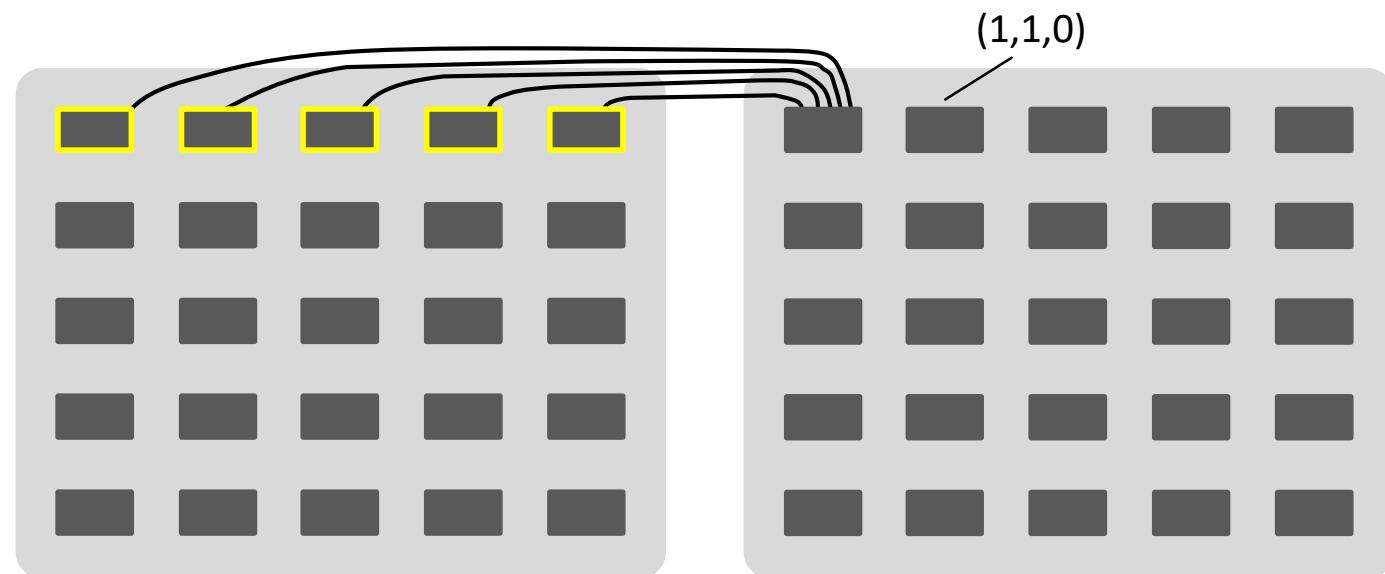
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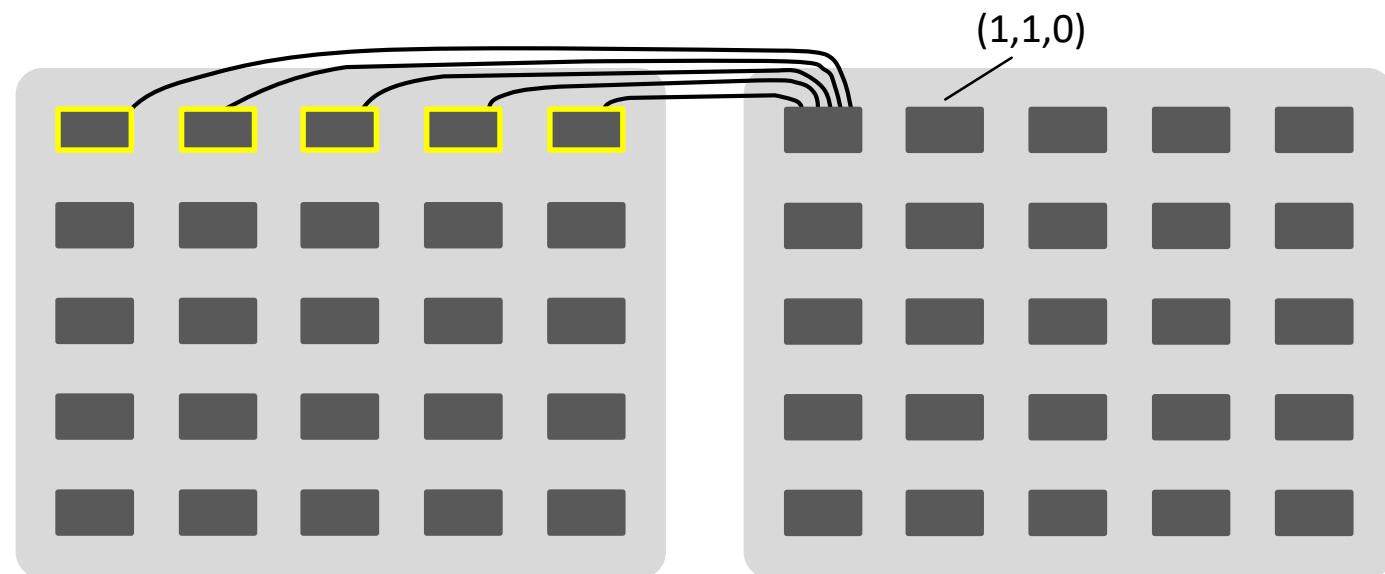
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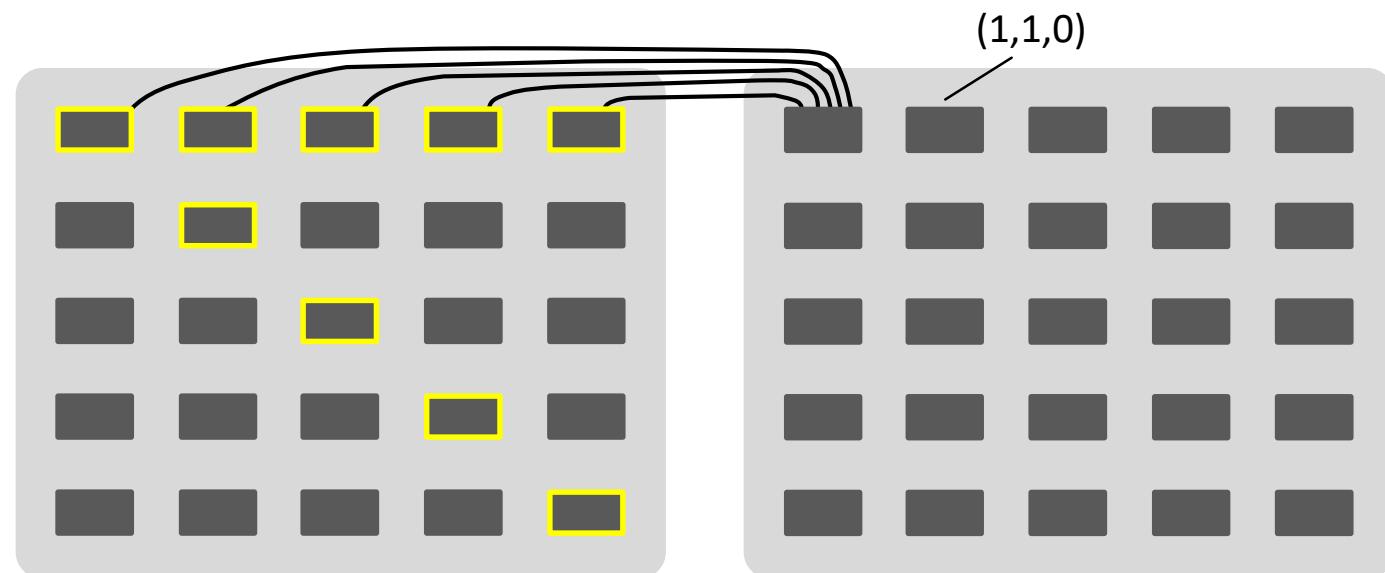
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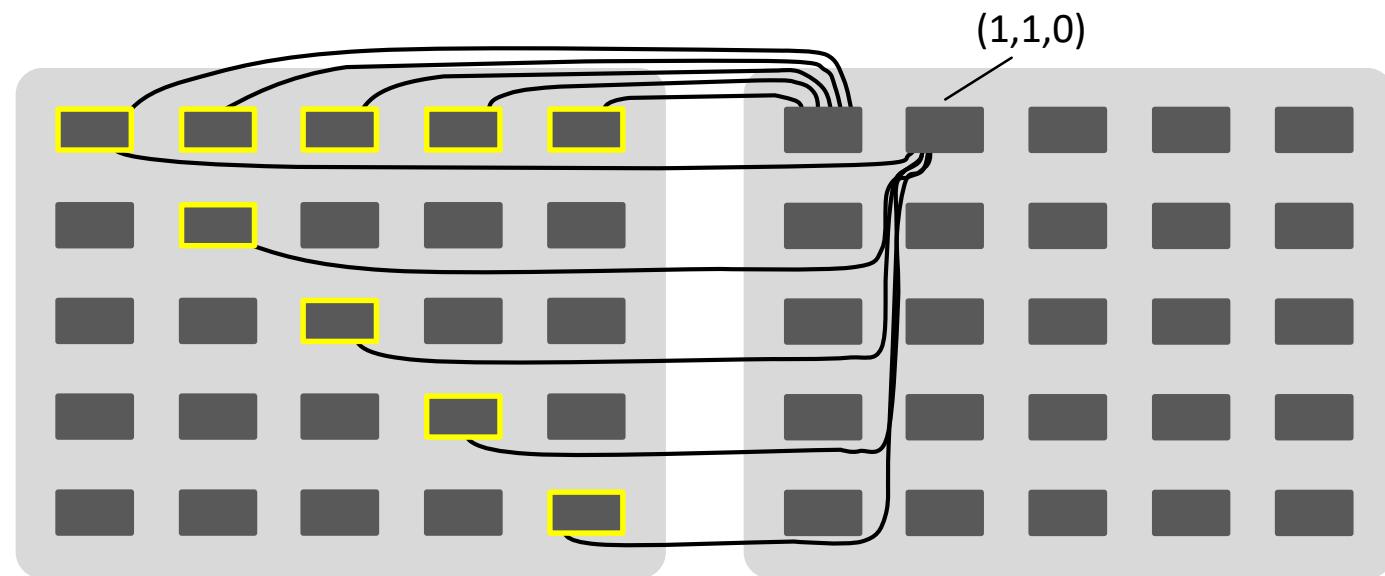
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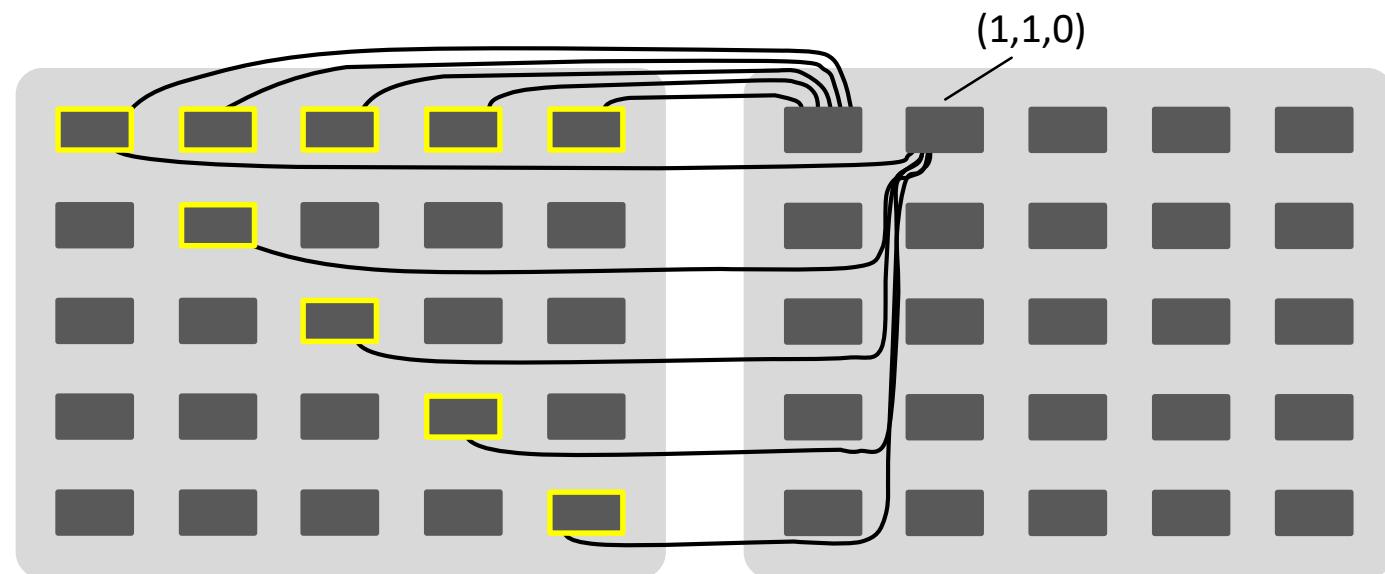
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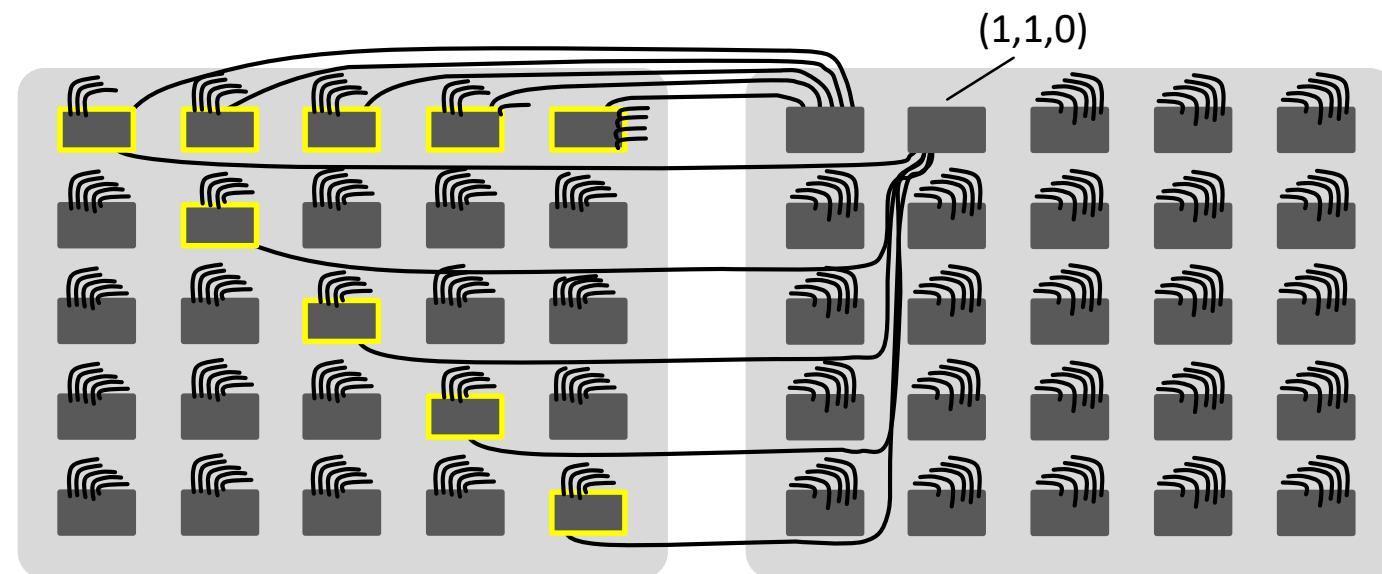
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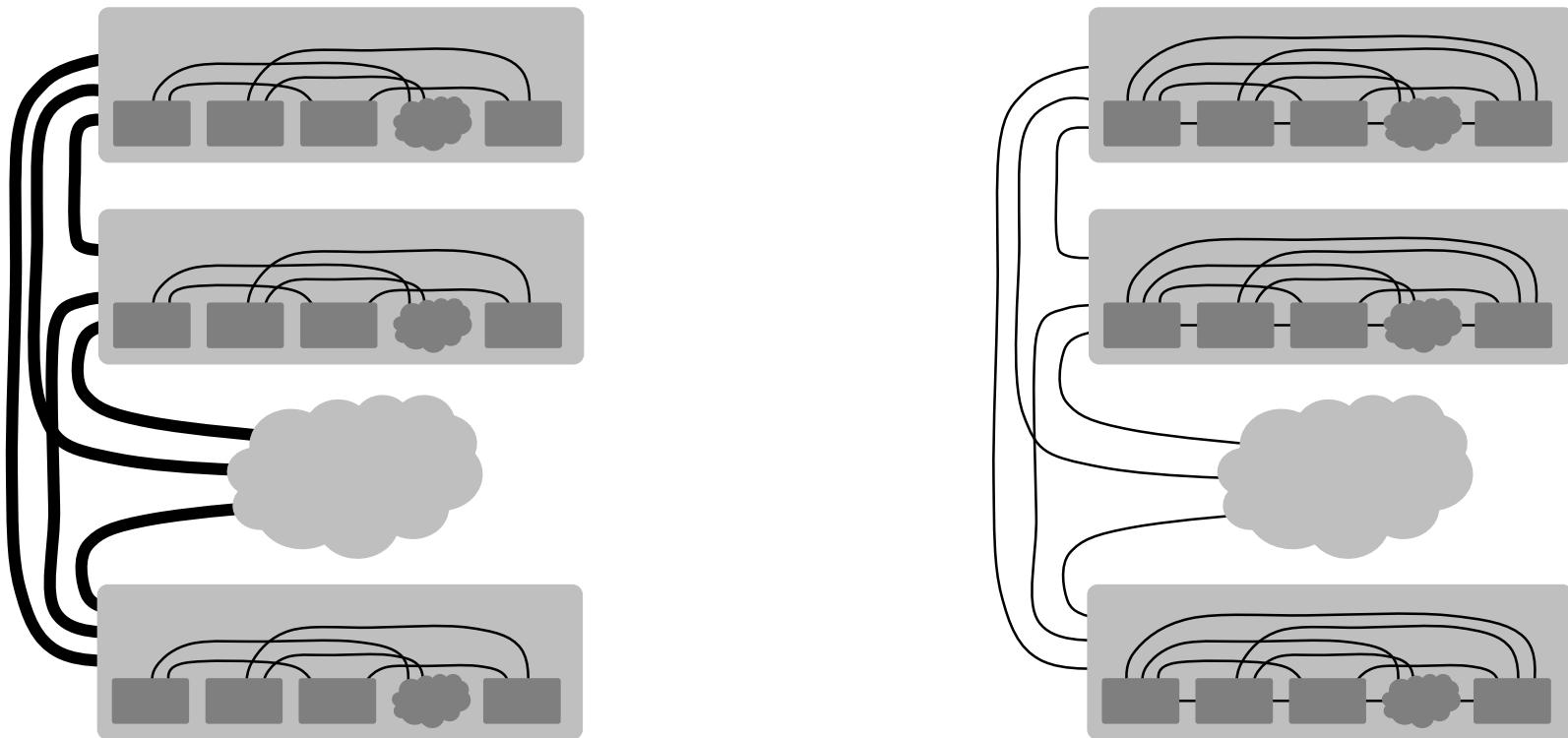
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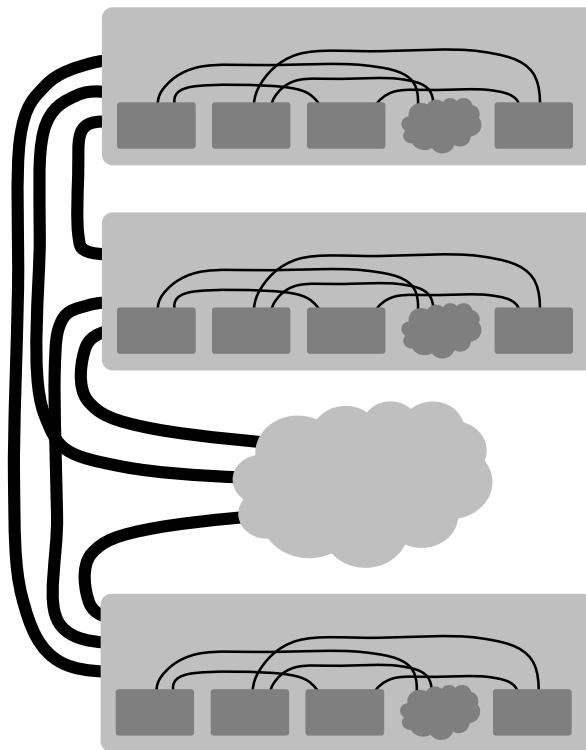


# DESIGN INTUITION

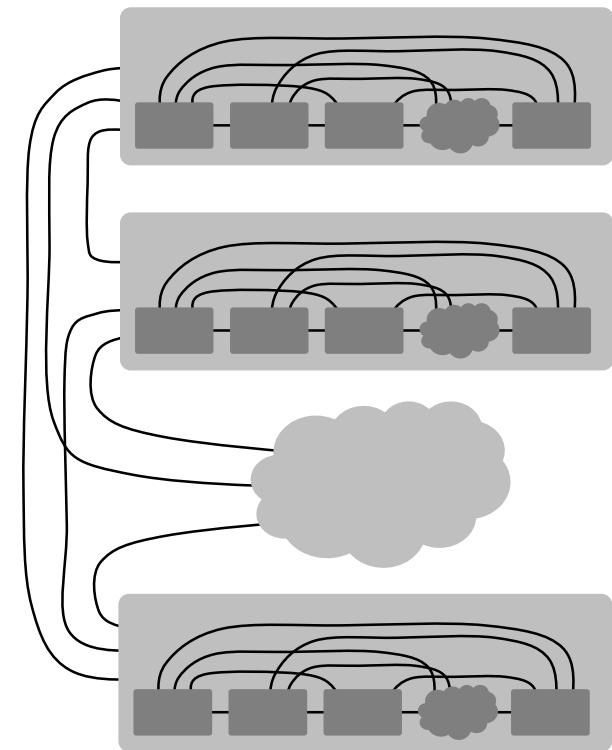


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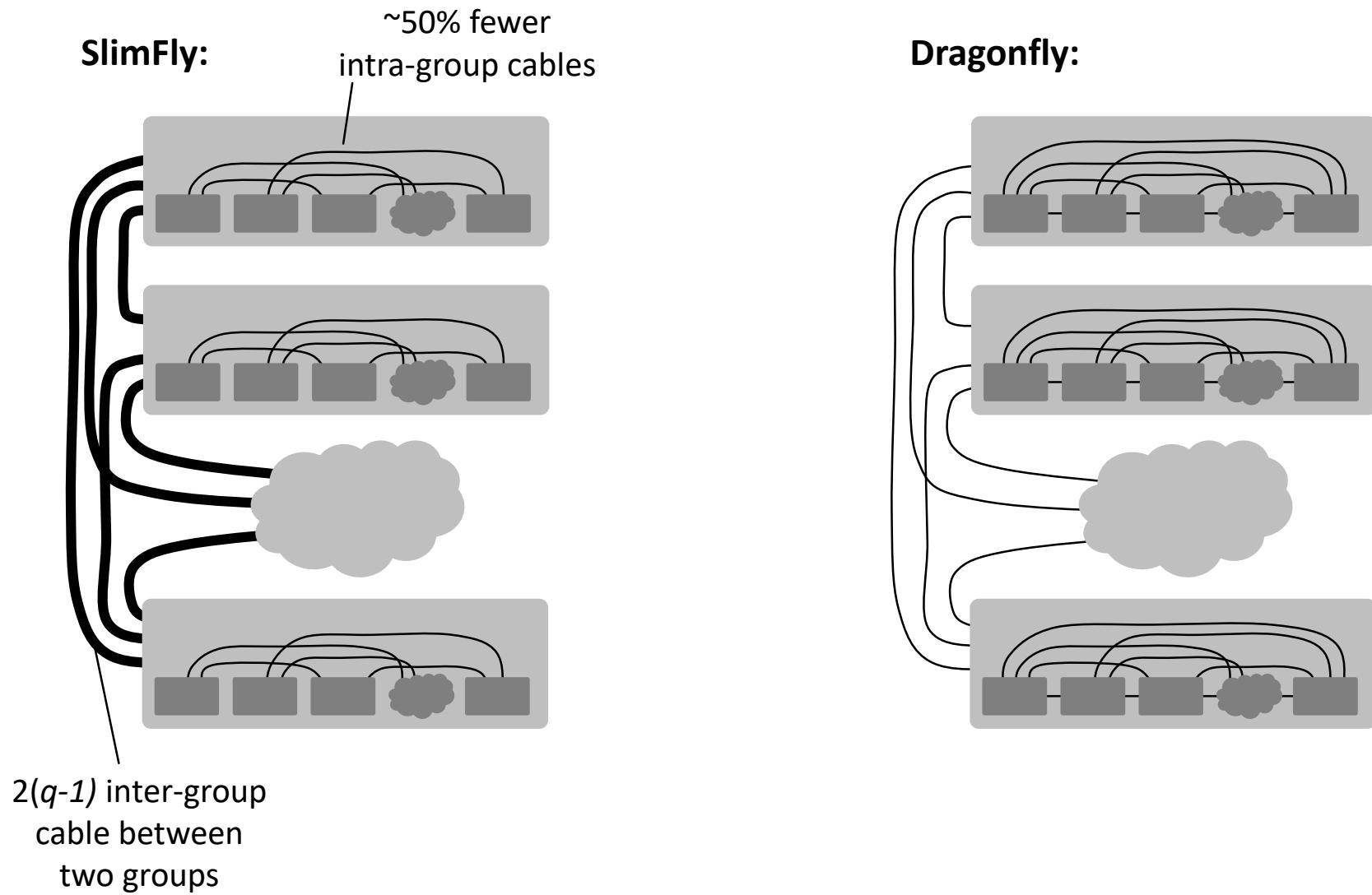
**SlimFly:**



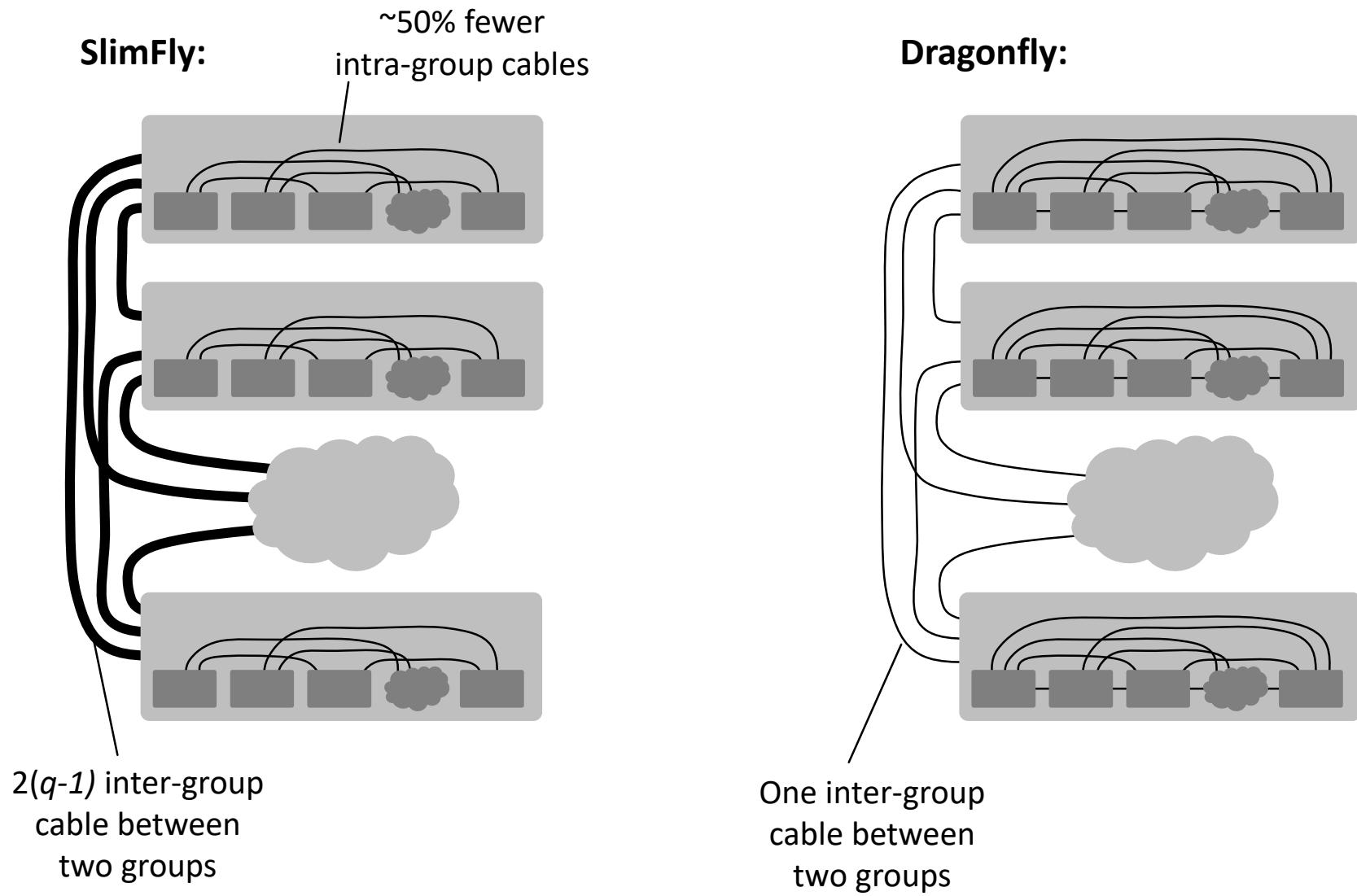
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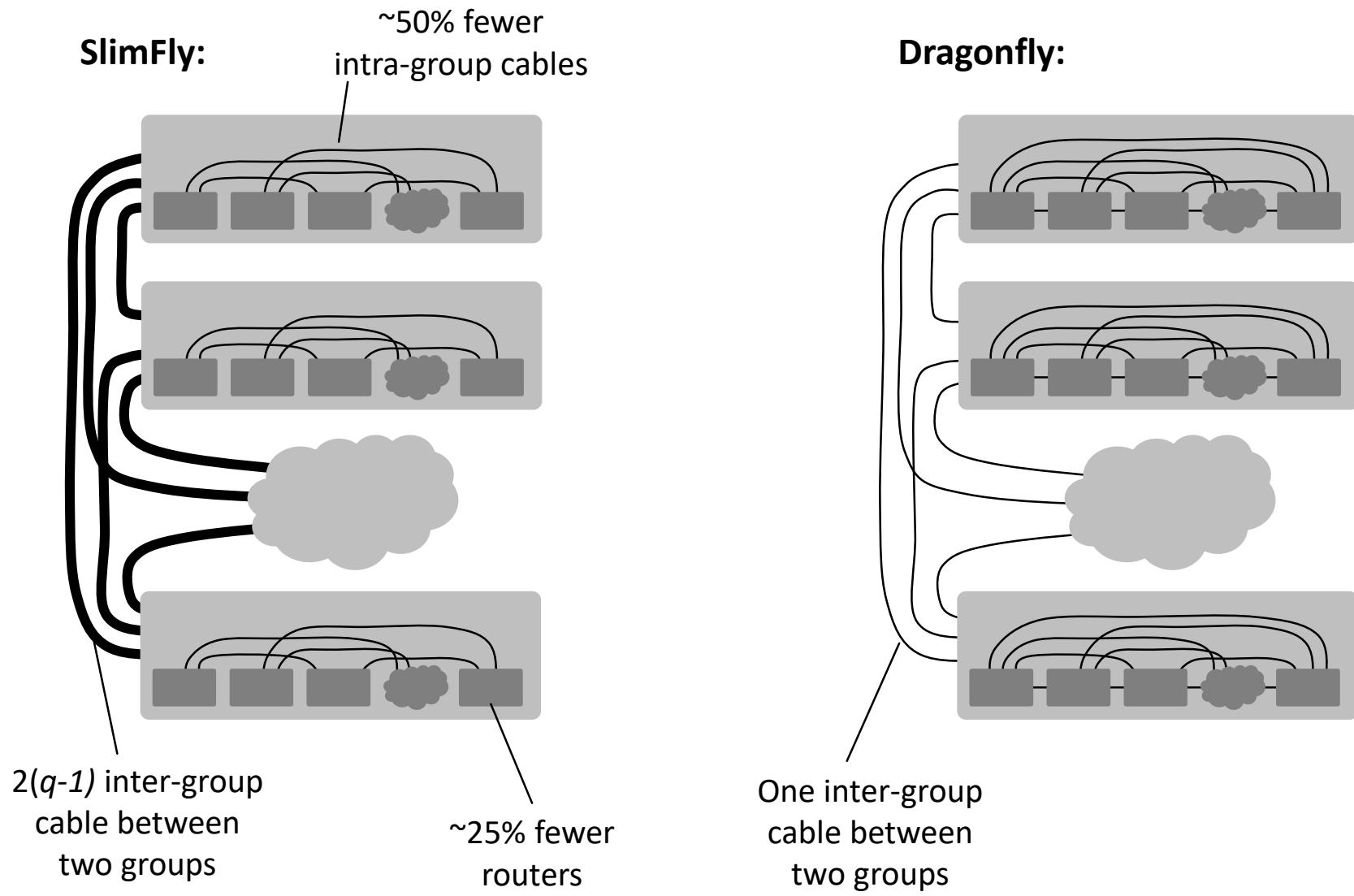
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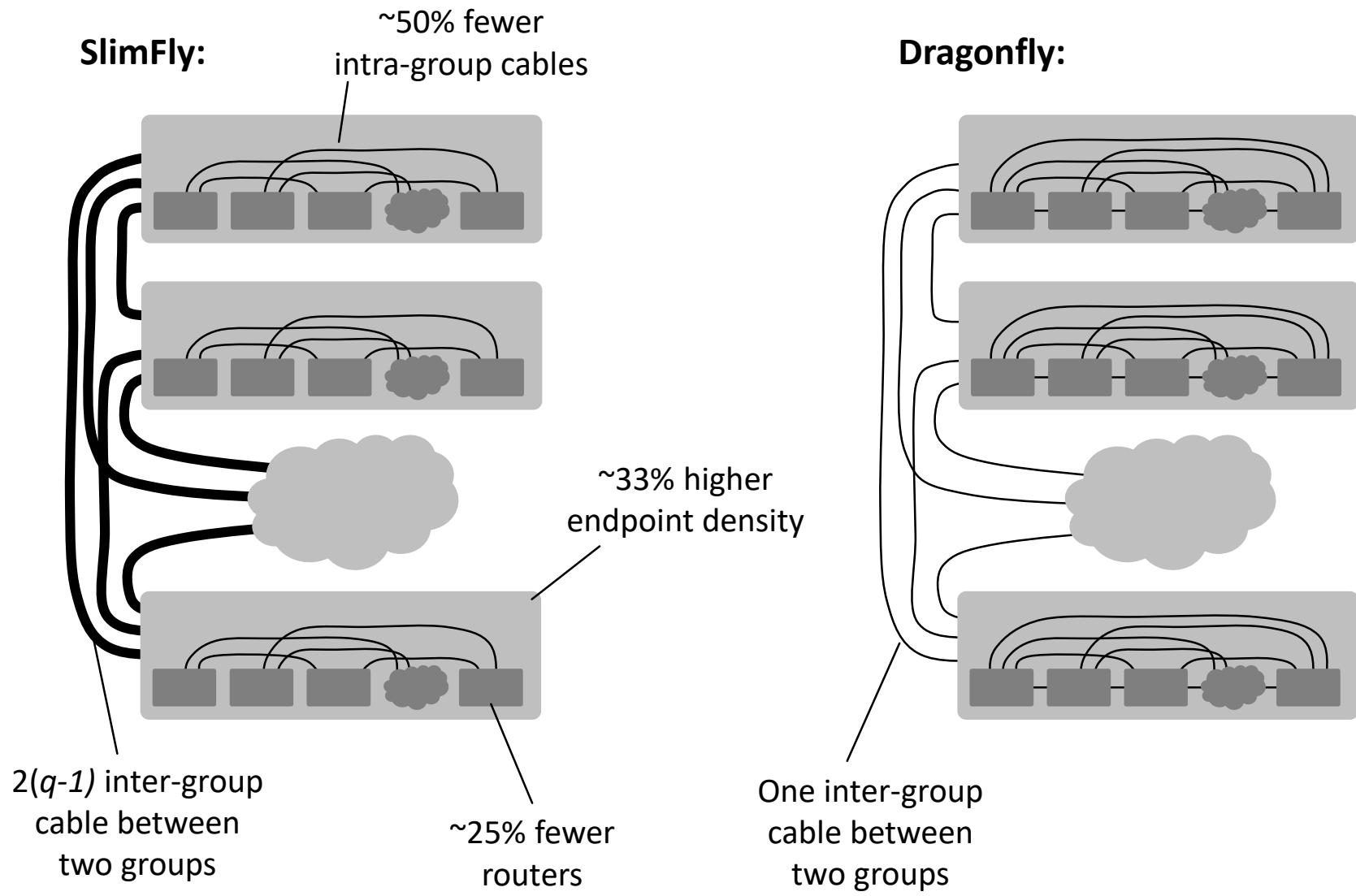
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# DEADLOCK FREEDOM

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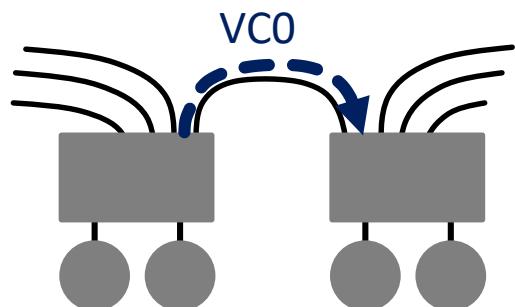
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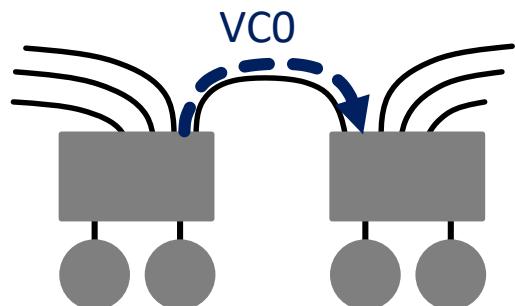
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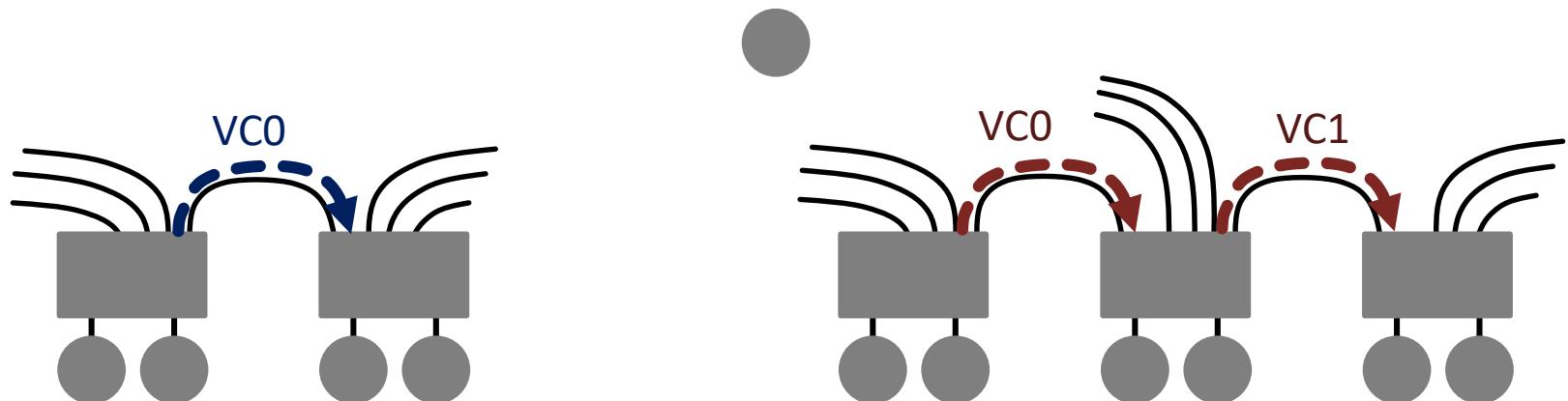
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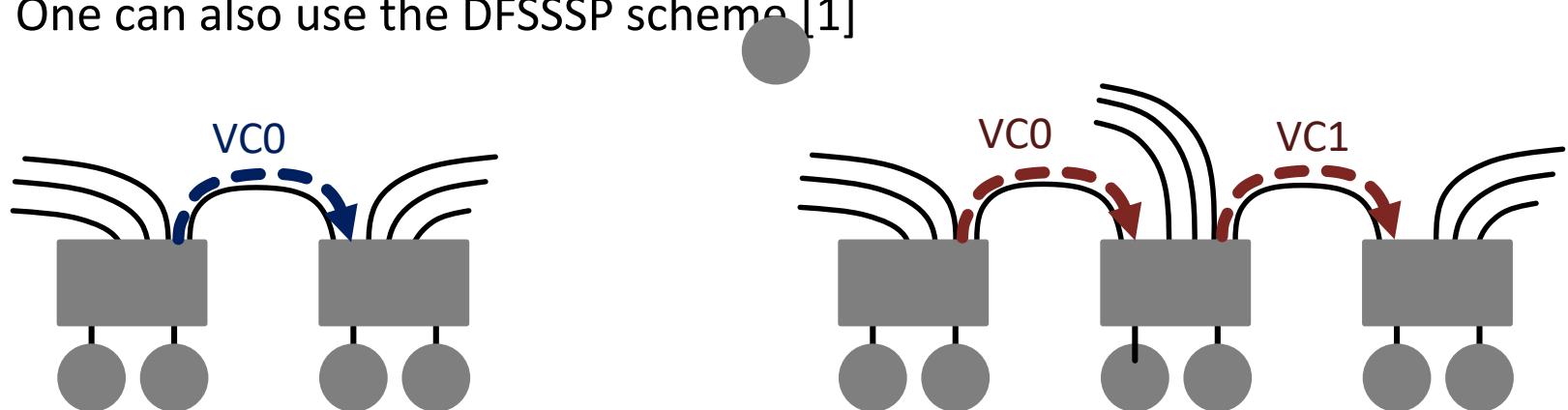
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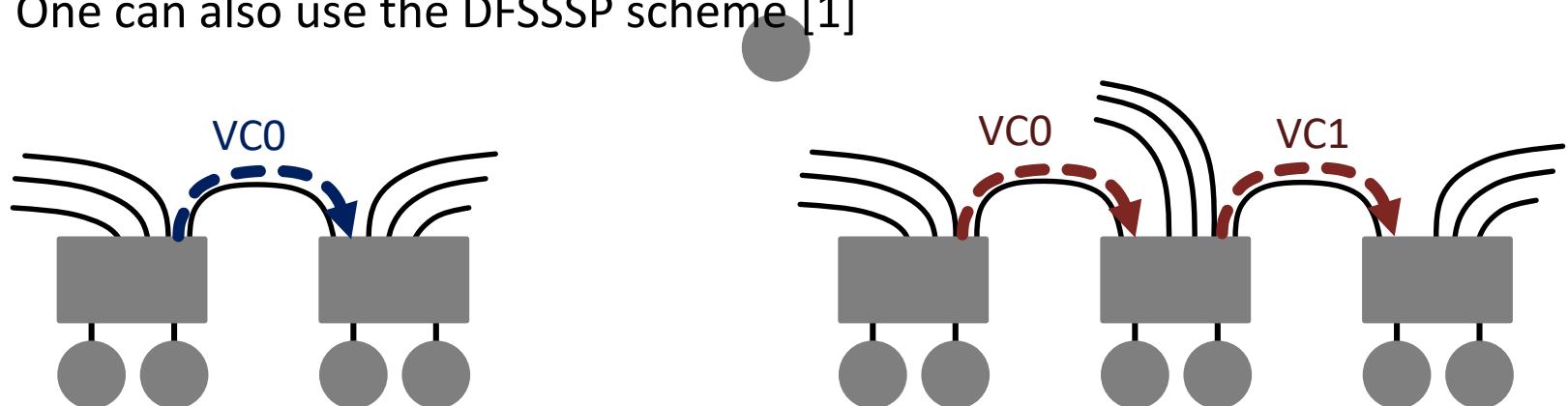
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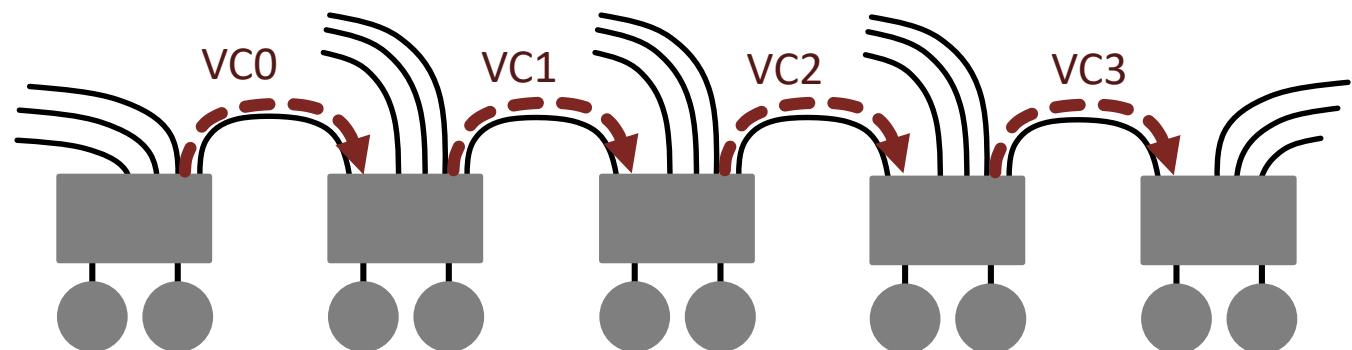
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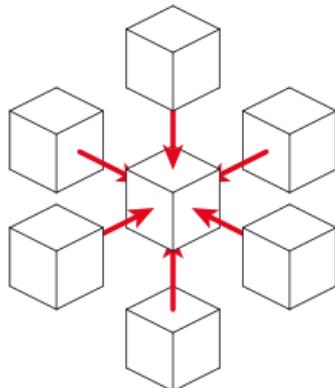
# PERFORMANCE

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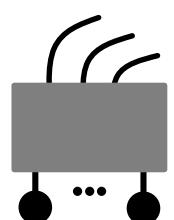
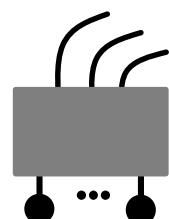
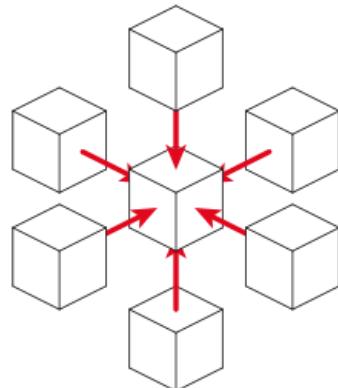
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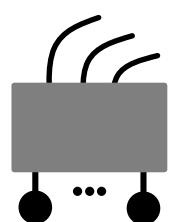
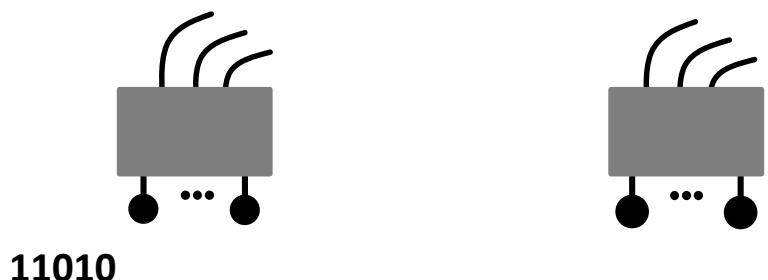
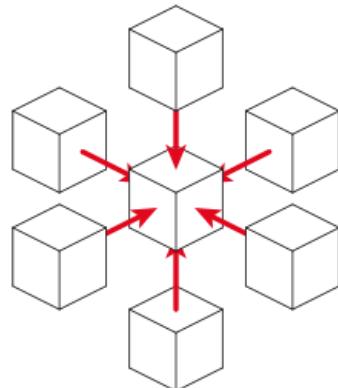
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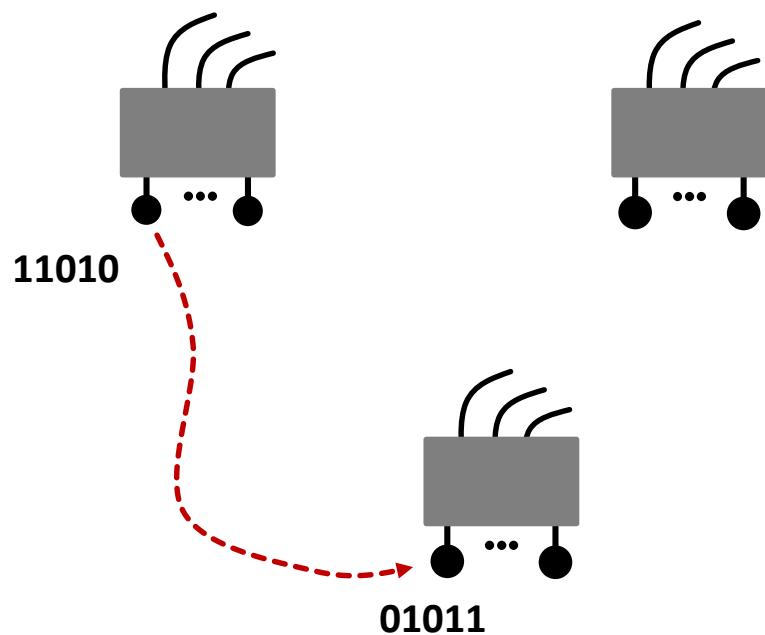
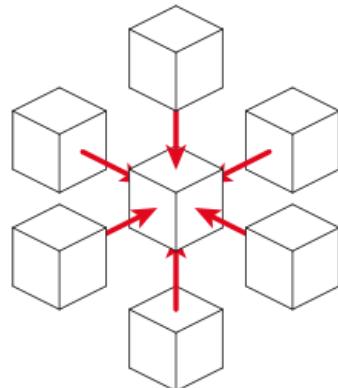
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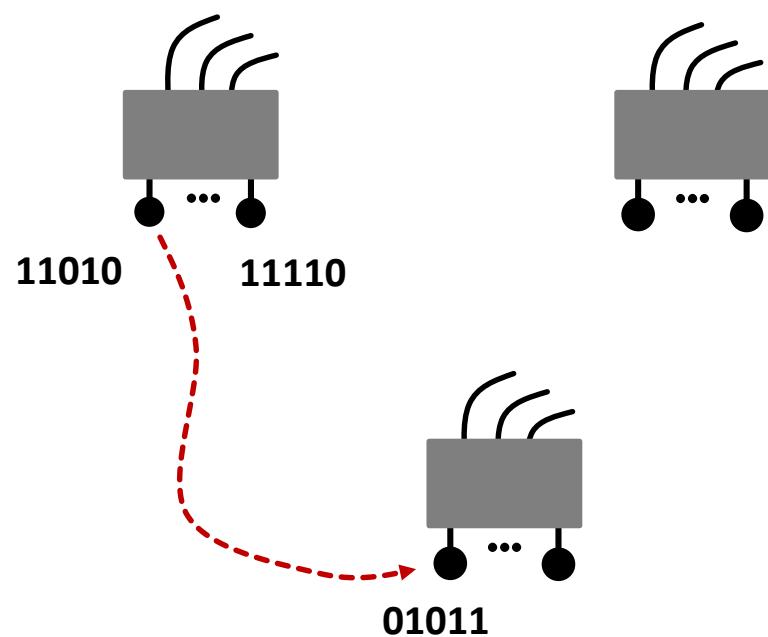
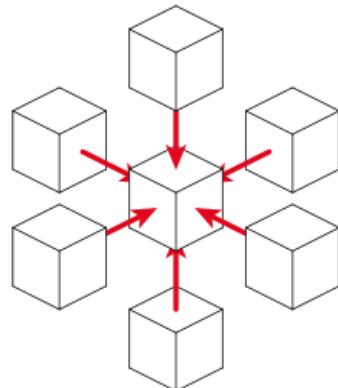
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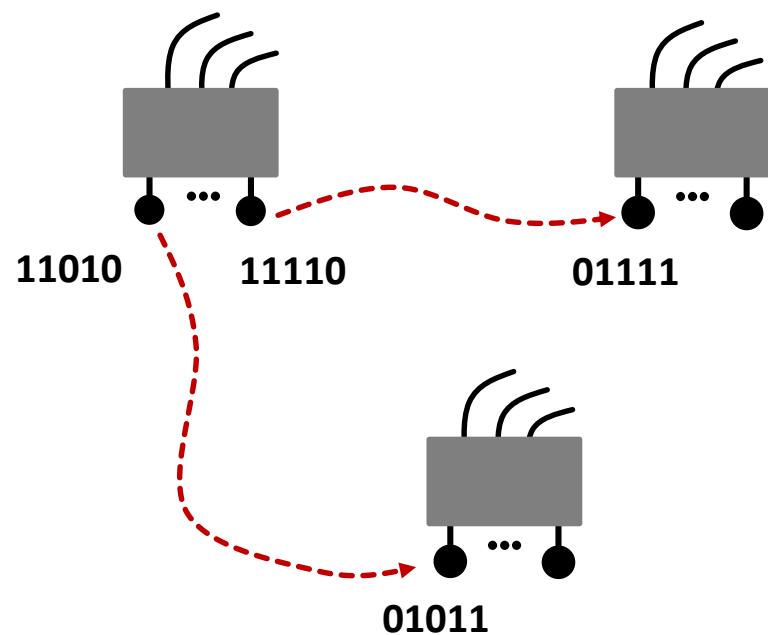
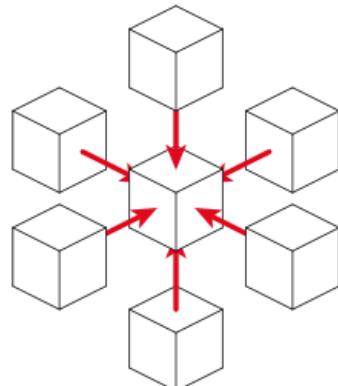
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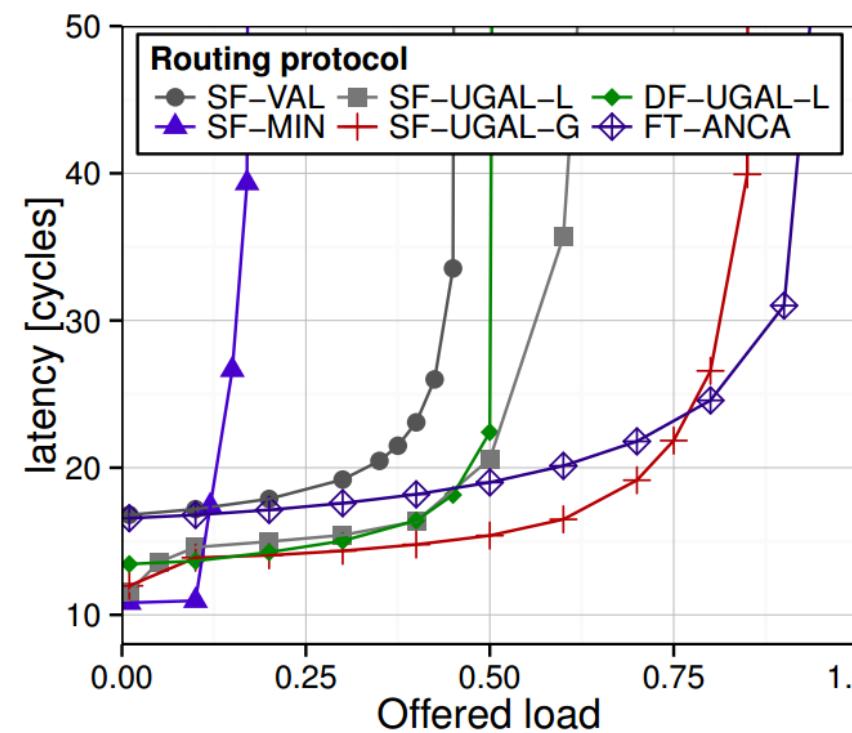
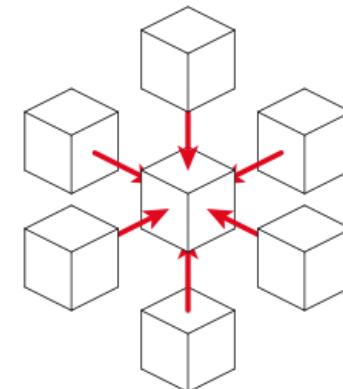
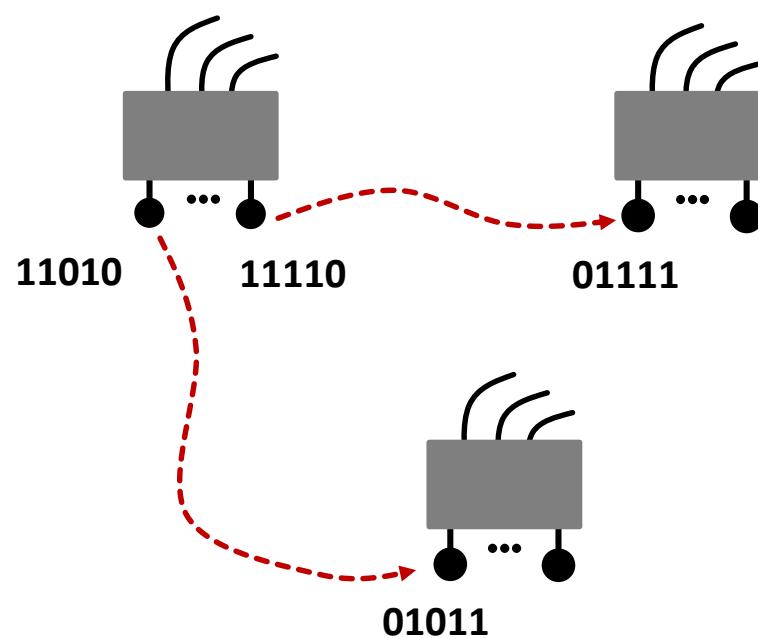
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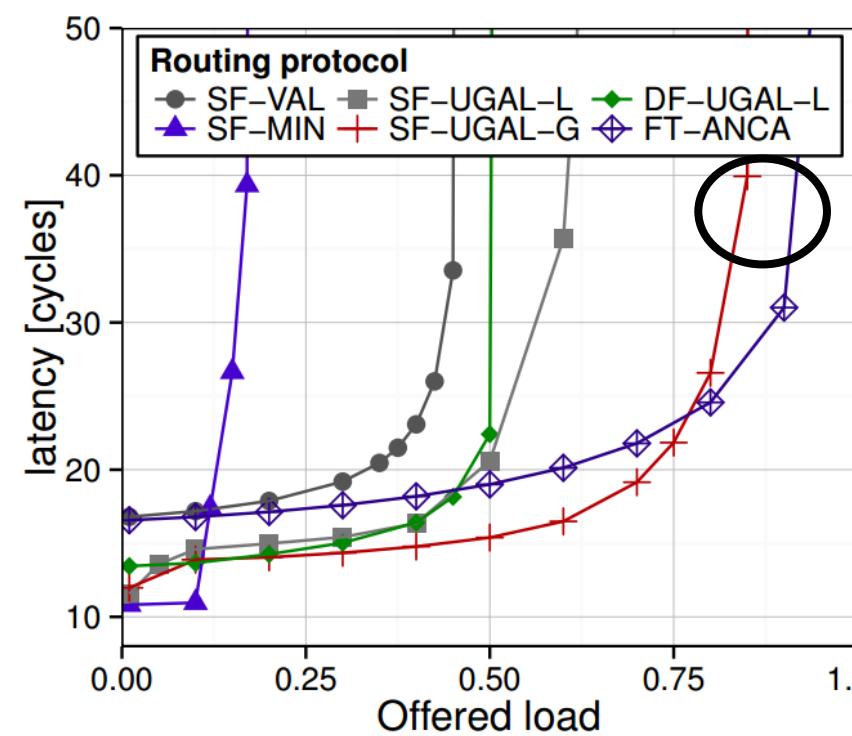
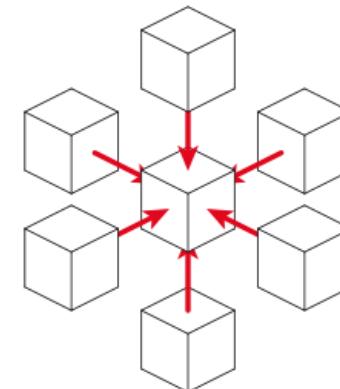
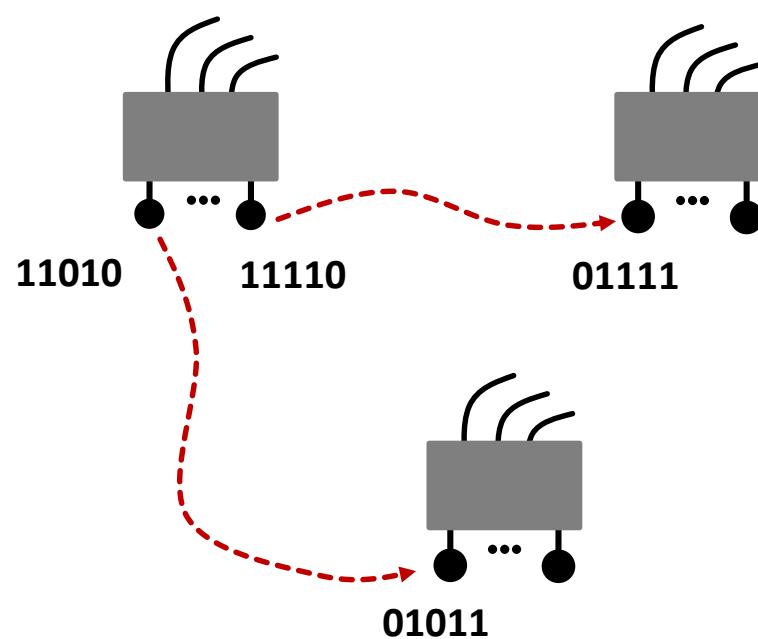
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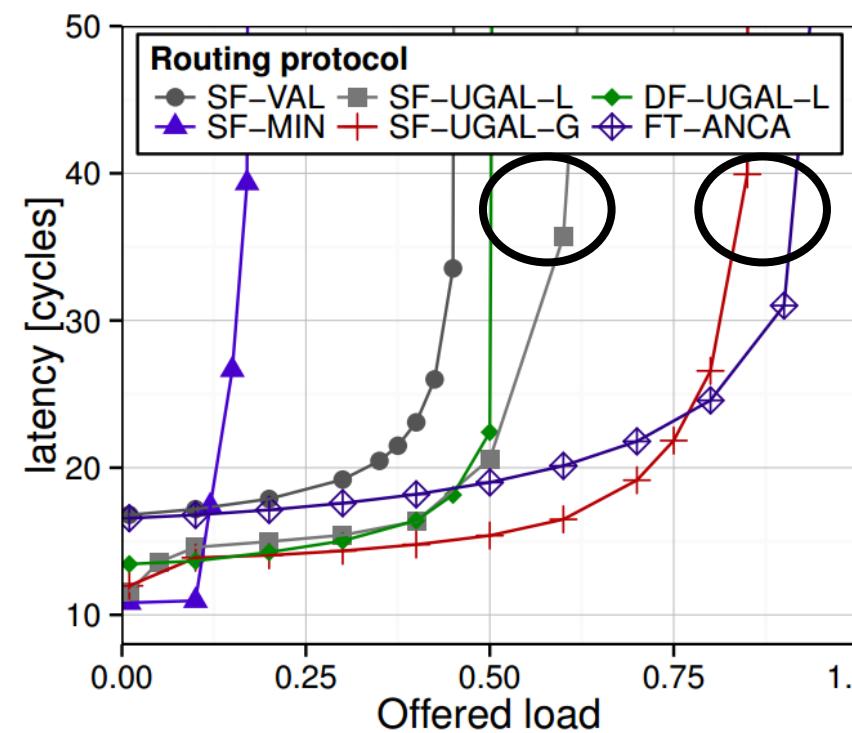
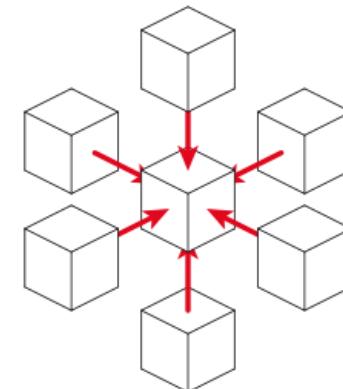
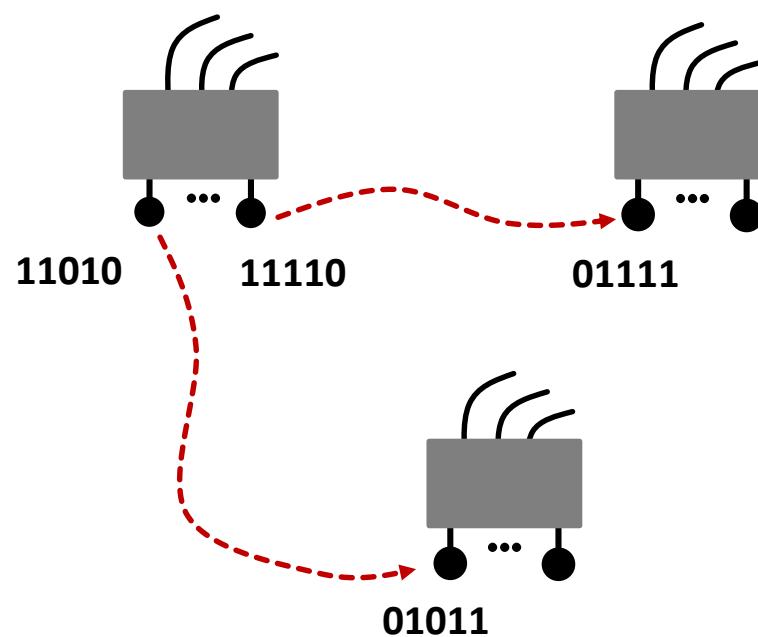
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dest id  
 $d =$

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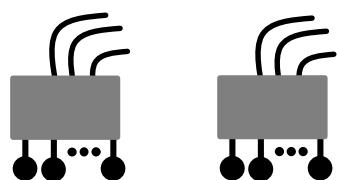
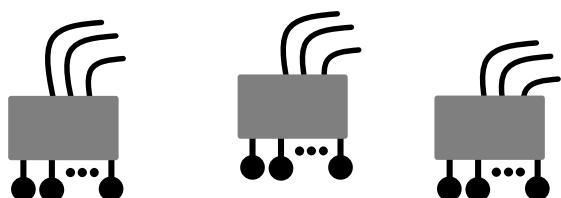
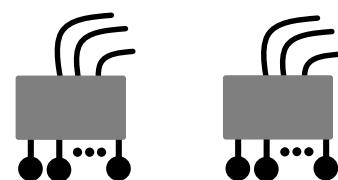
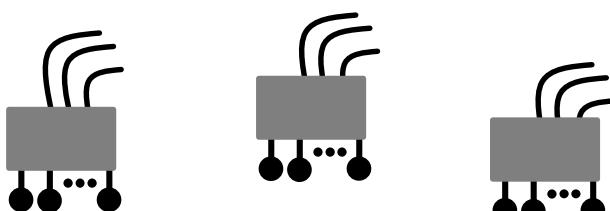
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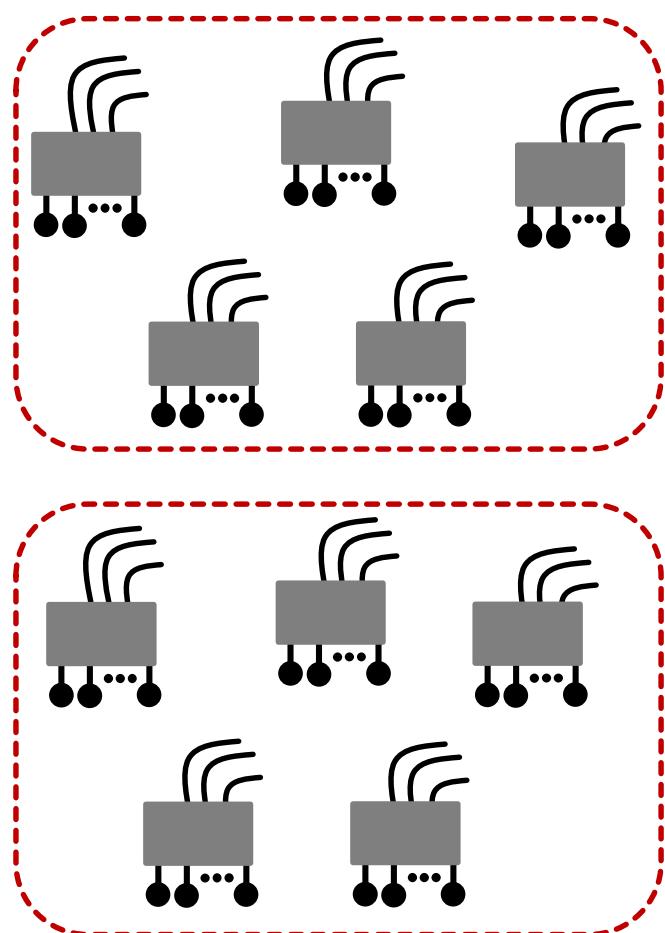
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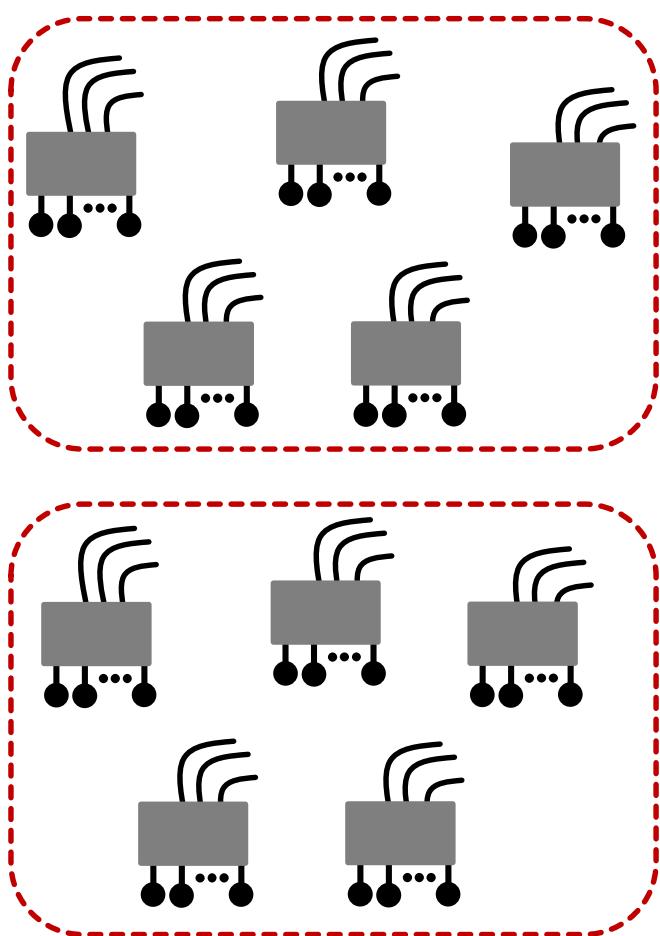
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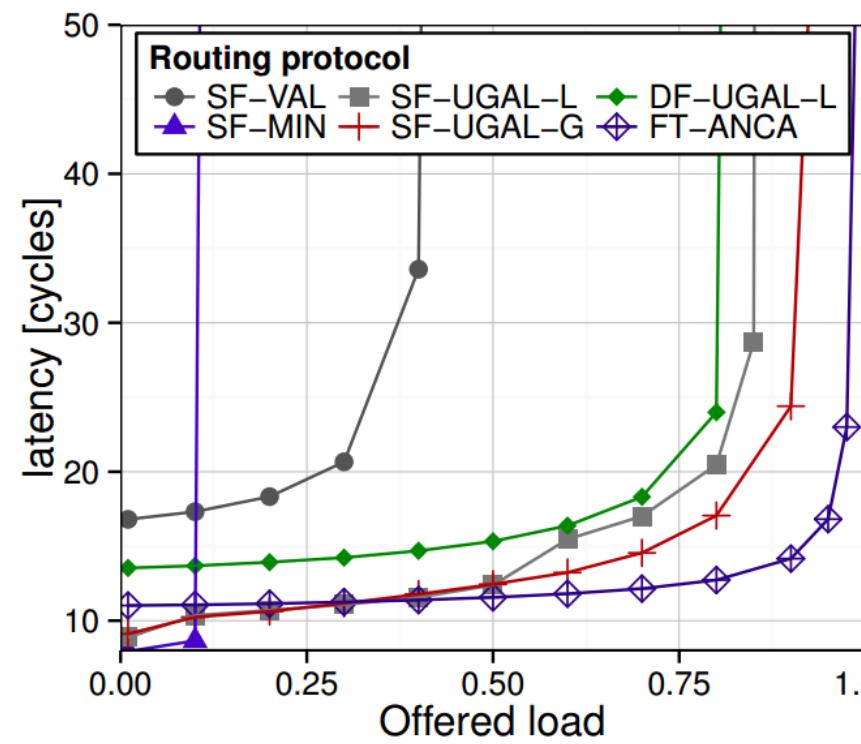
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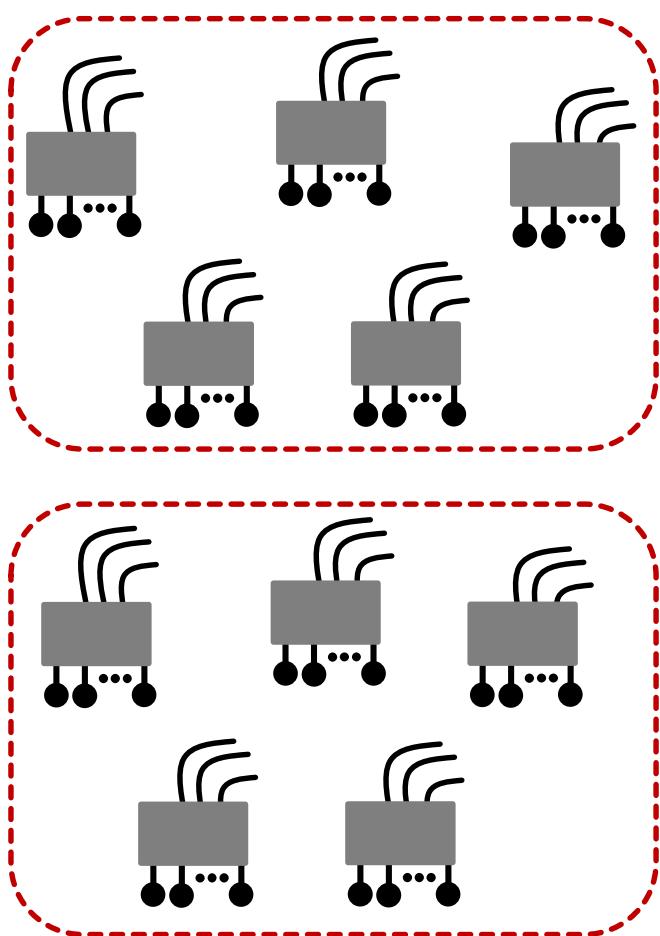


$$d = \left( s \bmod \frac{N}{2} \right) + \frac{N}{2}$$

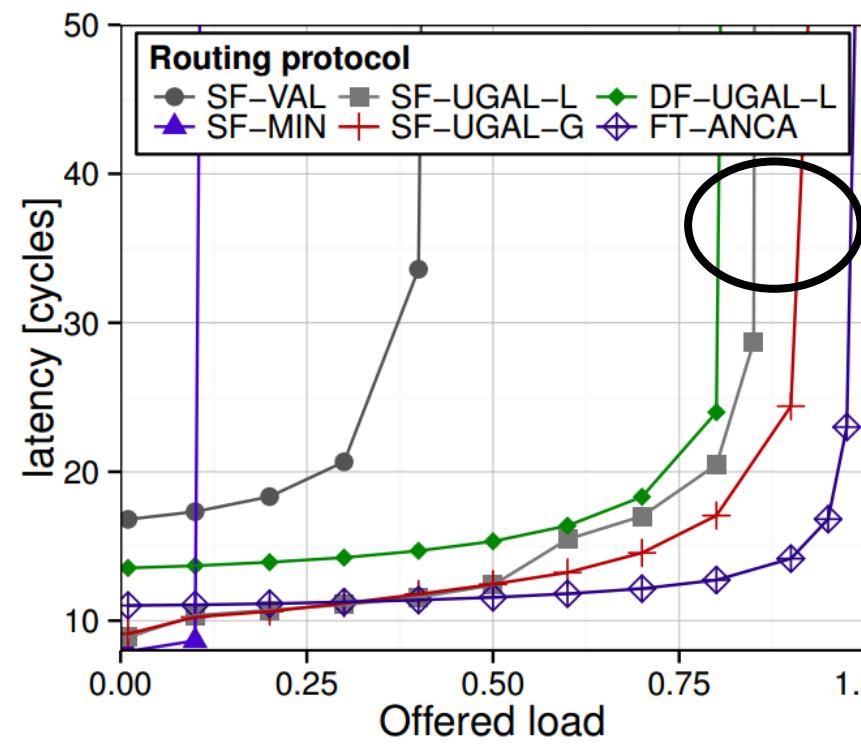


# PERFORMANCE

- ### ■ Shift traffic



$$d = \begin{cases} s \bmod \frac{N}{2} & \text{if } \text{dest id} \\ \left( s \bmod \frac{N}{2} \right) + \frac{N}{2} & \text{if } \text{source id} \end{cases}$$



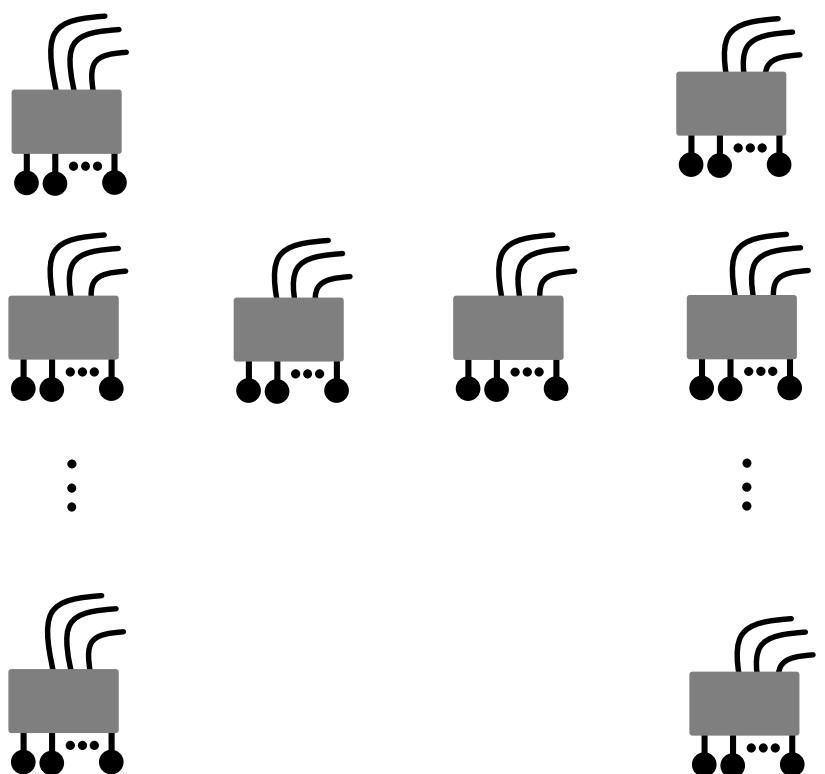
# PERFORMANCE

# PERFORMANCE

- Worst-case traffic

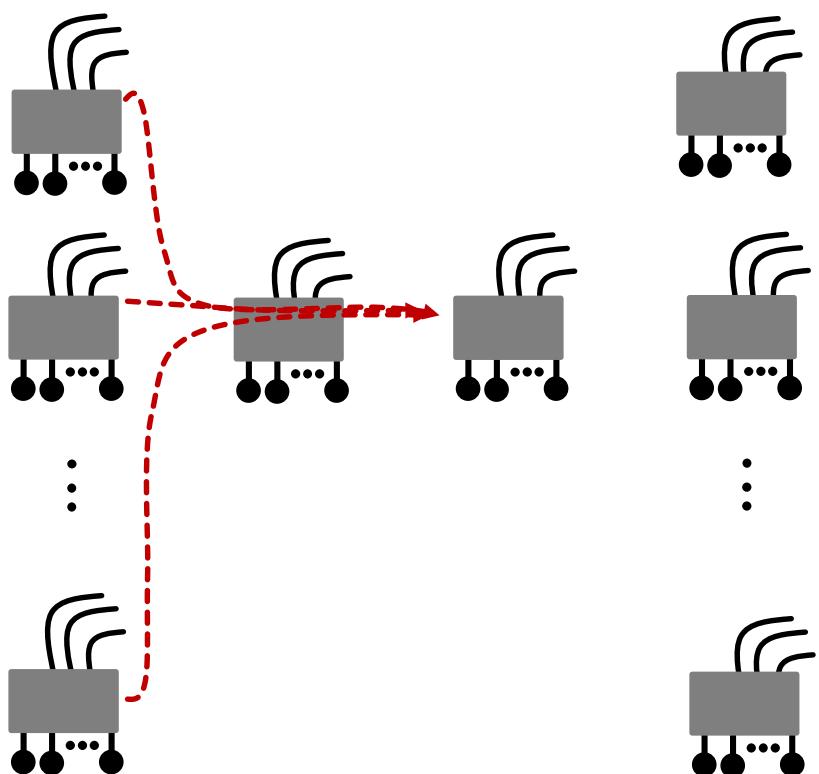
# PERFORMANCE

- Worst-case traffic



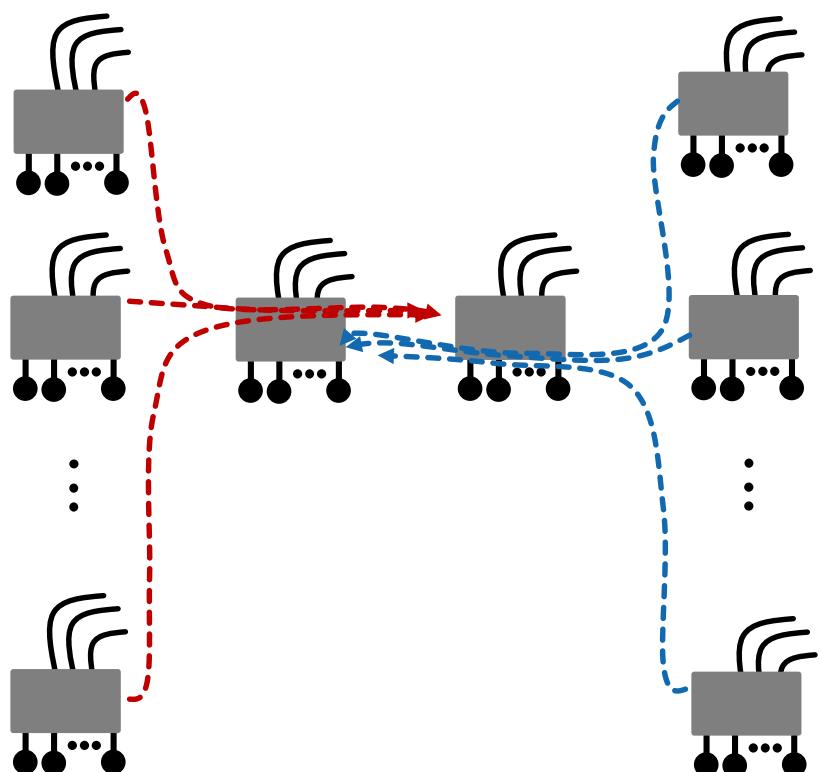
# PERFORMANCE

- Worst-case traffic



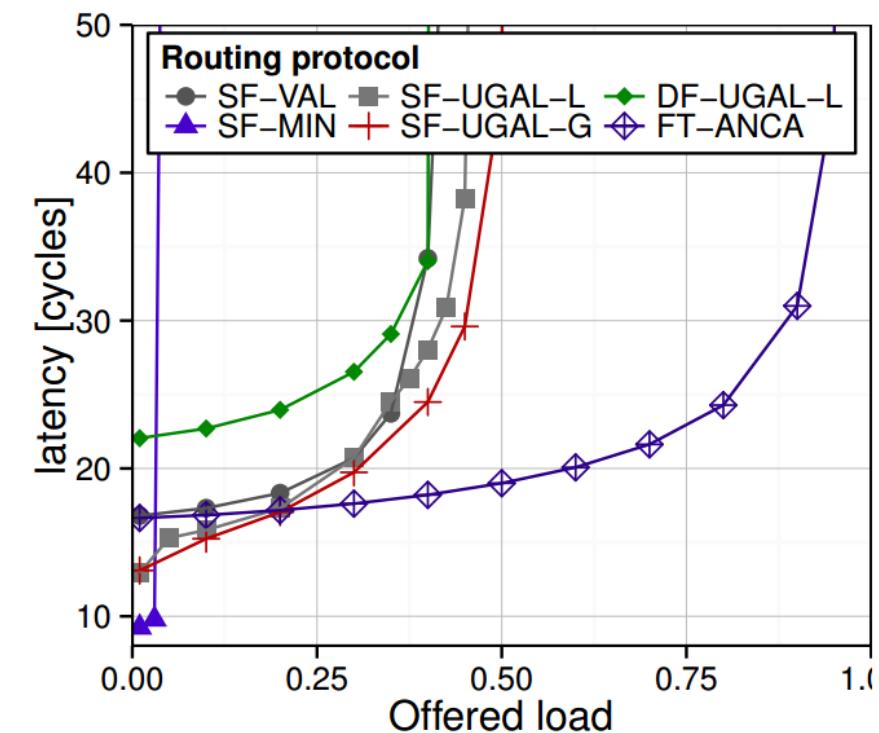
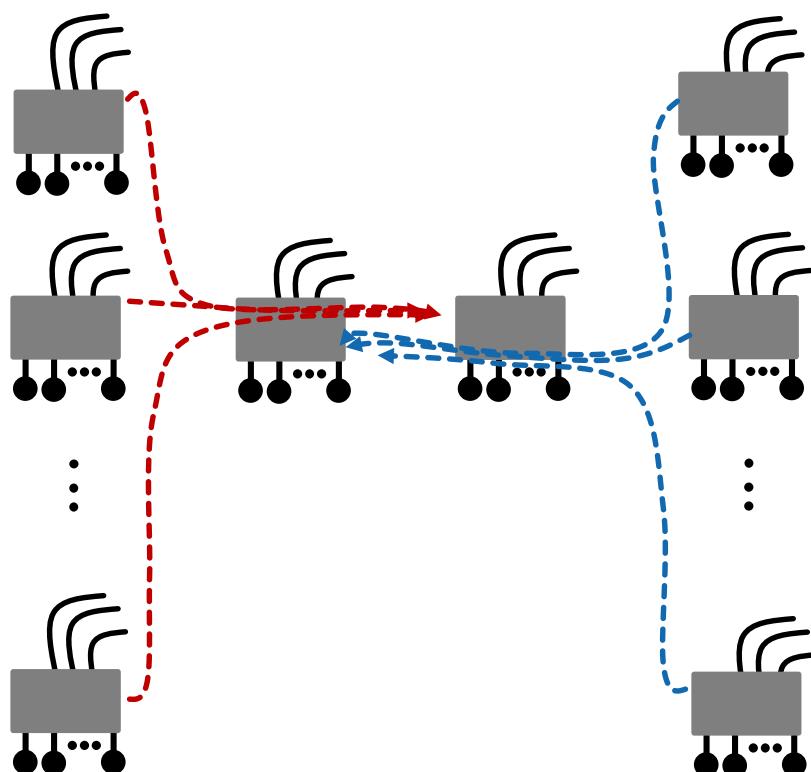
# PERFORMANCE

- Worst-case traffic



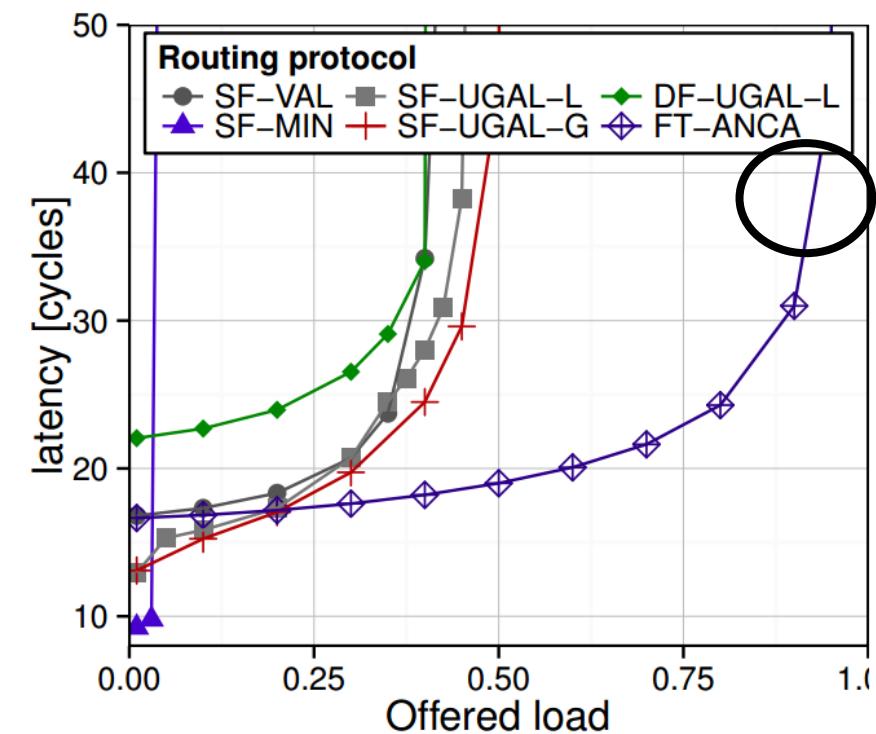
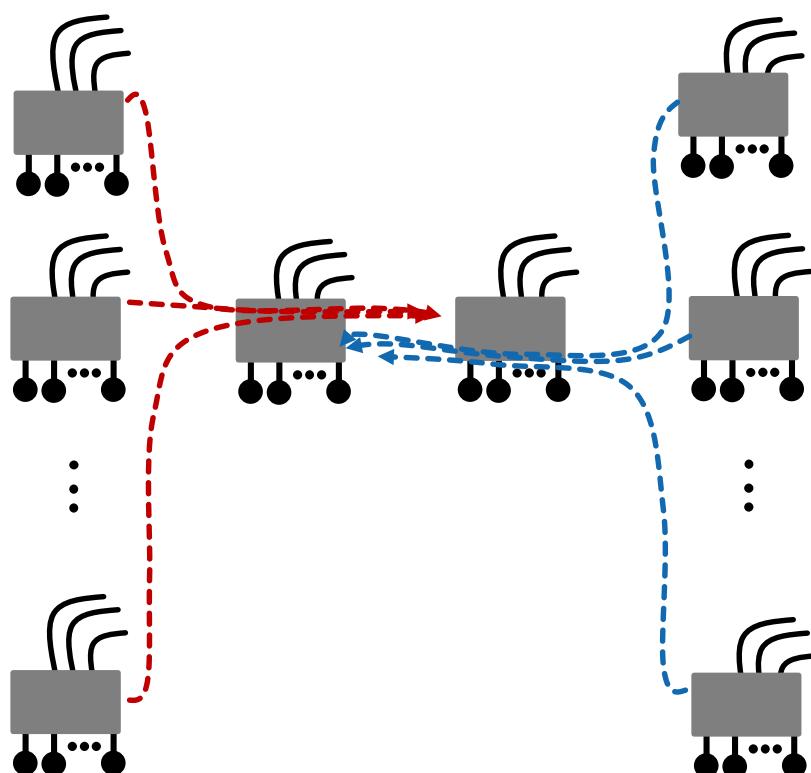
# PERFORMANCE

- Worst-case traffic



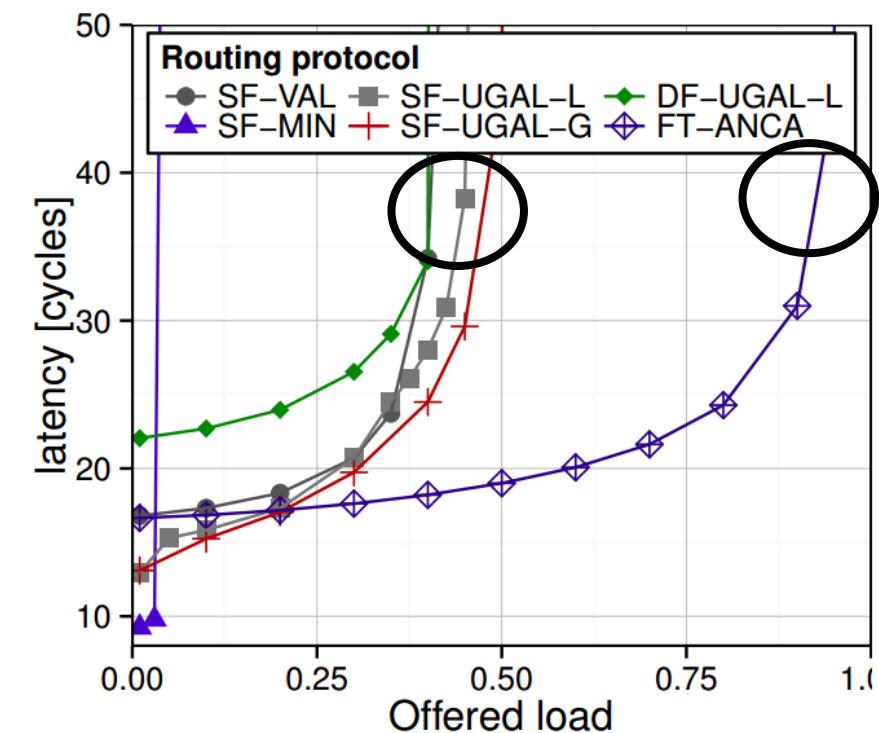
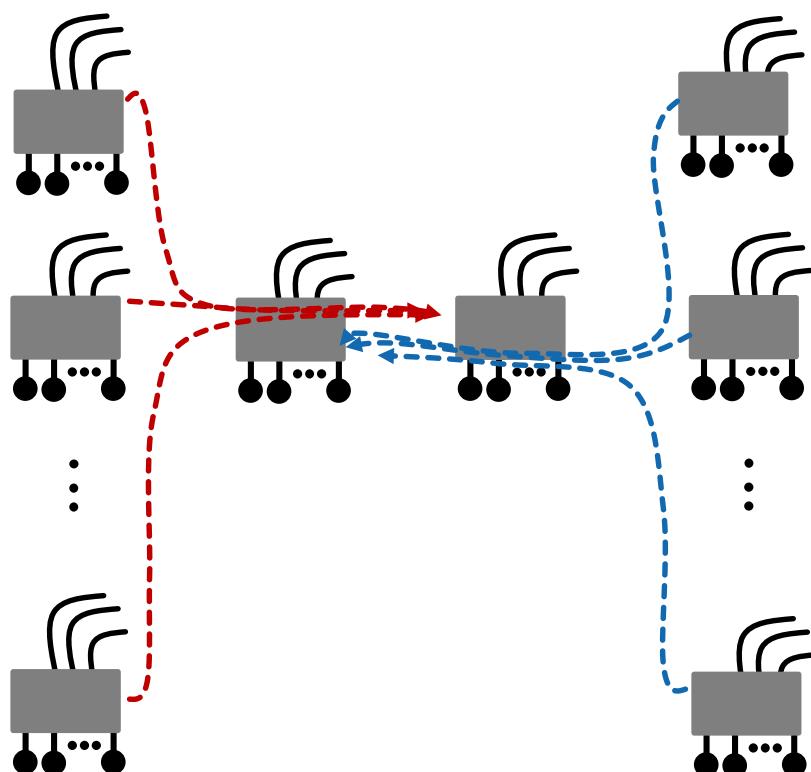
# PERFORMANCE

- Worst-case traffic



# PERFORMANCE

- Worst-case traffic



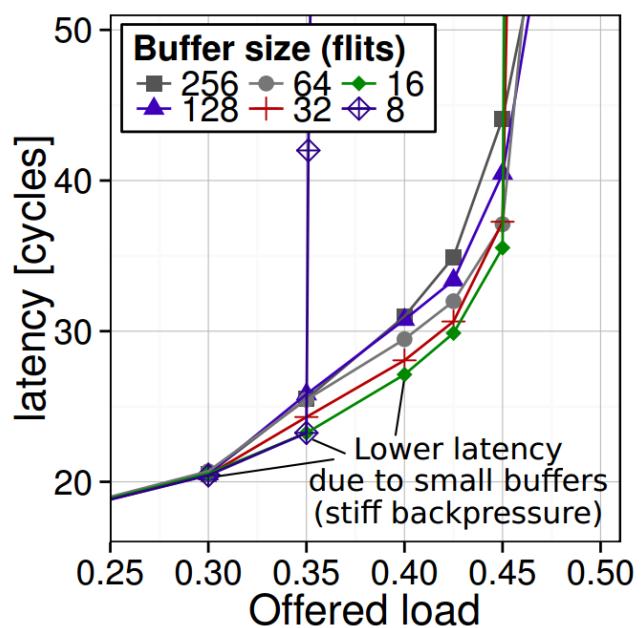
# PERFORMANCE

# PERFORMANCE

- Buffer sizes (UGAL-L, worst-case traffic)

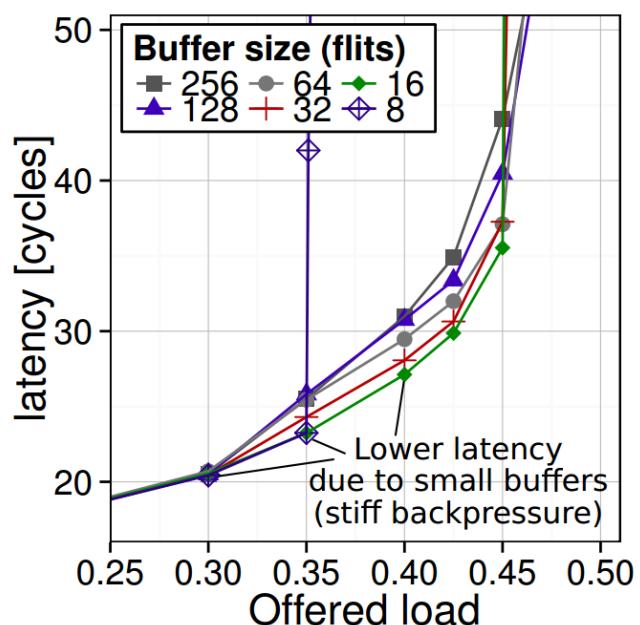
# PERFORMANCE

- Buffer sizes (UGAL-L, worst-case traffic)



# PERFORMANCE

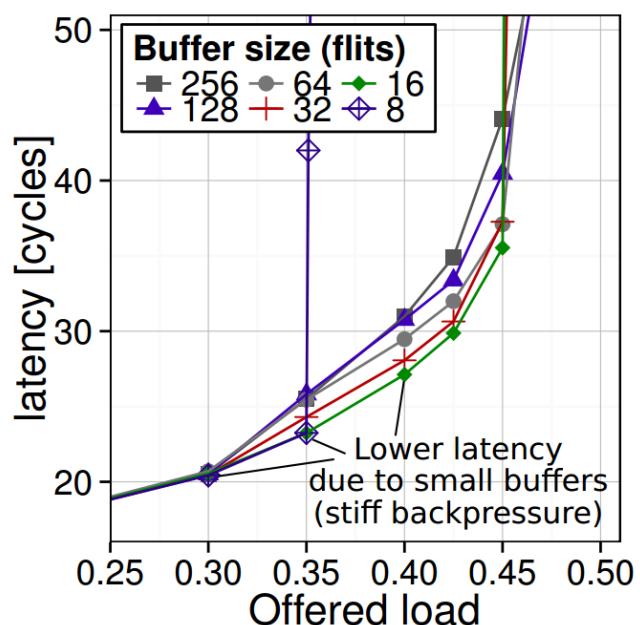
- Buffer sizes (UGAL-L, worst-case traffic)
- Oversubscription (64 flits)



# PERFORMANCE

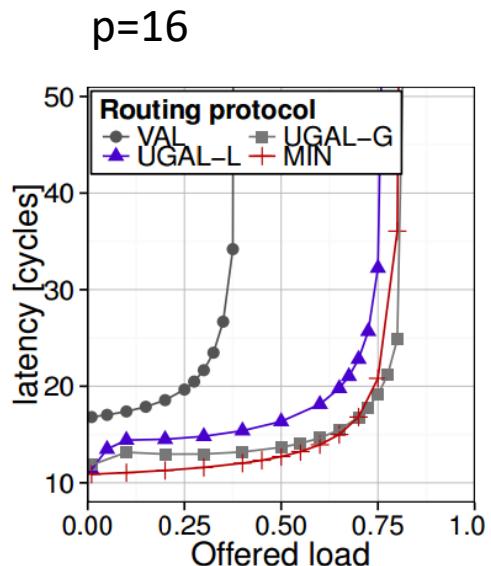
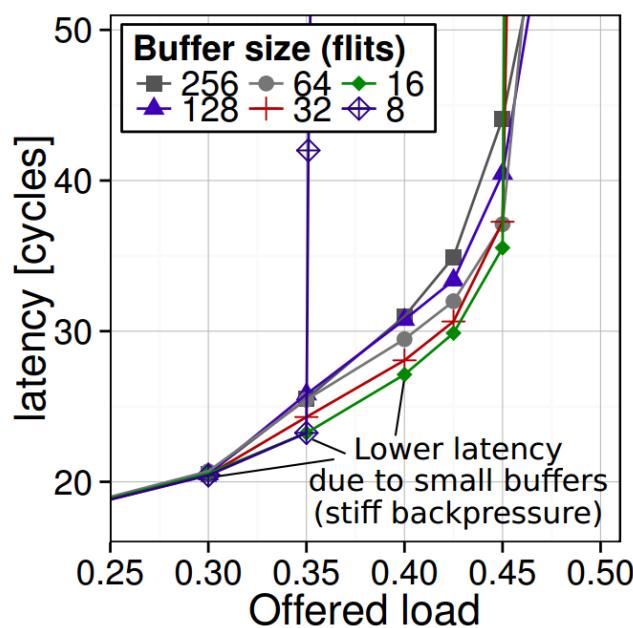
- Buffer sizes (UGAL-L, worst-case traffic)
- Oversubscription (64 flits)

p=16



# PERFORMANCE

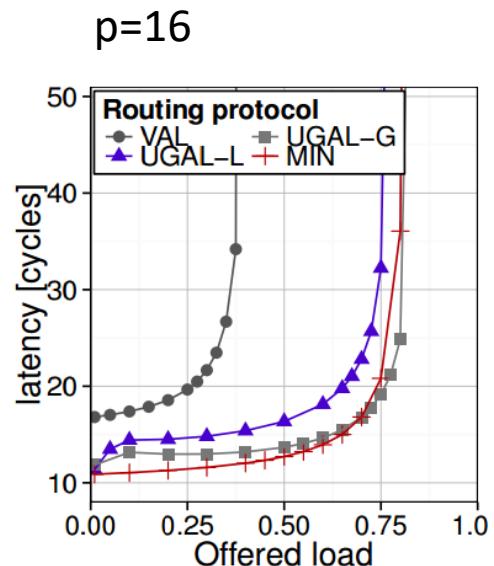
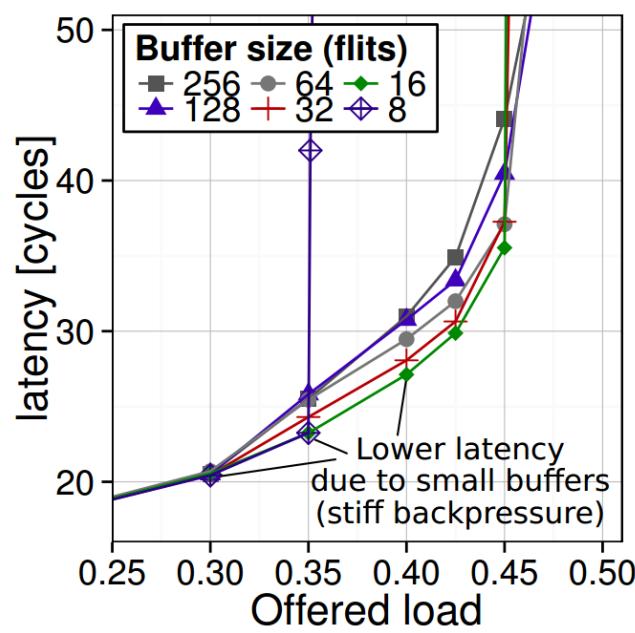
- Buffer sizes (UGAL-L, worst-case traffic)
- Oversubscription (64 flits)



(b) Random traffic,  $p = 16$ .

# PERFORMANCE

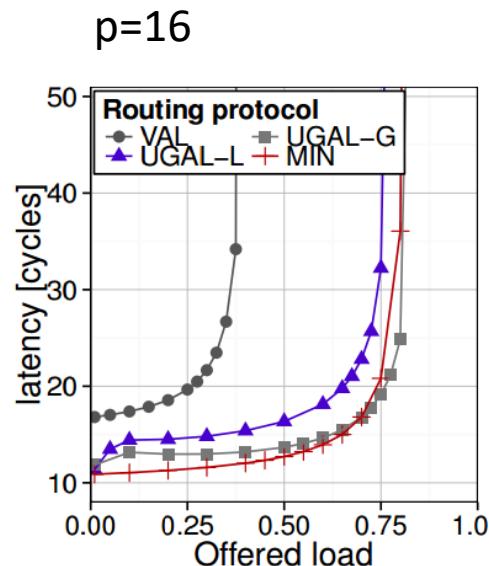
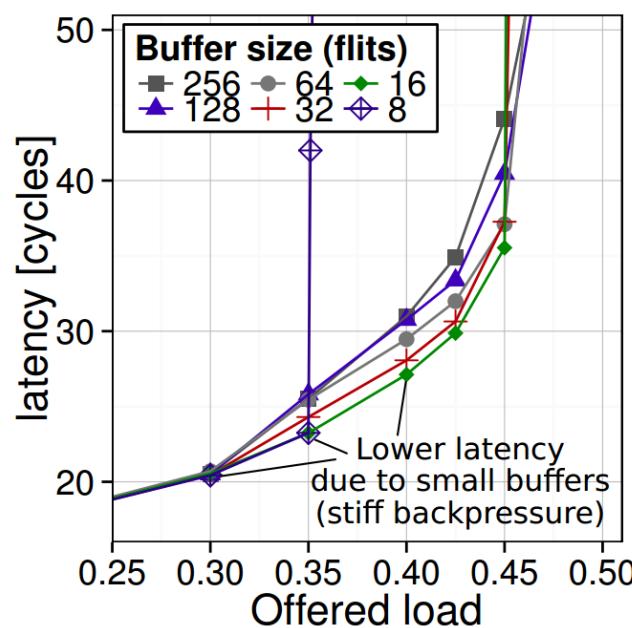
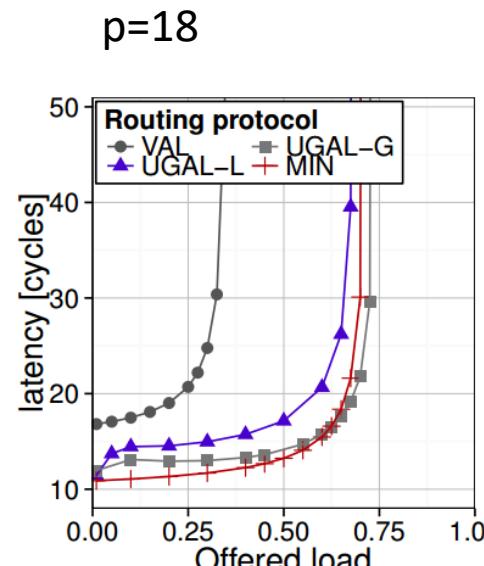
- Buffer sizes (UGAL-L, worst-case traffic)
- Oversubscription (64 flits)



(b) Random traffic,  $p = 16$ .

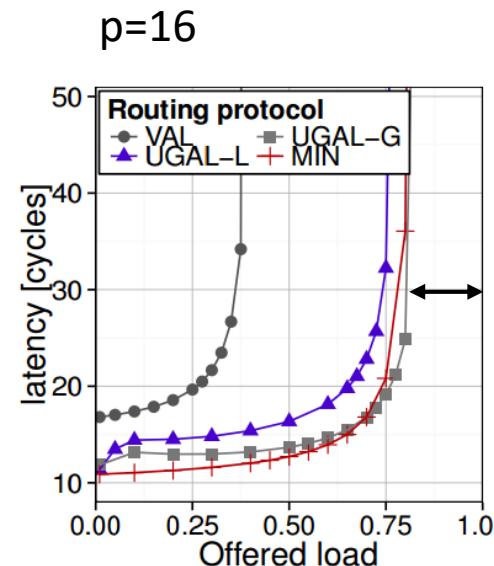
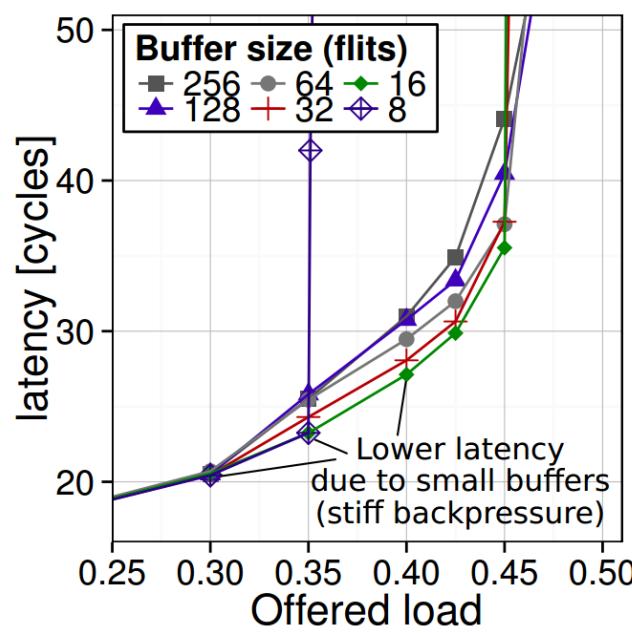
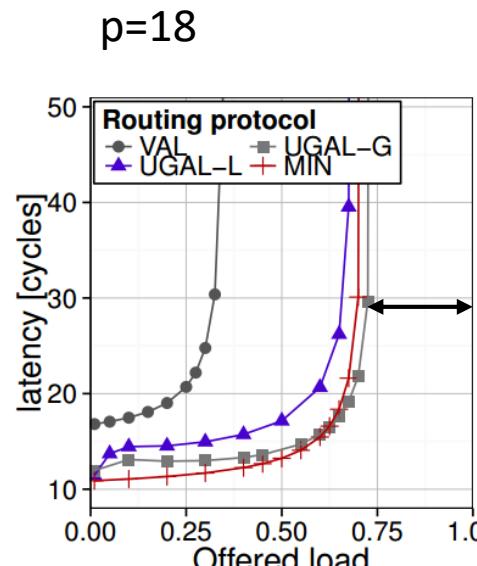
# PERFORMANCE

- Buffer sizes (UGAL-L, worst-case traffic)
- Oversubscription (64 flits)

(b) Random traffic,  $p = 16$ .(d) Random traffic,  $p = 18$ .

# PERFORMANCE

- Buffer sizes (UGAL-L, worst-case traffic)
- Oversubscription (64 flits)

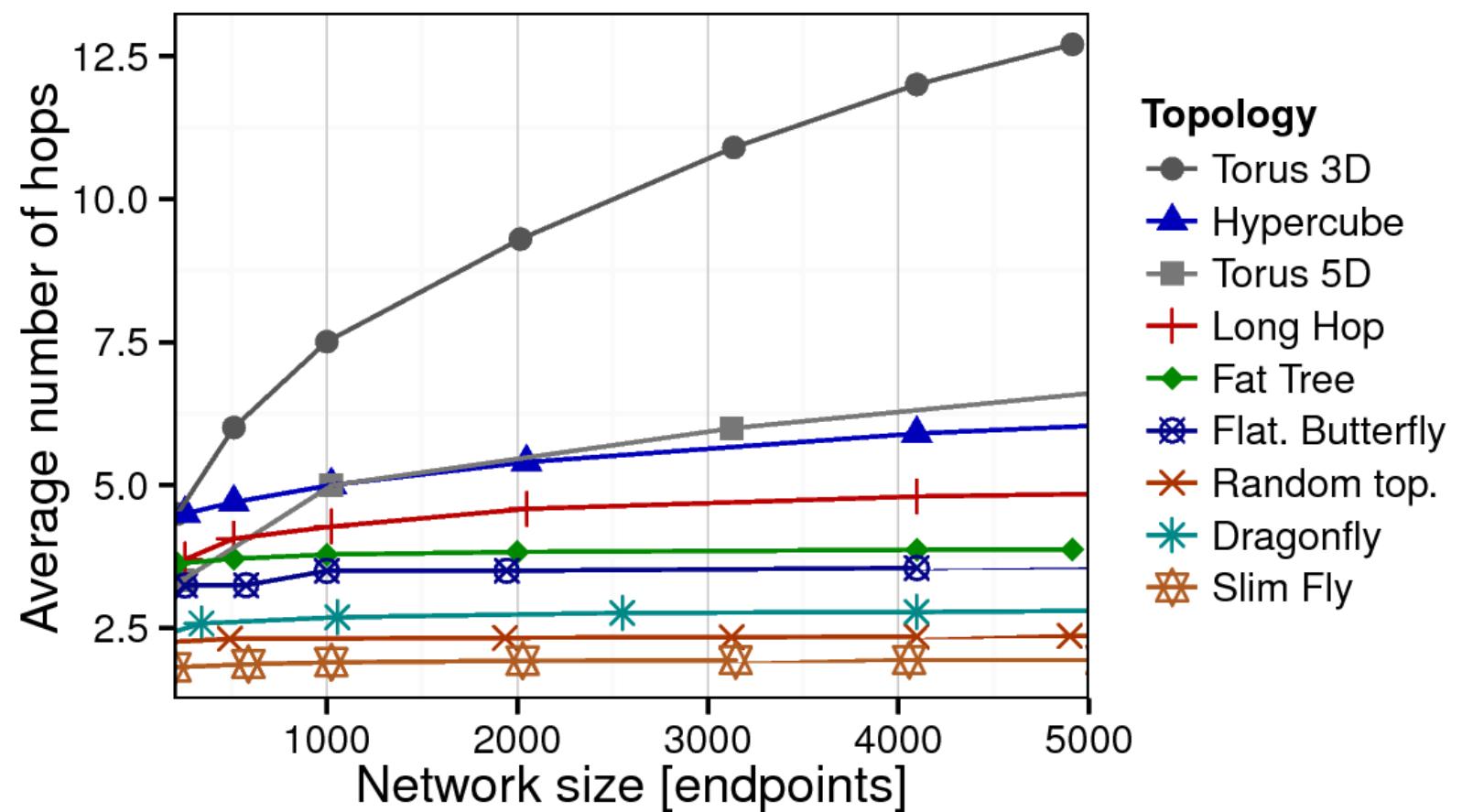
(b) Random traffic,  $p = 16$ .(d) Random traffic,  $p = 18$ .

# STRUCTURE ANALYSIS

## AVERAGE DISTANCE

# STRUCTURE ANALYSIS

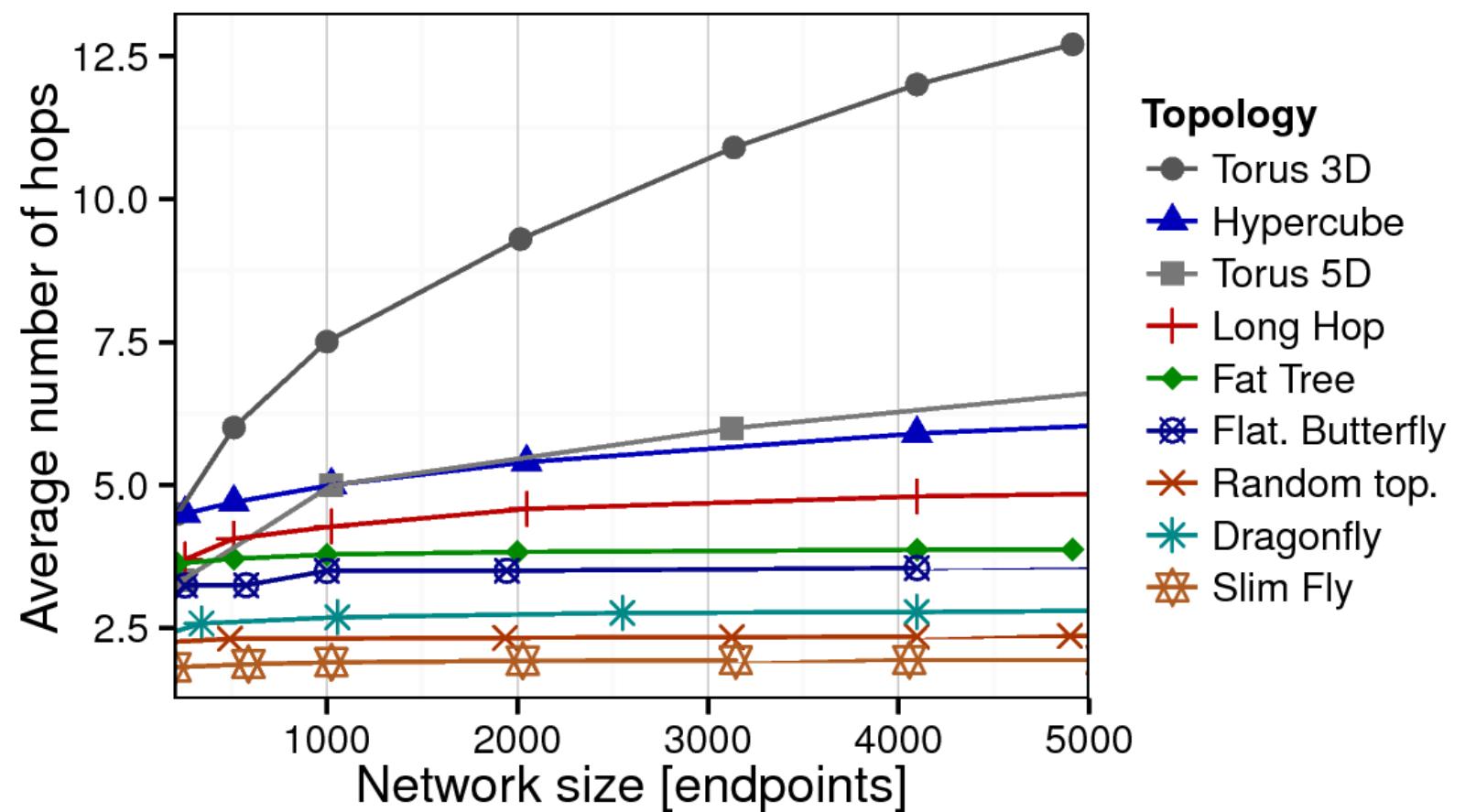
## AVERAGE DISTANCE



# STRUCTURE ANALYSIS

## AVERAGE DISTANCE

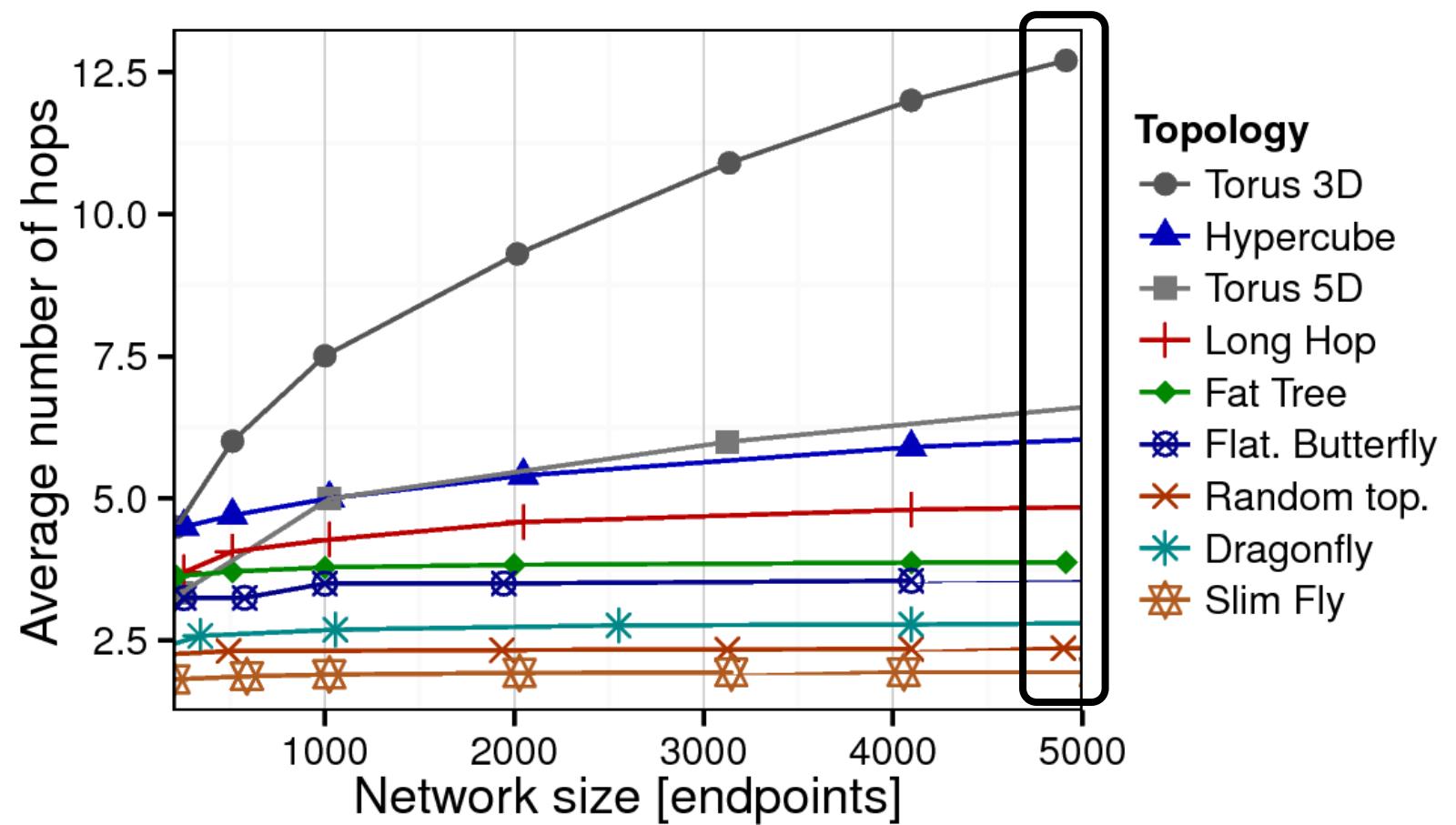
Random uniform traffic  
using minimum path routing



# STRUCTURE ANALYSIS

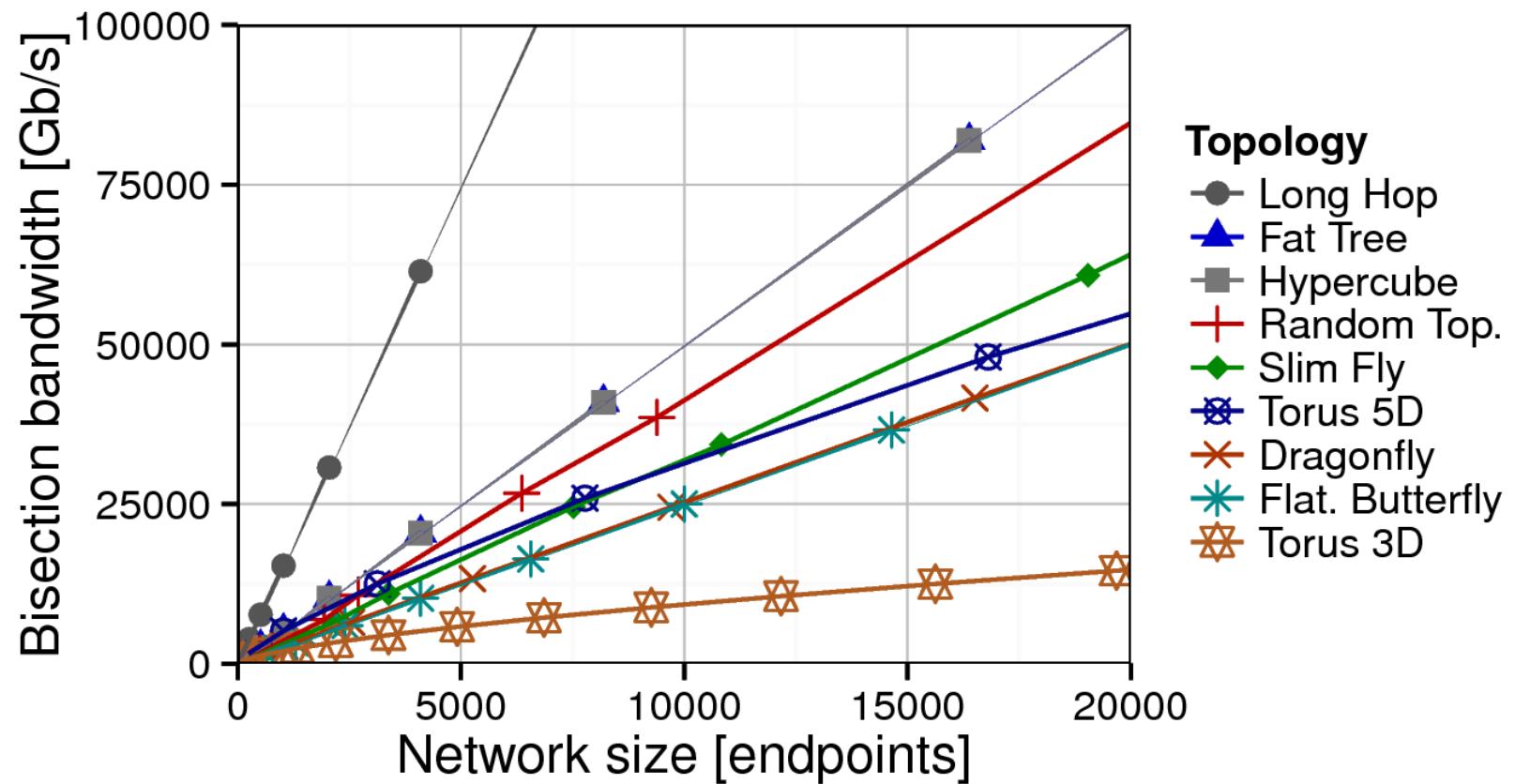
## AVERAGE DISTANCE

Random uniform traffic  
using minimum path routing



# STRUCTURE ANALYSIS

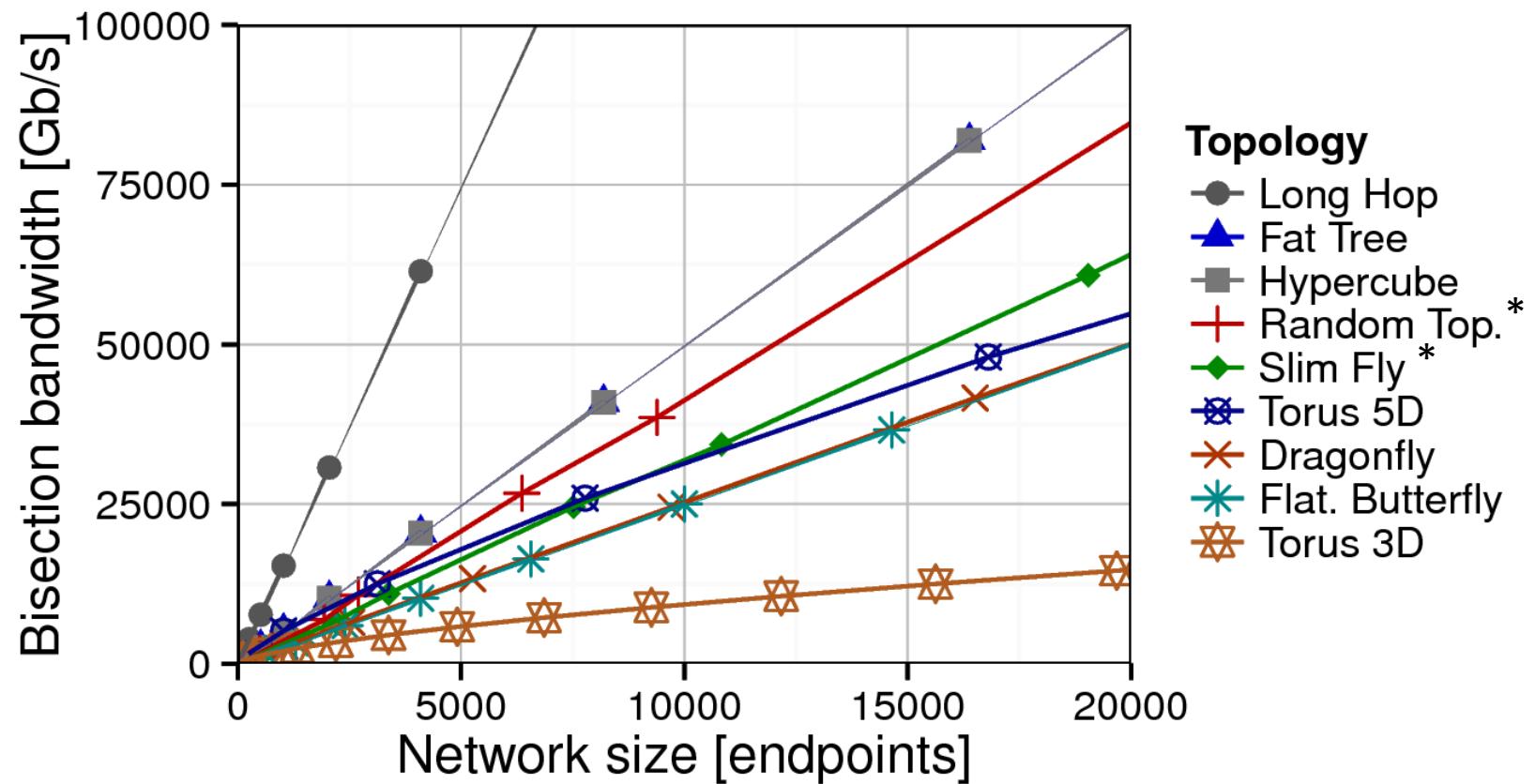
## BISECTION BANDWIDTH (BB)



# STRUCTURE ANALYSIS

## BISECTION BANDWIDTH (BB)

\*BB approximated with  
the Metis partitioner [1]



# STRUCTURE ANALYSIS

## BISECTION BANDWIDTH (BB)

\*BB approximated with  
the Metis partitioner [1]

